



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	I _D max T _A = +25°C
30V	14mΩ @ VGS = 10V	8.0A
	20mΩ @ VGS = 4.5V	6.7A

Mechanical Data

- 14mΩ @ V_{GS} = 10V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Description and Applications

This new generation MOSFET has been designed to minimize the onstate resistance (RDS(on)) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

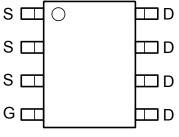
- DC-DC Converters
- Power management functions

Mechanical Data

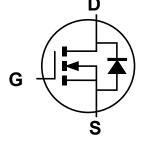
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208
- Weight: 0.072 grams (approximate)



Top View



Top View Internal Schematic



Equivalent circuit

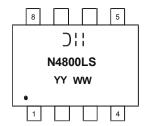
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN4800LSSL-13	SO-8	2500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



);; = Manufacturer's Marking
N4800LS = Product Type Marking Code
YYWW = Date Code Marking
YY or YY = Year (ex: 20 = 2020)
WW = Week (01 - 53)



Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
Drain Current (Note 5) VGS = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	8.0 6.4	А
Drain Current (Note 5) VGS = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	6.7 5.3	А
Pulsed Drain Current (Note 6)			I _{DM}	50	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_{D}	1.46	W
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	86	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

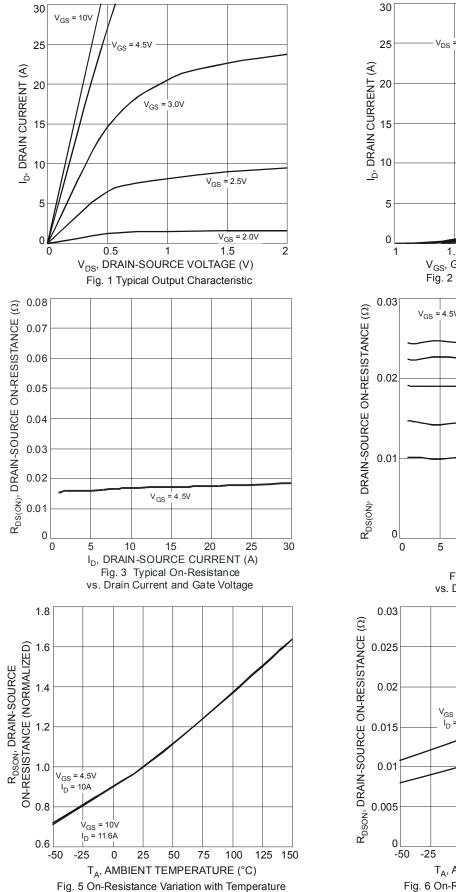
Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

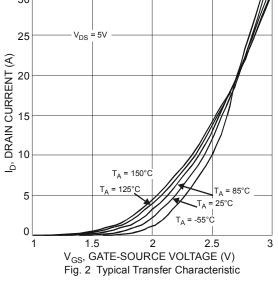
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	30		_	V	V _{GS} = 0V, I _D = 250μA	
Zero Gate Voltage Drain Current	I _{DSS}	_		1	μΑ	V _{DS} = 30V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	V_{GS} = ±20V, V_{DS} = 0V	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	0.8	1.2	1.6	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D-a		11	14	mΩ	V _{GS} = 10V, I _D = 8A	
Static Drain-Source On-Resistance	R _{DS(on)}		14	20	11122	$V_{GS} = 4.5V, I_D = 7A$	
Forward Transconductance	g _{fs}	_	8	_	S	V _{DS} = 10V, I _D = 8A	
Diode Forward Voltage (Note 7)	V_{SD}		0.72	0.94	V	V _{GS} = 0V, I _S = 1A	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{iss}	_	798	_	pF		
Output Capacitance	Coss	_	128	_	pF	V _{DS} = 10V, V _{GS} = 0V f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	122	_	pF	- 1.0IVID2	
Gate Resistance	R _G	_	1.37	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg	_	8.7	_		V _{GS} = 5V, V _{DS} = 15V, I _D = 9A	
Gate-Source Charge	Qgs	_	1.7	_	nC		
Gate-Drain Charge	Q_{gd}	_	2.4	_			
Turn-On Delay Time	t _{d(on)}	_	5.03	_		V_{DD} = 15V, V_{GEN} = 10V, R_{L} = 15 Ω , R_{G} = 6.0 Ω , I_{D} = 1A	
Rise Time	t _r		4.50	_	ns		
Turn-Off Delay Time	t _{d(off)}	_	26.33	_	115		
Fall Time	t _f	1	8.55	_			

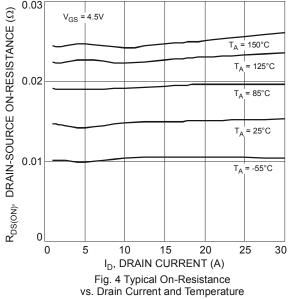
Notes:

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.6. Repetitive rating, pulse width limited by junction temperature.7. Short duration pulse test used to minimize self-heating effect.









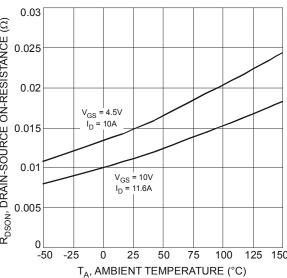


Fig. 6 On-Resistance Variation with Temperature



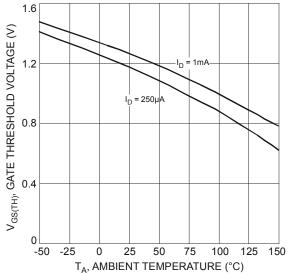
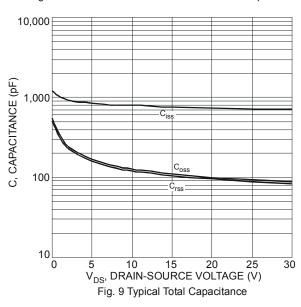


Fig. 7 Gate Threshold Variation vs. Ambient Temperature



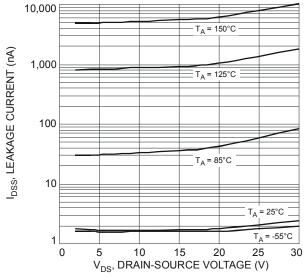
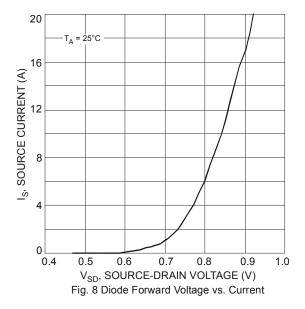
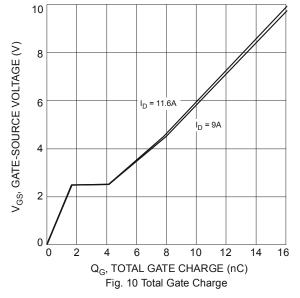


Fig. 11 Typical Leakage Current vs. Drain-Source Voltage







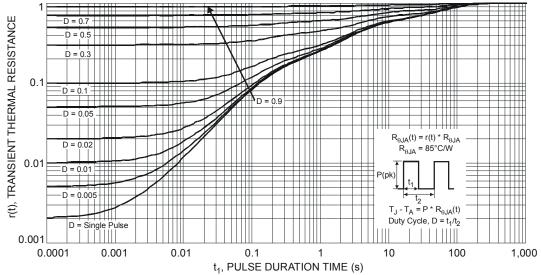
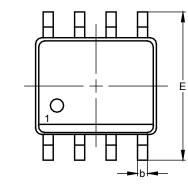


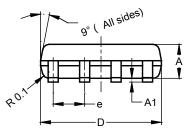
Fig. 12 Transient Thermal Response

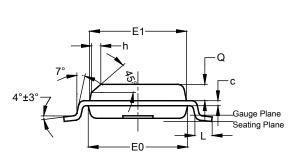


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.







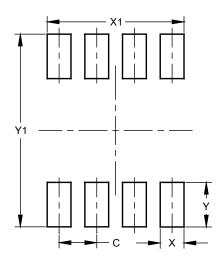
SO-8

SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
A 1	0.10	0.20	0.15		
b	0.30	0.50	0.40		
С	0.15	0.25	0.20		
D	4.85	4.95	4.90		
Е	5.90	6.10	6.00		
E1	3.80	3.90	3.85		
E0	3.85	3.95	3.90		
е	-		1.27		
h	-		0.35		
L	0.62	0.82	0.72		
Q	0.60	0.70	0.65		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Y	1.505
V1	6.50

July 2021



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