



COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

Product Summary

Device	BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C	
		0.99Ω @ $V_{GS} = 4.5V$	450mA	
01	201/	1.2Ω @ $V_{GS} = 2.5V$	400mA	
Qı	Q1 20V	1.8Ω @ $V_{GS} = 1.8V$	330mA	
	2.4Ω @ $V_{GS} = 1.5V$	300mA		
		1.9Ω @ V _{GS} = -4.5V	-310mA	
Q2	-20V	$2.4\Omega @ V_{GS} = -2.5V$		-280mA
Q2		3.4Ω @ $V_{GS} = -1.8V$	-240mA	
		5Ω @ $V_{GS} = -1.5V$	-180mA	

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch





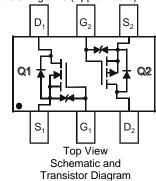
Top View

Features and Benefits

- Low On-Resistance
- Very Low Gate Threshold Voltage, 1.0V Max
- Low Input Capacitance
- Fast Switching Speed
- Ultra-Small Surface Mount Package 1mm x 1mm
- Low Package Profile, 0.45mm Maximum Package Height
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Note 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: SOT963
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.027 grams (Approximate)



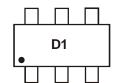
Ordering Information (Note 5)

Part Number	Case	Packaging
DMC2990UDJQ-7	SOT963	10K/Tape & Reel
DMC2990UDJQ-7B	SOT963	10K/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



D1 = Product Type Marking Code



Maximum Ratings Q1 N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit			
Drain-Source Voltage	V _{DSS}	20	V			
Gate-Source Voltage			V_{GSS}	±8	V	
State T		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	450 350	mA	
Continuous Drain Current (Note 6) V _{GS} = 4.5V	t<5s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	520 410	mA	
State		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	330 260	mA	
Continuous Drain Current (Note 6) V _{GS} = 1.8V	t<5s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	390 310	mA	
Maximum Continuous Body Diode Forward Current (Note 6)			Is	440	mA	
Pulsed Drain Current (Note 7)			I _{DM}	800	mA	

Maximum Ratings Q2 P-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	-20	V		
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Drain Current (Note 6) V _{GS} = -4.5V		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-310 -240	mA
		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-360 -280	mA
Continuous Drain Current (Note 6) // 4.9/	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-240 -190	mA
Continuous Drain Current (Note 6) V _{GS} = -1.8V	t<5s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-280 -220	mA
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	-440	mA
Pulsed Drain Current (Note 7)			I _{DM}	-800	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)	P_{D}	350	mW	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	0	360	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<5s	$R_{\theta JA}$	270	°C/W
Operating and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to +150	°C

Notes: 6. Device mounted on FR-4 PCB, with minimum recommended pad layout.

^{7.} Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.



Electrical Characteristics Q1 N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	-	-	٧	$V_{GS} = 0V, I_D = 250\mu A$
Zoro Coto Voltogo Drain Current @T- 125%		-	-	100	nΛ	$V_{DS} = 16V, V_{GS} = 0V$
Zero Gate Voltage Drain Current @T _C = +25°C	IDSS	-	-	50	nA	$V_{DS} = 5V$, $V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 5V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.4	-	1.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
		-	0.60	0.99		$V_{GS} = 4.5V, I_D = 100mA$
		-	0.75	1.2		$V_{GS} = 2.5V, I_D = 50mA$
Static Drain-Source On-Resistance	R _{DS(ON)}	-	0.90	1.8	Ω	$V_{GS} = 1.8V, I_D = 20mA$
		-	1.2	2.4		$V_{GS} = 1.5V, I_D = 10mA$
		-	2.0	-		$V_{GS} = 1.2V, I_D = 1mA$
Forward Transfer Admittance	Y _{fs}	180	850	-	ms	$V_{DS} = 5V, I_{D} = 125mA$
Diode Forward Voltage	V _{SD}	-	0.6	1.0	V	V _{GS} = 0V, I _S = 10mA
DYNAMIC CHARACTERISTICS (Note 9)		•		•		•
Input Capacitance	C _{iss}	-	27.6	-	pF	45)()(0)(
Output Capacitance	Coss	-	4.0	-	pF	$V_{DS} = 15V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	Crss	-	2.8	-	pF	- I = 1.0WH2
Gate Resistance	R_{g}	-	113	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge	Qq	-	0.5	-	nC	V _{GS} = 4.5V, V _{DS} = 10V.
Gate-Source Charge	Q _{gs}	-	0.07	-	nC	I _D = 250mA
Gate-Drain Charge	Q _{gd}	-	0.07	-	nC	
Turn-On Delay Time	t _{D(ON)}	-	4.0	_	ns	
Turn-On Rise Time	t _R	-	3.3	-	ns	$V_{DD} = 15V, V_{GS} = 4.5V,$
Turn-Off Delay Time	t _{D(OFF)}	-	19.0	-	ns	$R_L = 47\Omega$, $R_g = 2\Omega$,
Turn-Off Fall Time	t _F	-	6.4	-	ns	$I_D = 200 \text{mA}$

Electrical Characteristics Q2 P-CHANNEL (@T_A = +25°C, unless otherwise specified.)

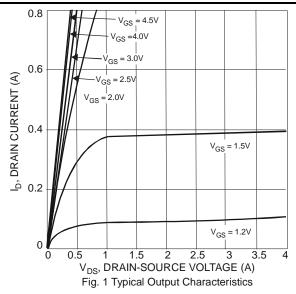
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	-	-	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current @T _C = +25°0		-	-	100	nA	$V_{DS} = -16V, V_{GS} = 0V$
Zero Gate Voltage Drain Current @ 1c = +25 t	C I _{DSS}	-	-	50	IIA	$V_{DS} = -5V$, $V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 5V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	-0.4	-	-1.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
		-	1.2	1.9		$V_{GS} = -4.5V$, $I_D = -100mA$
		-	1.5	2.4		$V_{GS} = -2.5V$, $I_{D} = -50mA$
Static Drain-Source On-Resistance	R _{DS(ON)}	-	2.1	3.4	Ω	$V_{GS} = -1.8V, I_D = -20mA$
		-	2.5	5		$V_{GS} = -1.5V, I_D = -10mA$
		-	4.0	-		$V_{GS} = -1.2V, I_D = -1mA$
Forward Transfer Admittance	Y _{fs}	100	450	-	ms	$V_{DS} = -5V, I_{D} = -125mA$
Diode Forward Voltage	V_{SD}	-	-0.6	-1.0	V	$V_{GS} = 0V, I_{S} = -10mA$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	-	28.7	-	pF	151/1/ 01/
Output Capacitance	Coss	-	4.2	-	pF	$V_{DS} = -15V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	-	2.9	-	pF	1 = 1.01/1112
Gate Resistance	Rg	-	399	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge	Q_{g}	-	0.4	-	nC	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Gate-Source Charge	Q_{gs}	-	0.08	-	nC	$V_{GS} = -4.5V, V_{DS} = -10V,$
Gate-Drain Charge	Q_{gd}	-	0.06	-	nC	$I_D = -250 \text{mA}$
Turn-On Delay Time	t _{D(ON)}	-	5.8	-	ns	
Turn-On Rise Time	t _R	-	5.7	-	ns	V _{DD} = -15V, V _{GS} = -4.5V,
Turn-Off Delay Time	t _{D(OFF)}	-	31.1	-	ns	$R_g = 2\Omega, I_D = -200 \text{mA}$
Turn-Off Fall Time	t _F	-	16.4	-	ns] -

Notes:

- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.



Typical Characteristics - N-CHANNEL



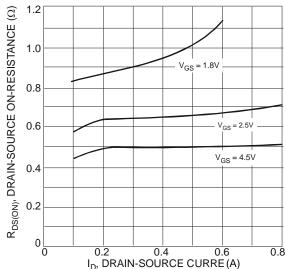


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

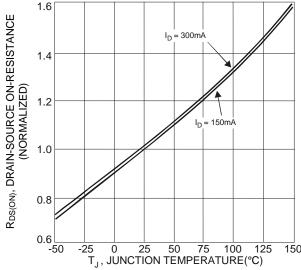
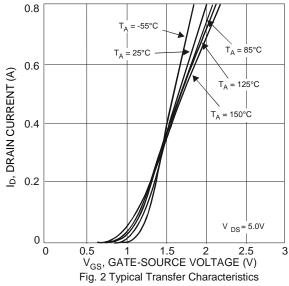


Fig. 5 On-Resistance Variation with Temperature



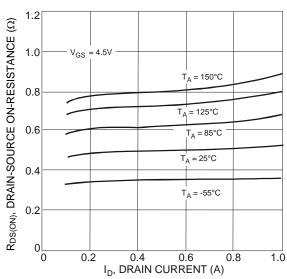


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

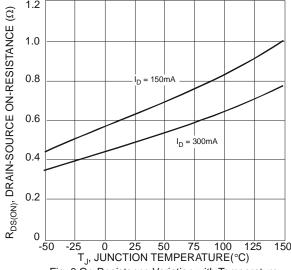


Fig. 6 On-Resistance Variation with Temperature



Typical Characteristics - N-CHANNEL (Cont.)

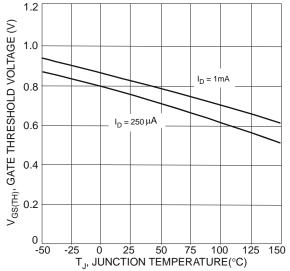
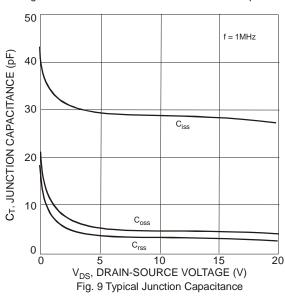
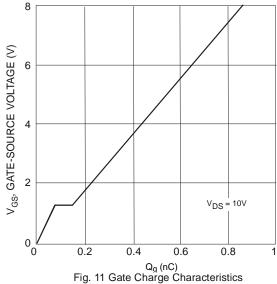
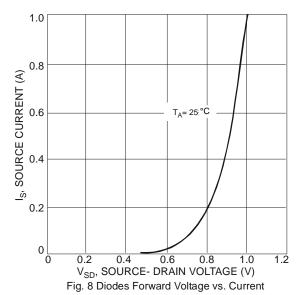


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







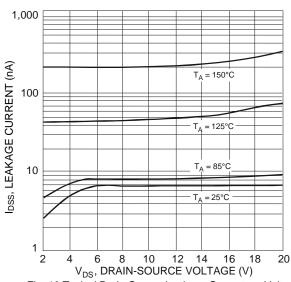
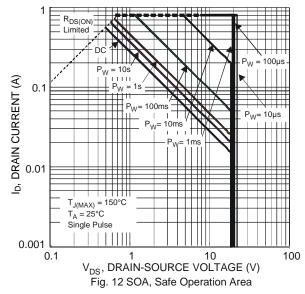
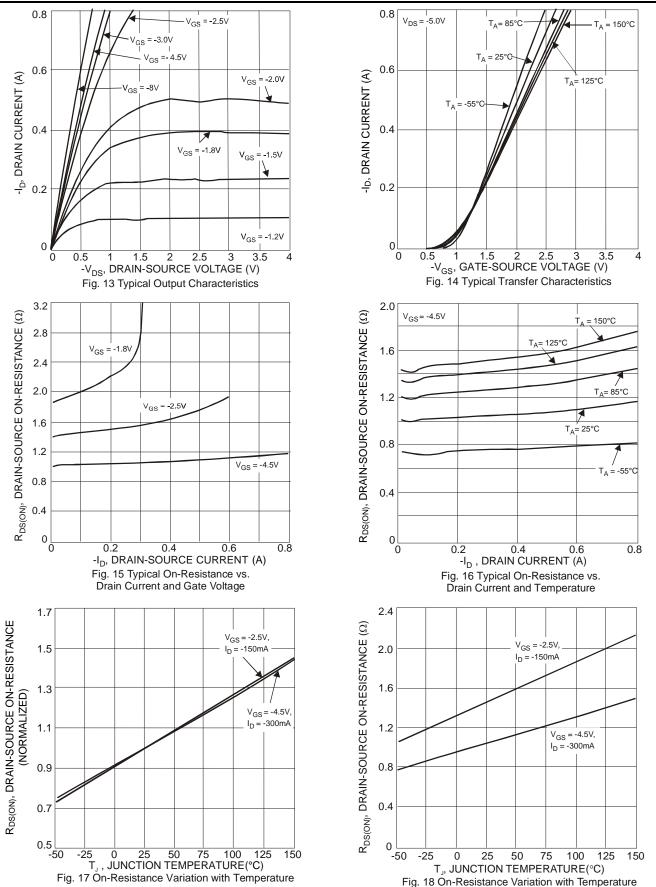


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage





Typical Characteristics - P-CHANNEL





Typical Characteristics - P-CHANNEL (Cont.)

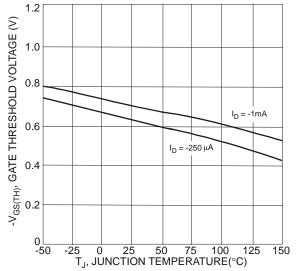
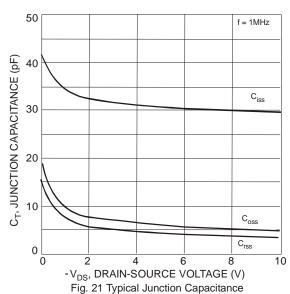
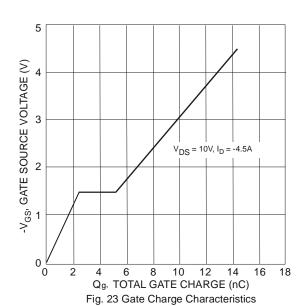


Fig. 19 Gate Threshold Variation vs. Ambient Temperature





0.8

(v)
0.6

0.7

0.7

0.8

T_A= 25 °C

0.4

0.6

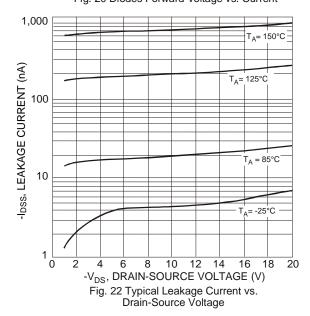
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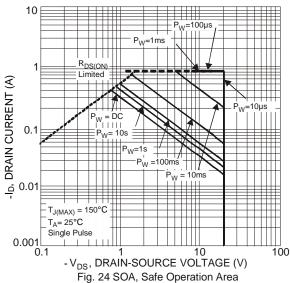
1.0

1.2

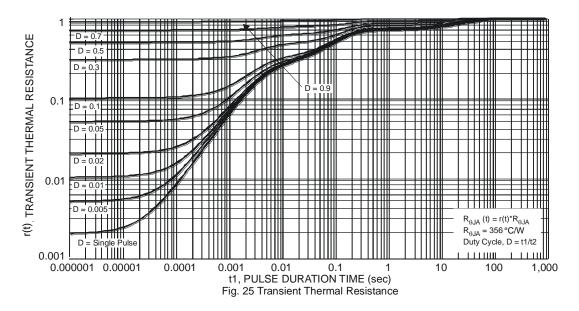
- V_{SD}, SOURCE- DRAIN VOLTAGE (V)

Fig. 20 Diodes Forward Voltage vs. Current





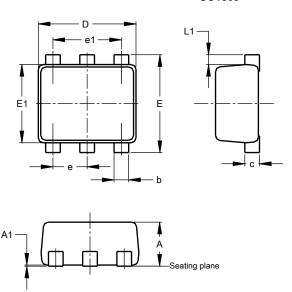




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT963



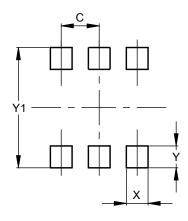
SOT963							
Dim	Min	Max	Тур				
Α	0.40	0.50	0.45				
A1	0.00	0.05					
b	0.10	0.20	0.15				
С	0.120	0.180	0.150				
D	0.95	1.05	1.00				
Е	0.95	1.05	1.00				
E1	0.75	0.85	0.80				
е			0.35				
e1			0.70				
L1	0.05	0.15	0.10				
All Dimensions in mm							



Suggested Pad Layout

 $Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

SOT963



Dimensions	Value (in mm)
С	0.350
Х	0.200
Y	0.200
Y1	1.100

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