onsemi

Programmable Shunt Regulator

LM431A, LM431B, LM431C

Description

The LM431A/LM431B/LM431C are three-terminal output adjustable regulators with thermal stability over the full operating temperature range. The output voltage can be set to any value between V_{REF} (approximately 2.5 V) and 36 V with two external resistors. These devices have a typical dynamic output impedance of 0.2 Ω . Active output circuit provides a sharp turn-on characteristic, making these devices excellent replacements for Zener diodes in many applications.

Features

- Programmable Output Voltage to 36 V
- Low Dynamic Output Impedance: 0.2 Ω (Typical)
- Sink Current Capability: 1.0 to 100 mA
- Equivalent Full-Range Temperature Coefficient of 50 ppm/°C (Typical)
- Temperature Compensated for Operation Over Full Rated Operating Temperature Range
- Low Output Noise Voltage
- Fast Turn-on Response

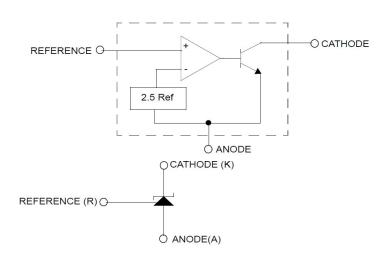
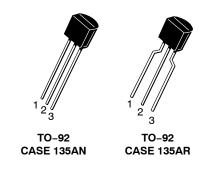
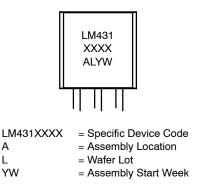


Figure 1. Block Diagram

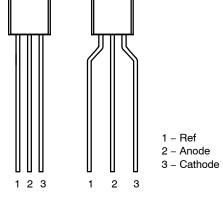








PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

Semiconductor Components Industries, LLC, 2003 April, 2021 – Rev. 3

LM431A, LM431B, LM431C

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit	
V _{KA}	Cathode Voltage	37	V	
I _{KA}	Cathode Current Range (Continuous)	-100 to +150	mA	
I _{REF}	Reference Input Current Range	–0.05 to +10	mA	
PD	Power Dissipation	770	mW	
$R_{\theta jA}$	Thermal Resistance, Junction to Ambient	160	°C/W	
T _{OPR}	Operating Temperature Range – LM431xC	-25 to +85	°C	
	Operating Temperature Range – LM431xI	-40 to +85	°C	
Τ _J	Junction Temperature	150	°C	
T _{STG}	Storage Temperature Range	–65 to +150	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit	
V _{KA}	Cathode Voltage	V _{REF}	36	V	
I _{KA}	Cathode Current	1.0	100	mA	

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

		LM431A		1	LM431B			LM431C					
Symbol	Parameter	Condi	tions	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit
V _{REF}	Reference Input Voltage	V _{KA} = V _{REF} , I _{KA} = 10 mA		2.450	2.500	2.550	2.470	2.495	2.520	2.482	2.495	2.508	V
$\Delta V_{\text{REF}} / \Delta T$	Deviation of Reference Input Voltage Over– Temperature	V _{KA} = V _{REF} , I _{KA} =10 mA T _{MIN} ≤ T _A ≤ T _{MAX} (Note 1)		-	4.5	17.0	-	4.5	17.0	-	4.5	17.0	mV
ΔV _{REF} / ΔV _{KA} Reference Input Voltage to the Change in Cathode Voltage	I _{KA} = 10 mA	ΔV _{KA} = 10 V–V _{REF}	-	-1.0	-2.7	-	-1.0	-2.7	-	-1.0	-2.7	mV/V	
	Change in Cathode		∆V _{KA} = 36 V−10 V	-	-0.5	-2.0	_	-0.5	-2.0	_	-0.5	-2.0	
I _{REF}	Reference Input Current	$I_{KA} = 10 \text{ mA}, \text{ R1} = 10 \text{ k}\Omega,$ R2 = ∞		_	1.5	4.0	_	1.5	4.0	_	1.5	4.0	μΑ
ΔI _{REF} /ΔT	Deviation of Reference Input Current Over Full Temperature Range	I _{KA} = 10 mA, R1 = 10 kΩ, R2 = ∞, T _A = Full Range		-	0.4	1.2	-	0.4	1.2	_	0.4	1.2	μA
IKA(MIN)	Minimum Cathode Current for Regulation	V _{KA} = V _{REF}		-	0.45	1.00	-	0.45	1.00	-	0.45	1.00	mA
I _{KA(OFF)}	Off – Stage Cathode Current	V _{KA} = 36 V, V _{REF} = 0		-	0.05	1.00	_	0.05	1.00	_	0.05	1.00	μΑ
Z _{KA}	Dynamic Impedance	$V_{KA} = V_{REF}$, $I_{KA} = 1$ to 100 mA, f \geq 1.0 kHz		-	0.15	0.50	_	0.15	0.50	_	0.15	0.50	Ω

ELECTRICAL CHARACTERISTICS (Values are at T_A = 25°C unless otherwise noted)

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
LM431xC: T_{MIN} = -25°C, T_{MAX} = +85°C LM431xI: T_{MIN} = -40°C, T_{MAX} = +85°C

TEST CIRCUIT

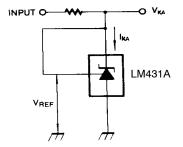


Figure 2. Test Circuit for $V_{\text{KA}}\text{=}~V_{\text{REF}}$

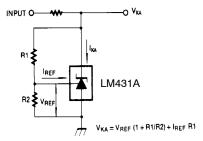


Figure 3. Test Circuit for $V_{KA} \ge V_{REF}$

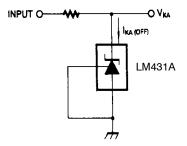


Figure 4. Test Circuit for IKA(OFF)

LM431A, LM431B, LM431C

TYPICAL PERFORMANCE CHARACTERISTICS

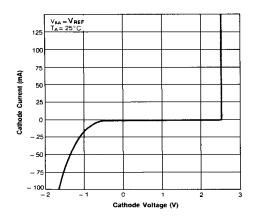


Figure 5. Cathode Current vs. Cathode Voltage

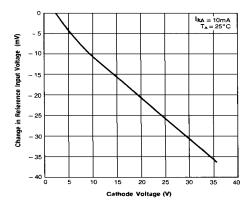


Figure 7. Change in Reference Input Voltage vs. Cathode Voltage

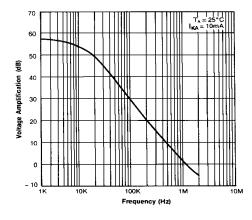


Figure 9. Small Signal Voltage Amplification vs. Frequency

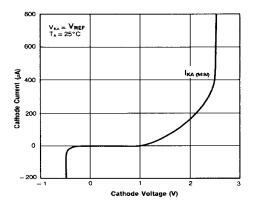


Figure 6. Cathode Current vs. Cathode Voltage

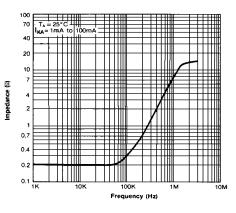


Figure 8. Dynamic Impedance Frequency

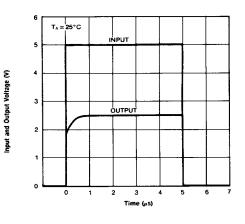


Figure 10. Pulse Response

LM431A, LM431B, LM431C

TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

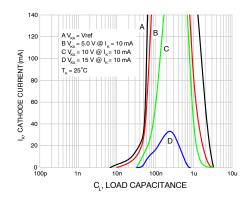


Figure 11. Stability Boundary Conditions

TYPICAL APPLICATION

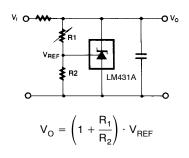


Figure 12. Shunt Regulator

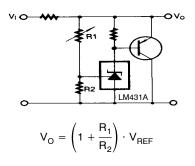


Figure 14. High–Current Shunt Regulator

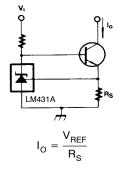


Figure 16. Constant-Current Sink

ORDERING INFORMATION

Part Number	Operating Temperature Range	Output Voltage Tolerance	Tom Mark	Package	Packing Method [†]
LM431CCZ	−25 ~ +85°C	0.5%	LM431CCZ	TO-92	Bulk
LM431BCZX		1%	LM431BCZ	TO-92	Tape and Reel
LM431BCZXA			LM431BCZ	TO-92	Ammo
LM431ACZ		2%	LM431ACZ	TO-92	Bulk
LM431ACZX			LM431ACZ	TO-92	Tape and Reel
LM431BIZX	−40 ~ +85°C	1%	LM431BIZ	TO-92	Tape and Reel
LM431AIZ		2%	LM431AIZ	TO-92	Bulk

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

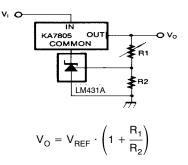


Figure 13. Output Control for Three–Terminal Fixed Regulator

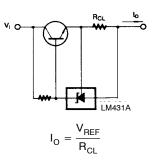


Figure 15. Current Limit or Current Source

onsemi

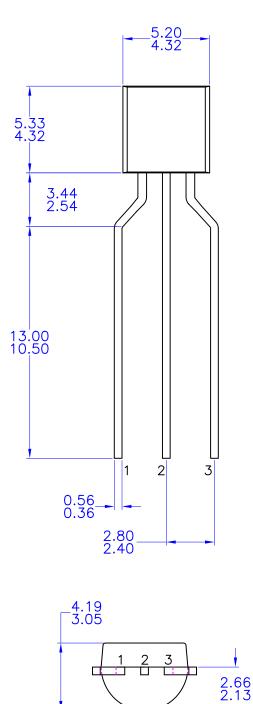
TO-92 3 4.825x4.76 CASE 135AN ISSUE O DATE 31 JUL 2016 _5.20_ ______ 5.33 (0.81) 15.62 2 3 1 0.52 0.56 0.36 1.27 NOTES: UNLESS OTHERWISE SPECIFIED 2.54 A) DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS. B) ALL DIMENSIONS ARE IN MILLIMETERS. с́э DRAWING CONFORMS TO ASME Y14.5M-2009. 4.19 3.05 2.66 2.13 2 3 1 Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. **DOCUMENT NUMBER:** 98AON13880G **DESCRIPTION:** TO-92 3 4.825X4.76 PAGE 1 OF 1 onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

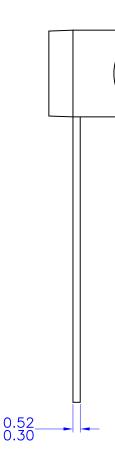
© Semiconductor Components Industries, LLC, 2016



TO-92 3 4.83x4.76 LEADFORMED CASE 135AR ISSUE O

DATE 30 SEP 2016





NOTES: UNLESS OTHERWISE SPECIFIED

A) DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS.

- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DRAWING CONFORMS TO ASME Y14.5M-1994

DOCUMENT NUMBER:	98AON13879G	the Document Repository. O COPY" in red.	
DESCRIPTION:	TO-92 3 4.83X4.76 LEADF	PAGE 1 OF 1	

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

© Semiconductor Components Industries, LLC, 2016

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent_Marking.pdf</u>. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or indental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification. Buyer shall indemnify and hold onsemi and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs,

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation onsemi Website: www.onsemi.com

ONLINE SUPPORT: <u>www.onsemi.com/support</u> For additional information, please contact your local Sales Representative at <u>www.onsemi.com/support/sales</u>