# RGW50NL65DHRBTL

## 650V 25A Field Stop Trench IGBT

Datasheet

V <sub>CES</sub>	650V
I <sub>C</sub>	25A
V <sub>CE(sat) (Typ.)</sub>	1.5V
$P_{D}$	165W

# Outline LPDL (TO-263L) (1) (3)

#### Features

- 1) AEC-Q101 Qualified
- 2) Low Collector Emitter Saturation Voltage
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating; RoHS Compliant

#### Application

Automotive

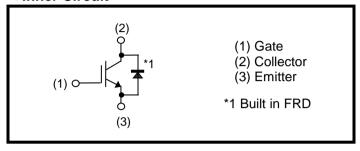
On & Off Board Chargers

**DC-DC Converters** 

**PFC** 

Industrial Inverter

#### ●Inner Circuit



Packaging Specifications

T ackaging opecinications					
Туре	Packaging	Taping			
	Reel Size (mm)	330			
	Tape Width (mm)	24			
	Basic Ordering Unit (pcs)	1,000			
	Packing Code	TL			
	Marking	RGW50NL65D			

## ● Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		$V_{CES}$	650	V
Gate - Emitter Voltage		$V_{GES}$	±30	V
Collector Current	T <sub>C</sub> = 25°C	I <sub>C</sub>	57	А
Collector Current	T <sub>C</sub> = 100°C	I <sub>C</sub>	35	А
Pulsed Collector Current	Pulsed Collector Current		100	А
Diode Forward Current	$T_C = 25^{\circ}C$	I <sub>F</sub>	24	А
	T <sub>C</sub> = 100°C	I <sub>F</sub>	14	А
Diode Pulsed Forward Current	Diode Pulsed Forward Current		100	А
Dawer Dissination	T <sub>C</sub> = 25°C	P <sub>D</sub>	165	W
Power Dissipation	T <sub>C</sub> = 100°C	P <sub>D</sub>	82	W
Operating Junction Temperature		T <sub>j</sub>	-40 to +175	°C
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C

<sup>\*1</sup> Pulse width limited by T<sub>imax.</sub>

## ●Thermal Resistance

Parameter	Symbol	Values			Unit
Farameter		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.91	°C/W
Thermal Resistance Diode Junction - Case	R <sub>θ(j-c)</sub>	-	-	2.61	°C/W

# ●IGBT Electrical Characteristics (at T<sub>i</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
r arameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	ı	ı	V
Collector Cut - off Current	I <sub>CES</sub>	$V_{CE} = 650V, V_{GE} = 0V$	ı	ı	10	μΑ
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V, V_{CE} = 0V$	ı	ı	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 16.4 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_{C} = 25A, V_{GE} = 15V,$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	1 1	1.5 1.85	1.9 -	V

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Cumbal	nbol Conditions -	Values			Unit
Parameter	Symbol		Min.	Тур.	Max.	Offic
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30V,	-	2080	-	
Output Capacitance	C <sub>oes</sub>	$V_{GE} = 0V$ ,	-	56	-	pF
Reverse transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	38	-	
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 400V,	-	73	-	
Gate - Emitter Charge	$Q_{ge}$	I <sub>C</sub> = 25A,	-	15	-	nC
Gate - Collector Charge	$Q_{gc}$	$V_{GE} = 15V$	-	28	-	
Turn - on Delay Time	t <sub>d(on)</sub>		-	31	-	
Rise Time	t <sub>r</sub>	$I_C = 12.5A, V_{CC} = 400V,$ $V_{GF} = 15V, R_G = 10\Omega,$	-	7	-	ns
Turn - off Delay Time	t <sub>d(off)</sub>	$V_{GE} = 15V, K_G - 1002,$ $T_i = 25^{\circ}C$	-	119	-	
Fall Time	t <sub>f</sub>	Inductive Load	-	42	-	
Turn - on Switching Loss	E <sub>on</sub>	*E <sub>on</sub> include diode reverse recovery	-	0.11	-	
Turn - off Switching Loss	E <sub>off</sub>	Toverse recovery	-	0.23	-	mJ
Turn - on Delay Time	t <sub>d(on)</sub>		-	30	-	
Rise Time	t <sub>r</sub>	$I_C = 12.5A, V_{CC} = 400V, V_{GE} = 15V, R_G = 10\Omega,$	-	7	-	ns
Turn - off Delay Time	t <sub>d(off)</sub>	$T_i = 175^{\circ}C$	-	130	-	
Fall Time	t <sub>f</sub>	Inductive Load *E <sub>on</sub> include diode reverse recovery	-	64	-	
Turn - on Switching Loss	E <sub>on</sub>		-	0.12	-	
Turn - off Switching Loss	E <sub>off</sub>		-	0.28	-	mJ
Reverse Bias Safe Operating Area	RBSOA	$I_C = 100A$ , $V_{CC} = 520V$ , $V_P = 650V$ , $V_{GE} = 15V$ , $R_G = 100\Omega$ , $T_j = 175^{\circ}C$	FU	LL SQUA	RE	-

# **•FRD Electrical Characteristics** (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Current ed	Symbol Conditions	Values			l limit
	Symbol		Min.	Тур.	Max.	Unit
		I <sub>F</sub> = 12A,				
Diode Forward Voltage	$V_{F}$	$T_j = 25^{\circ}C$	-	1.5	1.95	V
		T <sub>j</sub> = 175°C	-	1.6	-	
Diode Reverse Recovery Time	t <sub>rr</sub>		-	71	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>	$I_F = 12.5A,$ $V_{CC} = 400V,$ $di_F/dt = 200A/\mu s,$ $T_j = 25^{\circ}C$	-	5.3	1	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.21	-	μC
Diode Reverse Recovery Energy	E <sub>rr</sub>		-	8.3	1	μJ
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 12.5A,$ $V_{CC} = 400V,$ $di_F/dt = 200A/\mu s,$ $T_j = 175^{\circ}C$	-	87	ı	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	5.8	-	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.27	-	μC
Diode Reverse Recovery Energy	E <sub>rr</sub>		-	11.8	-	μJ

#### **•**Electrical Characteristic Curves

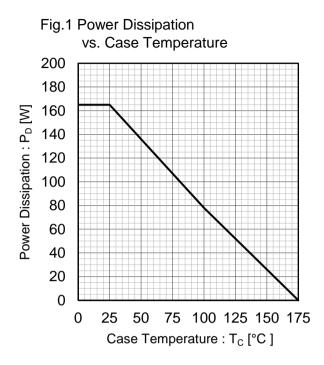
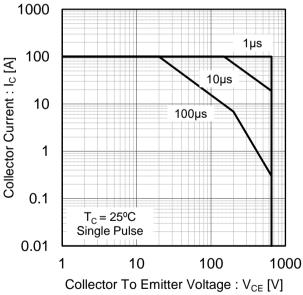


Fig.2 Collector Current vs. Case Temperature 80 70 Collector Current: Ic [A] 60 50 40 30 20 T<sub>j</sub> ≤ 175°C V<sub>GE</sub> ≥ 15V 10 0 25 50 75 100 125 150 175 Case Temperature : T<sub>C</sub> [°C]

Fig.3 Forward Bias Safe Operating Area



120
100
|Vertical State | 120 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100

Fig.4 Reverse Bias Safe Operating Area

Collector To Emitter Voltage: V<sub>CE</sub> [V]

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#### ● Electrical Characteristic Curves

Fig.5 Typical Output Characteristics

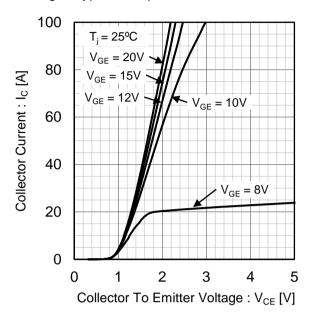


Fig.6 Typical Output Characteristics

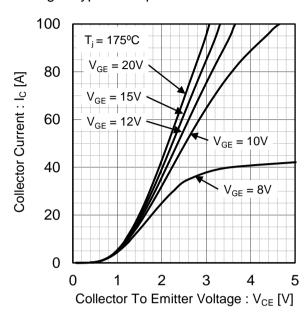


Fig.7 Typical Transfer Characteristics

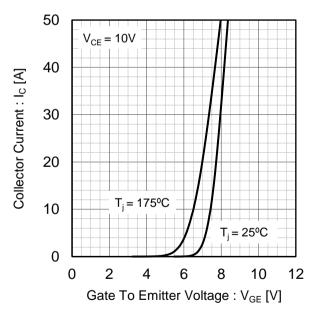
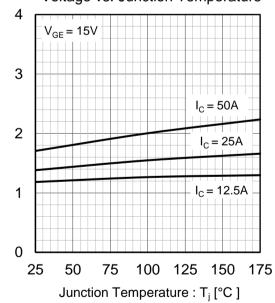


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



Collector To Emitter Saturation

Voltage: V<sub>CE(sat)</sub> [V]

0

5

#### Electrical Characteristic Curves

10

15

Gate To Emitter Voltage: VGE [V]

Fig.9 Typical Collector to Emitter Saturation

Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

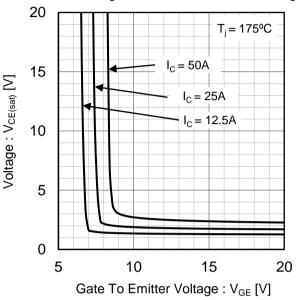
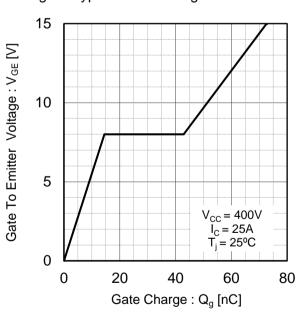


Fig.11 Typical Capacitance vs. Collector to Emitter Voltage 10000  $\boldsymbol{C}_{\text{ies}}$ 1000 Capacitance [pF] 100 Coes 10  $\mathsf{C}_{\mathsf{res}}$ f = 1MHz $V_{GE} = 0V$ = 25°C 0.01 0.1 1 10 100 Collector To Emitter Voltage: V<sub>CE</sub> [V]

Fig.12 Typical Gate Charge



Collector To Emitter Saturation

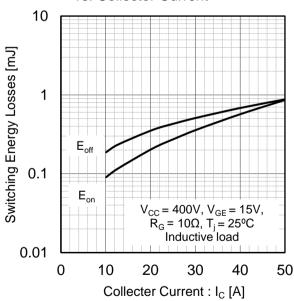
20

#### Electrical Characteristic Curves

Fig.13 Typical Switching Time vs. Collector Current 1000 Switching Time [ns]  $t_{d(off)}$ 100  $t_{\rm f}$  $t_{d(on)}$ 10  $V_{CC}$  = 400V,  $V_{GE}$  = 15V,  $R_G$  = 10 $\Omega$ ,  $T_j$  = 25°C Inductive load 1 0 10 20 30 40 50 Collecter Current : I<sub>C</sub> [A]

Fig.14 Typical Switching Time vs. Gate Resistance 1000  $t_{d(off)}$ Switching Time [ns] 100  $t_{d(on)}$ 10  $V_{CC} = 400V, V_{GE} = 15V,$   $I_{C} = 12.5A, T_{j} = 25^{\circ}C$ Inductive load 1 0 10 20 30 50 Gate Resistance :  $R_G[\Omega]$ 

Fig.15 Typical Switching Energy Losses vs. Collector Current 10



vs. Gate Resistance 10 Switching Energy Losses [mJ] 1 E<sub>off</sub> 0.1  $\mathsf{E}_{\mathsf{on}}$  $V_{CC} = 400V, V_{GE} = 15V, I_{C} = 12.5A, T_{j} = 25^{\circ}C$ Inductive load 0.01 0 10 20 30 50 Gate Resistance :  $R_G[\Omega]$ 

Fig.16 Typical Switching Energy Losses

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#### ● Electrical Characteristic Curves

Fig.17 Typical Switching Time vs. Collector Current 1000  $t_{d(off)}$ Switching Time [ns] 100  $t_{\text{d(on)}} \\$ 10  $V_{CC} = 400V, V_{GE} = 15V,$   $R_G = 10\Omega, T_j = 175^{\circ}C$ Inductive load 1 0 10 20 30 50 Collecter Current : I<sub>C</sub> [A]

Fig.18 Typical Switching Time vs. Gate Resistance 1000  $t_{d(off)}$ Switching Time [ns] 100  $t_{d(on)}$ 10  $V_{CC} = 400V, V_{GE} = 15V,$   $I_{C} = 12.5A, T_{j} = 175^{\circ}C$ Inductive load 1 0 10 20 30 50 Gate Resistance :  $R_G[\Omega]$ 

Fig.19 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1  $\mathsf{E}_{\mathsf{off}}$ 0.1  $\mathsf{E}_{\mathsf{on}}$  $V_{CC} = 400V, V_{GE} = 15V,$   $R_G = 10\Omega, T_j = 175^{\circ}C$ Inductive load 0.01 0 10 20 30 40 50 Collecter Current : I<sub>C</sub> [A]

vs. Gate Resistance

10

See Story

1

Eof V<sub>CC</sub> = 400V, V<sub>GE</sub> = 15V, I<sub>C</sub> = 12.5A, T<sub>j</sub> = 175°C Inductive load

0.01

0 10 20 30 40 50

Gate Resistance :  $R_G[\Omega]$ 

Fig.20 Typical Switching Energy Losses

#### ● Electrical Characteristic Curves

Fig.21 Typical Diode Forward Current vs. Forward Voltage

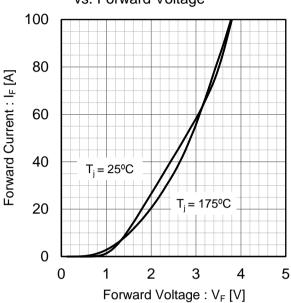


Fig.22 Typical Diode Revese Recovery Time vs. Forward Current

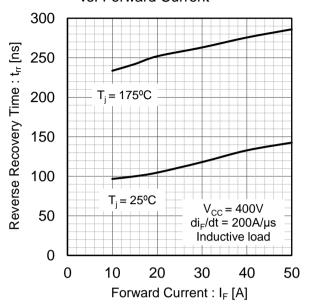


Fig.23 Typical Diode Reverse Recovery Current vs. Forward Current

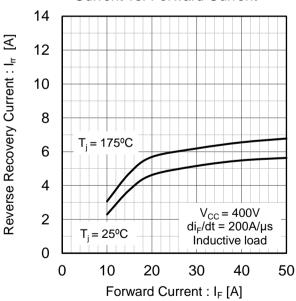
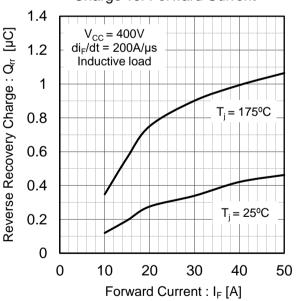


Fig.24 Typical Diode Rrverse Recovery Charge vs. Forward Current



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#### • Electrical Characteristic Curves

Fig.25 Typical IGBT Transient Thermal Impedance

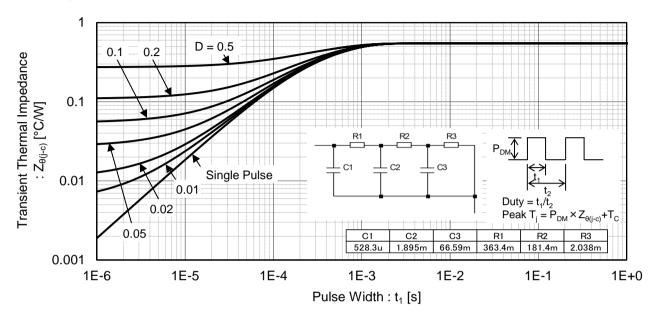
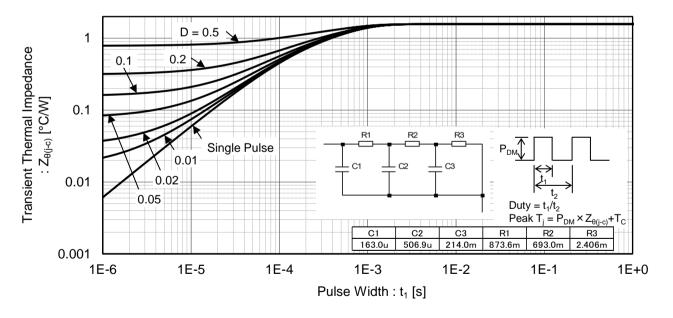


Fig.26 Typical Diode Transient Thermal Impedance



## Inductive Load Switching Circuit and Waveform

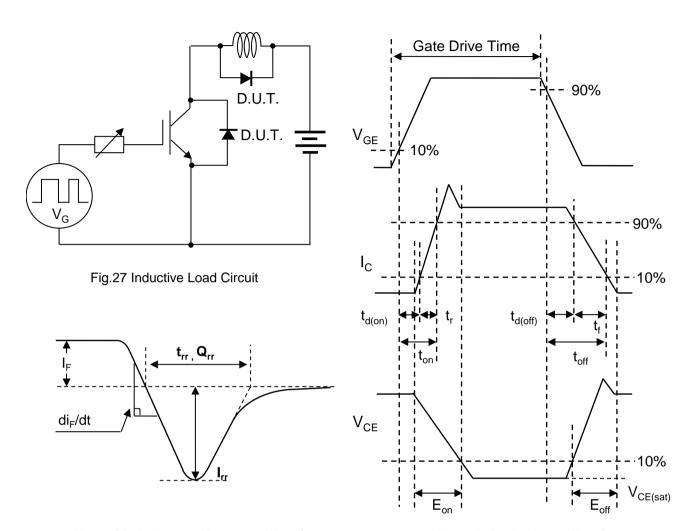


Fig.29 Diode Reverse Recovery Waveform

Fig.28 Inductive Load Waveform

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