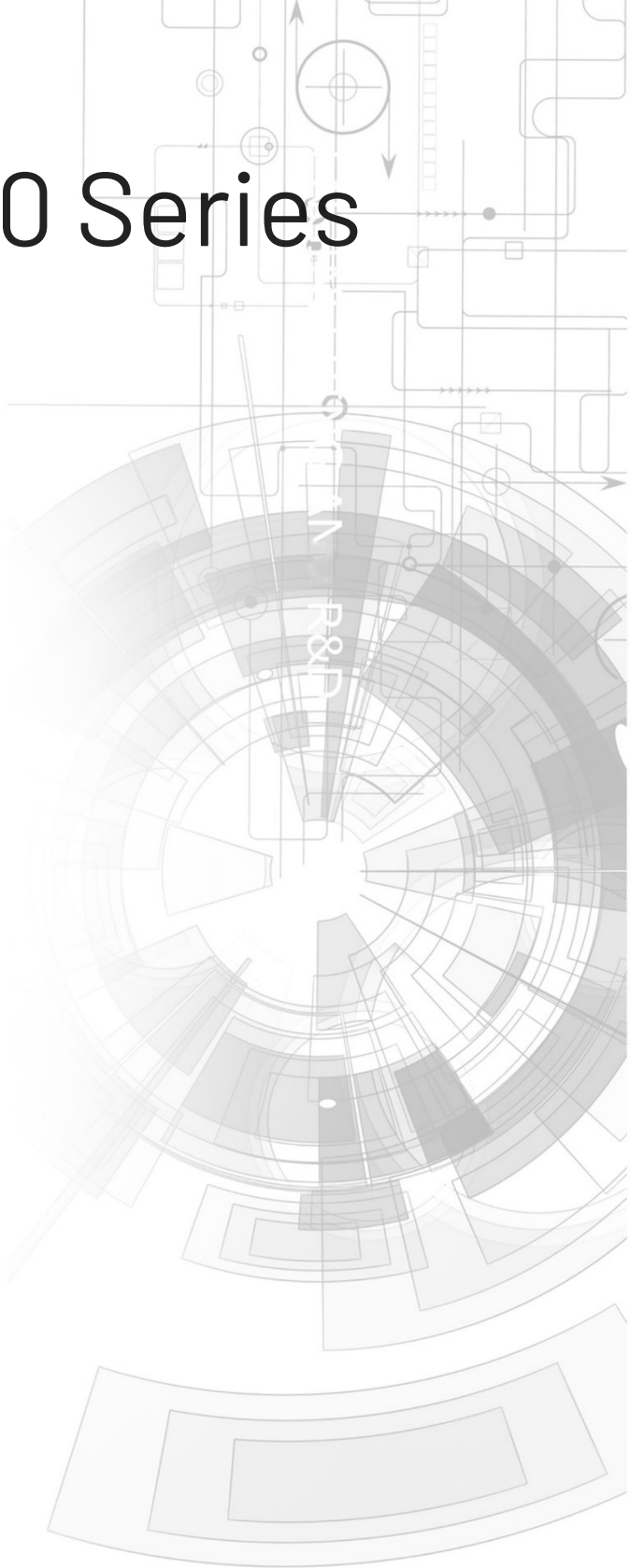
4DLCD-32320240 Series



4DLCD-32320240-RTP

4DLCD-32320240-CTP

4DLCD-32320240-CTP-CLB

Datasheet

Revision 1.3

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1. General Specification

4DLCD-32320240 is a colour active-matrix LCD module incorporating amorphous silicon TFT IPS (Thin Film Transistor). It is composed of a colour TFT-LCD panel, driver IC, FPC and a backlight unit with/without a Resistive/ Capacitive Touch Panel (RTP/CTP). The module display area contains 240 x 320 pixels. This product accords with RoHS environmental criteria.

Part Number Details:

4DLCD 4DLCD LCD Display

32320240 3.2 inches, 240 x 320 Resolution

RTP Resistive touch

CTP Capacitive touch

CLB Cover Lens Bezel





Note

1. RoHS compliant
2. LCD weight tolerance: ± 5%

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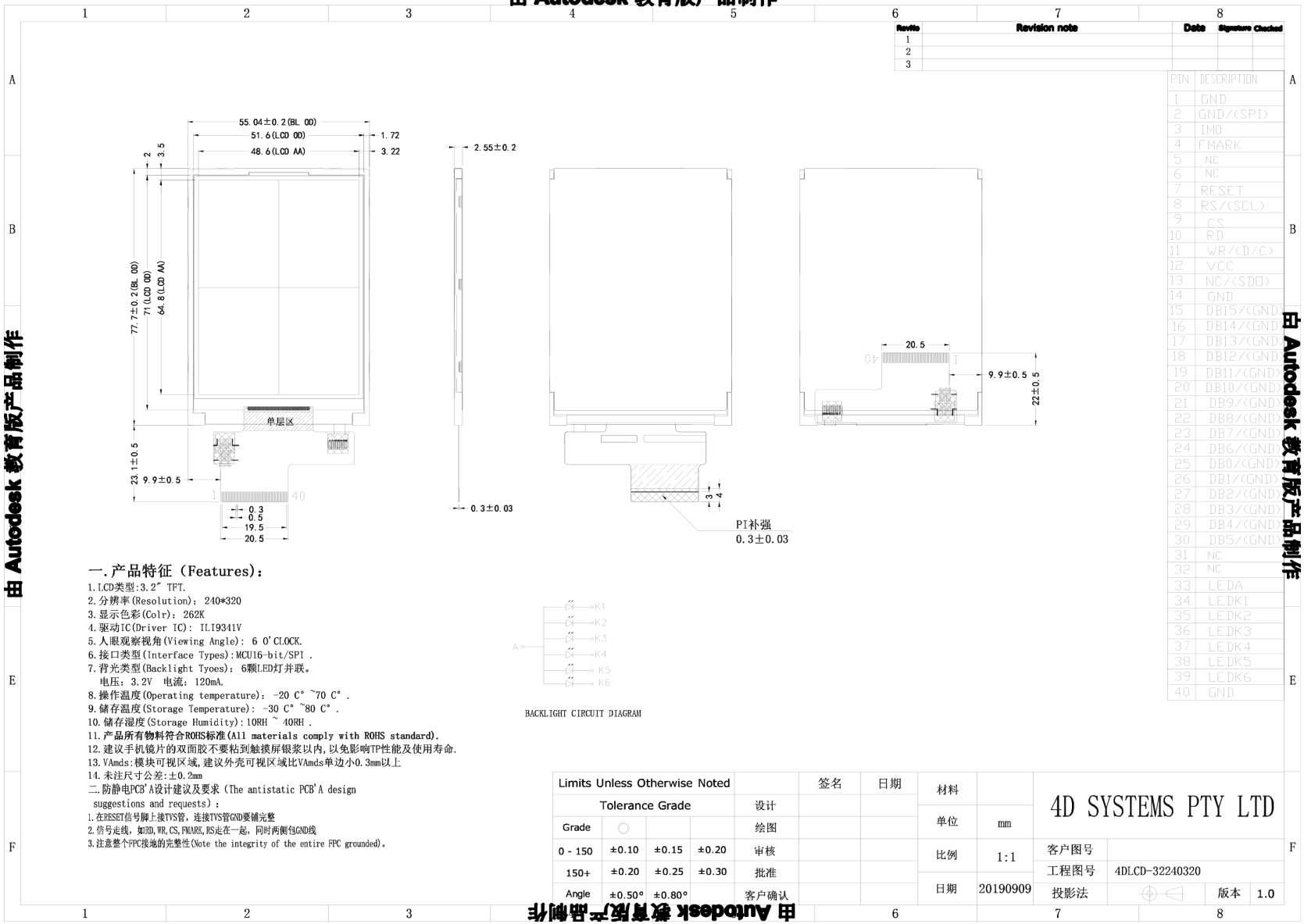
Specifications

ITEM	CONTENTS	UNIT
LCD Type	TFT / Transmissive / Normally white	
Size	3.2	Inch
Viewing Direction	6:00 (without image inversion)	O'Cloc
Gray Scale Inversion Direction	12:00	O'Cloc
LCD (W x H x T)	4DLCD-32320240: 55.04 x 77.70 x 2.57	mm
	4DLCD-32320240-RTP: 55.04 x 77.70 x 3.57	mm
	4DLCD-32320240-CTP: 55.04 x 77.70 x 5.00	mm
	4DLCD-32320240-CTP-CLB: 72.60 x 98.80 x 4.52 (including CLB)	mm
Active Area (W x H)	48.60 x 64.80	mm
Dot Pitch (W x H)	0.2025 x 0.2025	mm
Number of Dots (Pixels)	240 (RGB) x 320	
Driver IC	ILI9341V	
Backlight Type	6 LEDs	
Surface Luminance	4DLCD-32320240: 200 (typical)	
	4DLCD-32320240-RTP: 160 (typical)	cd/m2
	4DLCD-32320240-CTP: 190 (typical)	
	4DLCD-32320240-CTP-CLB: 190 (typical)	
Interface Type	MCU-16bit/SPI	
Colour Depth	262K	
Pixel Arrangement	RGB Vertical Stripe	
Surface Treatment	AG	
Input Voltage	2.8 (typical)	V
With/Without TP (Touch Panel)	4DLCD-32320240 - Without Touch Panel	
	4DLCD-32320240-RTP - With Resistive Touch	
	4DLCD-32320240-CTP - With Capacitive Touch	
	4DLCD-32320240-CTP-CLB - With Capacitive Touch and Cover Lens Bezel	
Weight	4DLCD-32320240: 19.8	
	4DLCD-32320240-RTP: 28.6	g
	4DLCD-32320240-CTP: 35.0	
	4DLCD-32320240-CTP-CLB: 42.4	

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2. TFT LCD Display Drawing (Non-Touch Version)

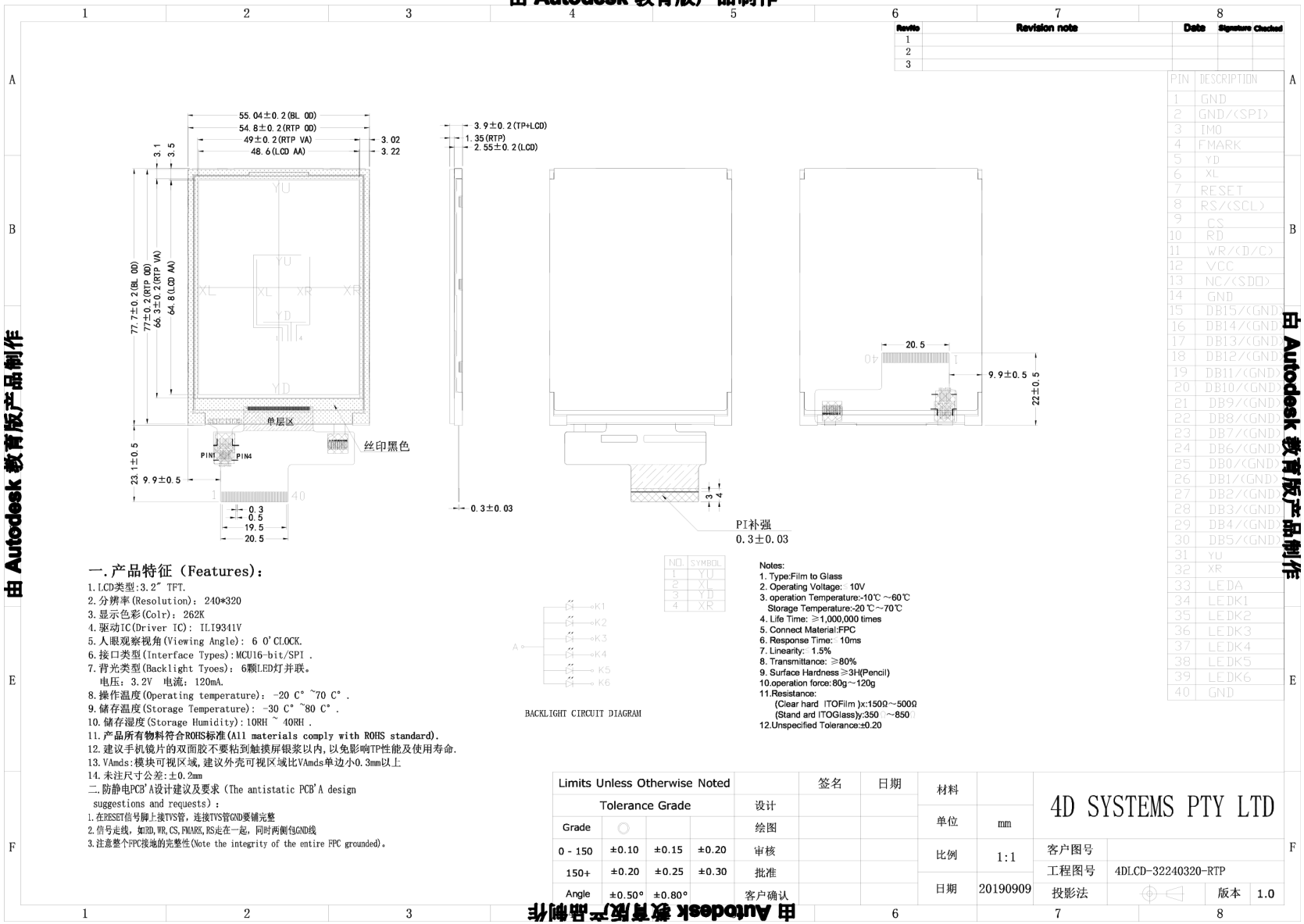
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3. TFT LCD Display Drawing (Resistive Touch Version)

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PIN	DESCRIPTION
1	GND
2	GND/(SPI)
3	IM0
4	FMARK
5	YD
6	XL
7	RESET
8	RS/(SCL)
9	CS
10	RD
11	WR/(D/C)
12	VCC
13	NC/(SDO)
14	GND
15	DB15/(GND)
16	DB14/(GND)
17	DB13/(GND)
18	DB12/(GND)
19	DB11/(GND)
20	DB10/(GND)
21	DB9/(GND)
22	DB8/(GND)
23	DB7/(GND)
24	DB6/(GND)
25	DB0/(GND)
26	DB1/(GND)
27	DB2/(GND)
28	DB3/(GND)
29	DB4/(GND)
30	DB5/(GND)
31	YU
32	XR
33	LEDA
34	LEDK1
35	LEDK2
36	LEDK3
37	LEDK4
38	LEDK5
39	LEDK6
40	GND

Revno	Revision note	Date	Signature	Checked
1				
2				
3				

一.产品特征（Features）：

1. LCD类型:3.2″ TFT.
2. 分辨率(Resolution)：240*320
3. 显示色彩(Colr)：262K
4. 驱动IC(Driver IC)：ILI9341V
5. 人眼观察视角(Viewing Angle)：6 O'CLOCK.
6. 接口类型(Interface Types):MCU16-bit/SPI .
7. 背光类型(Backlight Tyoes)：6颗LED灯并联。
   电压：3.2V 电流：120mA.
8. 操作温度(Operating temperature)：-20 C° ~70 C° .
9. 储存温度(Storage Temperature)：-30 C° ~80 C° .
10. 储存湿度(Storage Humidity):10RH ~ 40RH .
11. 产品所有物料符合ROHS标准(All materials comply with ROHS standard).
12. 建议手机镜片的双面胶不要粘到触摸屏银浆以内,以免影响TP性能及使用寿命.
13. VAmds:模块可视区域,建议外壳可视区域比VAmds单边小0.3mm以上
14. 未注尺寸公差:±0.2mm

二.防静电PCB'A设计建议及要求（The antistatic PCB'A design suggestions and requests）：

1.在RESET信号脚上接TVS管，连接TVS管GND要铺完整
2.信号走线，如RD,WR,CS,FMARK,RS走在一起，同时两侧包GND线
3.注意整个FPC接地的完整性(Note the integrity of the entire FPC grounded)。

NO.	SYMBOL
1	YU
2	XL
3	YD
4	XR

BACKLIGHT CIRCUIT DIAGRAM

Notes:
1. Type:Film to Glass
2. Operating Voltage: 10V
3. operation Temperature:-10℃～60℃
   Storage Temperature:-20℃～70℃
4. Life Time: ≥1,000,000 times
5. Connect Material:FPC
6. Response Time: 10ms
7. Linearity: 1.5%
8. Transmittance: ≥80%
9. Surface Hardness≥3H(Pencil)
10. operation force:80g～120g
11. Resistance:
    (Clear hard ITOFilm)x:150Ω～500Ω
    (Stand ard ITOGlass)y:350Ω～850Ω
12. Unspecified Tolerance:±0.20

Limits Unless Otherwise Noted				设计	签名	日期
Tolerance Grade				绘图		
Grade	○			审核		
0 - 150	±0.10	±0.15	±0.20	批准		
150+	±0.20	±0.25	±0.30	客户确认		
Angle	±0.50°	±0.80°				

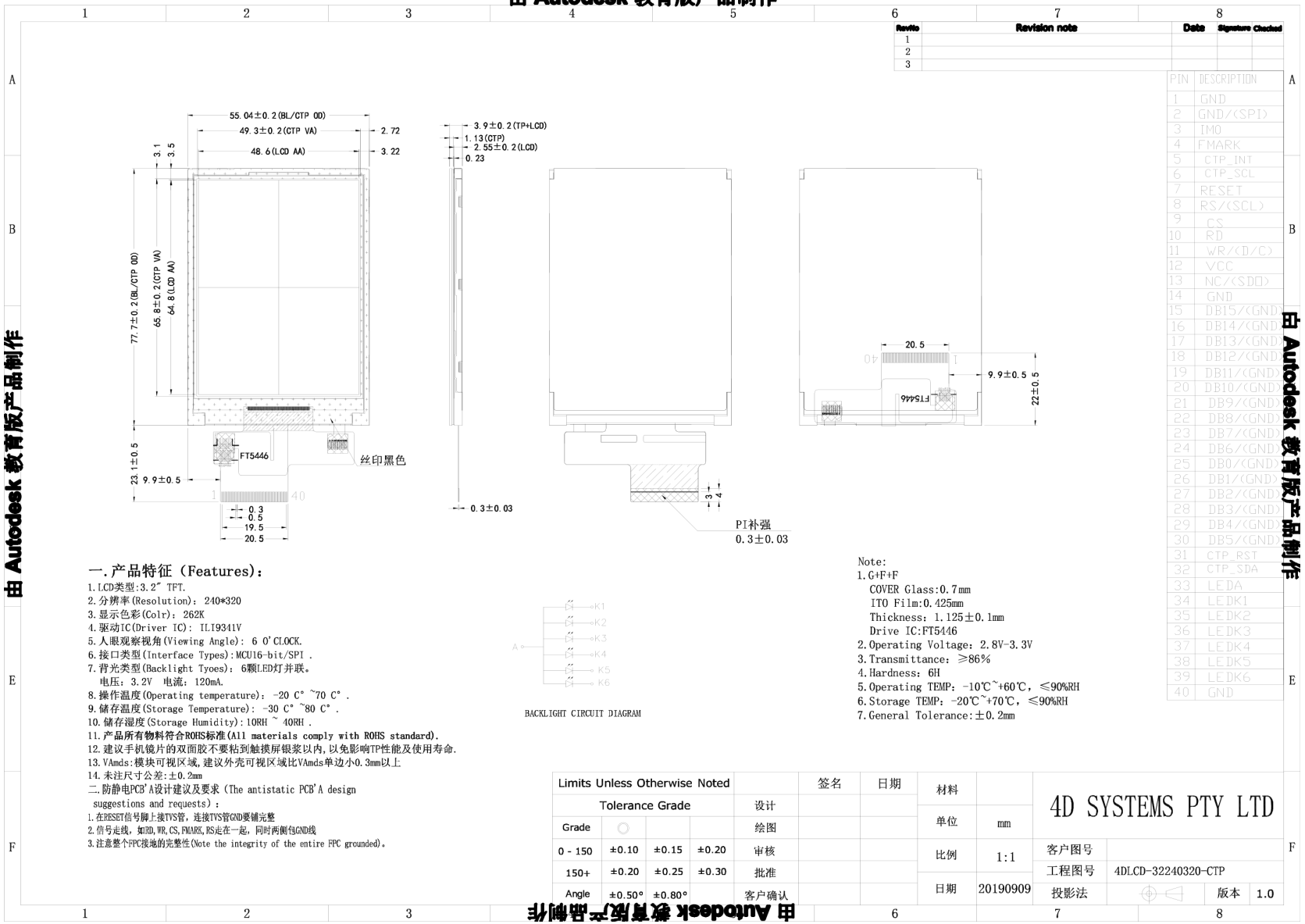
材料		4D SYSTEMS PTY LTD	
单位	mm		
比例	1:1	客户图号	
		工程图号	4DLCD-32240320-RTP
日期	20190909	投影法	版本 1.0

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4. TFT LCD Display Drawing (Capacitive Touch Version)

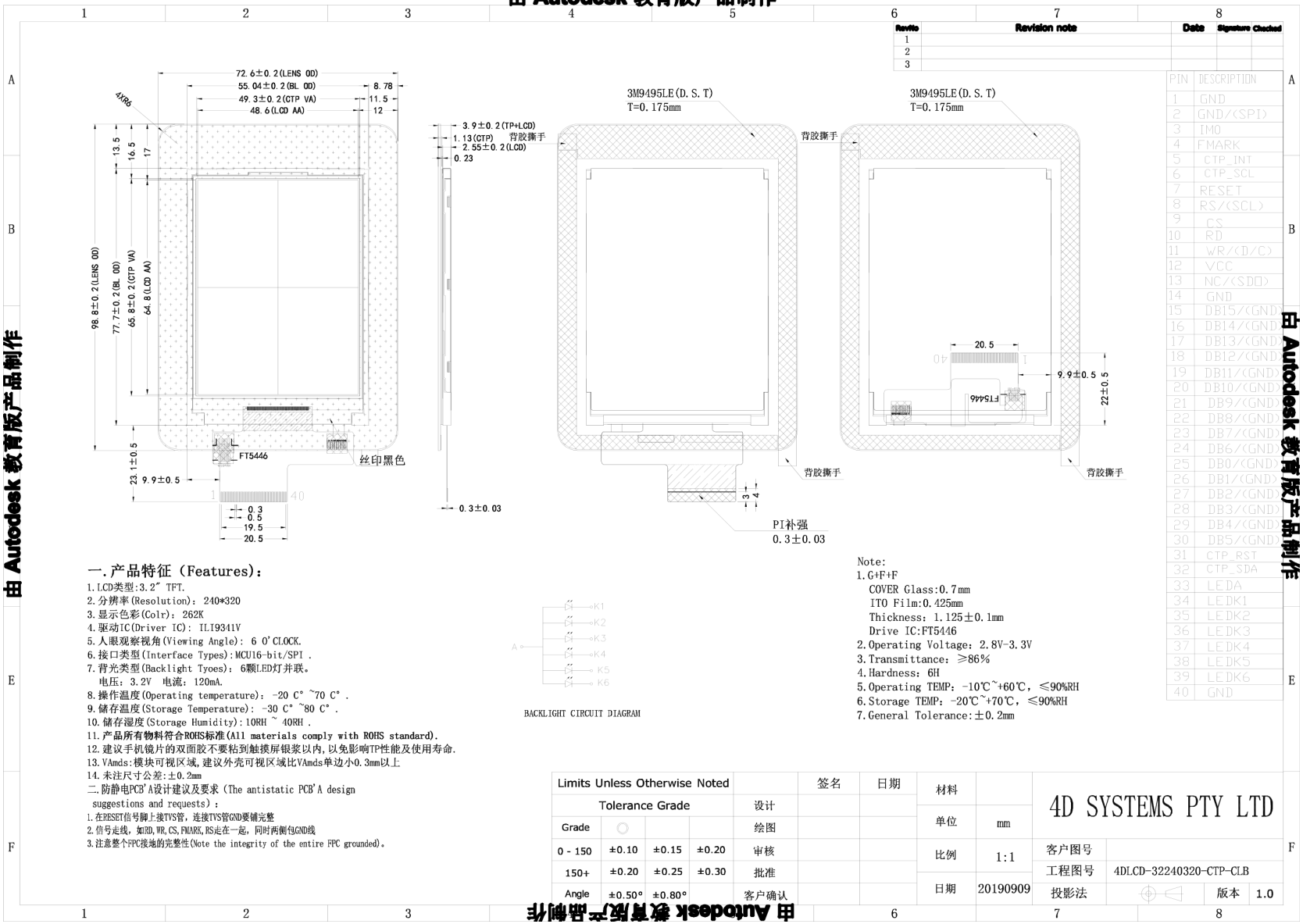
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5. TFT LCD Display Drawing (Capacitive Touch Version with Cover Lens Bezel)

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Revision	Revision note	Date	Signature	Checked
1				
2				
3				

PIN	DESCRIPTION
1	GND
2	GND/(SPI)
3	IM0
4	FMARK
5	CTP_INT
6	CTP_SCL
7	RESET
8	RS/(SCL)
9	CS
10	RD
11	WR/(D/C)
12	VCC
13	NC/(SDO)
14	GND
15	DB15/(GND)
16	DB14/(GND)
17	DB13/(GND)
18	DB12/(GND)
19	DB11/(GND)
20	DB10/(GND)
21	DB9/(GND)
22	DB8/(GND)
23	DB7/(GND)
24	DB6/(GND)
25	DB0/(GND)
26	DB1/(GND)
27	DB2/(GND)
28	DB3/(GND)
29	DB4/(GND)
30	DB5/(GND)
31	CTP_RST
32	CTP_SDA
33	LEDA
34	LEDK1
35	LEDK2
36	LEDK3
37	LEDK4
38	LEDK5
39	LEDK6
40	GND

一.产品特征（Features）：

1. LCD类型:3.2″ TFT.
2. 分辨率(Resolution)：240*320
3. 显示色彩(Colr)：262K
4. 驱动IC(Driver IC)：ILI9341V
5. 人眼观察视角(Viewing Angle)：6 O'CLOCK.
6. 接口类型(Interface Types):MCU16-bit/SPI .
7. 背光类型(Backlight Tyoes)：6颗LED灯并联。
   电压：3.2V 电流：120mA.
8. 操作温度(Operating temperature)：-20 C° ~70 C° .
9. 储存温度(Storage Temperature)：-30 C° ~80 C° .
10. 储存湿度(Storage Humidity):10RH ~ 40RH .
11. **产品所有物料符合ROHS标准(All materials comply with ROHS standard).**
12. 建议手机镜片的双面胶不要粘到触摸屏银浆以内,以免影响TP性能及使用寿命.
13. VAmds:模块可视区域,建议外壳可视区域比VAmds单边小0.3mm以上
14. 未注尺寸公差:±0.2mm

二.防静电PCB'A设计建议及要求（The antistatic PCB'A design suggestions and requests）：

1. 在RESET信号脚上接TVS管，连接TVS管GND要铺完整
2. 信号走线，如RD,WR,CS,FMARK,RS走在一起，同时两侧包GND线
3. 注意整个FPC接地的完整性(Note the integrity of the entire FPC grounded)。

Note:
1. G+F+F
   COVER Glass:0.7mm
   ITO Film:0.425mm
   Thickness：1.125±0.1mm
   Drive IC:FT5446
2. Operating Voltage：2.8V-3.3V
3. Transmittance：≥86%
4. Hardness：6H
5. Operating TEMP：-10℃~+60℃，≤90%RH
6. Storage TEMP：-20℃~+70℃，≤90%RH
7. General Tolerance:±0.2mm

Limits Unless Otherwise Noted				设计	签名	日期	材料		4D SYSTEMS PTY LTD	
Tolerance Grade				绘图			单位	mm		
Grade	○			审核			比例	1:1	客户图号	
0 - 150	±0.10	±0.15	±0.20	批准					工程图号	4DLCD-32240320-CTP-CLB
150+	±0.20	±0.25	±0.30	客户确认			日期	20190909	投影法	版本 1.0
Angle	±0.50°	±0.80°								

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6. Absolute Maximum Ratings

Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage for LCD Logic	VDD/VCC	-0.3	4.6	V
Input Voltage for Logic	VIN	VSS-0.5	VDD	V
LED forward current (each LED)	IF	-	25	mA
Operating Temperature	TOP	-20	70	°C
Storage Temperature	TST	-30	80	°C
Humidity	RH	-	90% (Max 60°C)	RH

7. Electrical Characteristics

Electrical Characteristics

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Power Voltage (Logic)	VDD/DCC	2.6	2.8	3.3	V
Input Current (Logic)	IVDD	-	13	-	mA
Input Voltage 'H' Level	VIH	0.7 VDD	-	VDD	V
Input Voltage 'L' Level	VIL	0	-	0.3 VDD	V

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8. Electro-Optical Characteristics

Electro-Optical Characteristics

ITEM	SYM	CONDITION	MIN	TYP	MAX	UNIT	REMARK
Response Time	Tr+Tf	θ=0	-	30	-	ms	see figure
Contrast Ratio	Cr	°	-	250	-	-	see figure
Luminance Uniformity	δ WHITE	∅=0	75	80	-	%	see figure
Surface Luminance	Lv	4DLCD-32320240	180	200	-	cd/ m2	see figure
		4DLCD-32320240-RTP	144	160	-		
		4DLCD-32320240-CTP	175	194	-		
		4DLCD-32320240-CTP-CLB	175	194	-		
Viewing Angle Range	θ	∅ = 90°	-	35	-	deg	see figure
		∅ = 270°	-	55	-	deg	
		∅ = 0°	-	55	-	deg	
		∅ = 180°	-	55	-	deg	
CIE (x,y) Cromacity - Red	x		0.574	0.624	0.674		
	y		0.318	0.368	0.418		
CIE (x,y) Cromacity - Green	x	θ=0°	0.300	0.350	0.400		
	y	∅=0°	0.500	0.550	0.600		see figure
CIE (x,y) Cromacity - Blue	x	Ta=25	0.093	0.143	0.193		
	y		0.069	0.119	0.169		
CIE (x,y) Cromacity - White	x		0.260	0.310	0.360		
	y		0.283	0.333	0.383		

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9. Backlight Characteristics

Backlight Characteristics

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Voltage for LED backlight (Each LED)	Vl	-	3.2	3.4	V
Current for LED backlight (Each LED)	Il-each	-	20	25	mA
Current for LED backlight (Total - 4 LEDS)	Il	-	120	150	mA
LED Lifetime (50% of original brightness)	-	30000	-	-	Hrs

Note

The LED lifetime is defined as the module brightness decreasing to 50% original brightness at Ta=25°C.

1. Contrast Ratio(CR) is defined mathematically as below, for more information see figure.

   Contrast Ratio

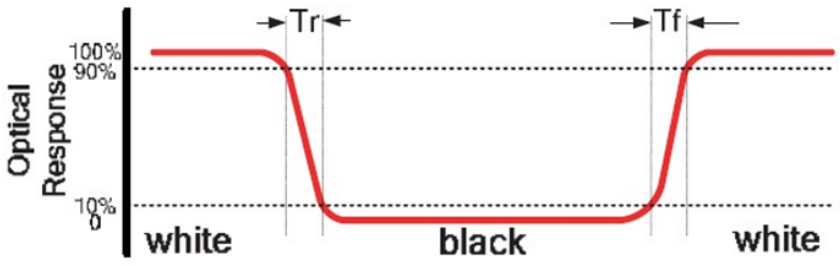
2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information, see Figure 2.

   Surface Luminance

3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by the minimum luminance of 5 points luminance. For more information, see figure.

   δ WHITE

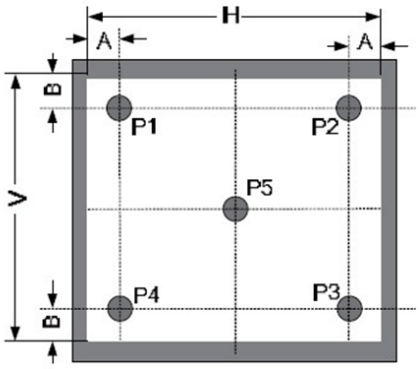
4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 1. The test equipment is the Autronic-Melchers ConoScope series.
5. CIE (x, y) chromaticity, the x and y value is determined by measuring luminance at each test position 1 through 5, and then making the average value.
6. Viewing angle is the angle at which the contrast ratio is greater than 2. For the TFT module, the contrast ratio is greater than 10. The angles are determined for the horizontal or x-axis and the vertical or y-axis to the z-axis which is normal to the LCD surface. For more information, see figure.
7. For viewing angle and response time testing, the testing data is based on the Autronic-Melchers ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, and CIE the test data is based on TOPCONs BM-5 photodetector.



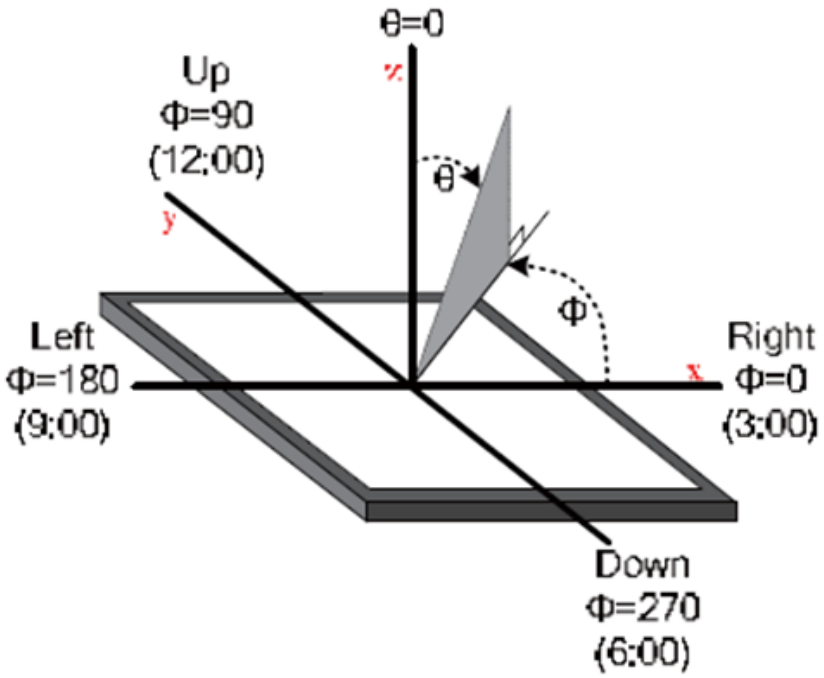
The definition of response time

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A : 5 mm
B : 5 mm
H, V : Active Area
Light spot size ∅=5mm, 500mm distance from the LCD surface to detector lens
measurement instrument is TOPCON's luminance meter BM-5



Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

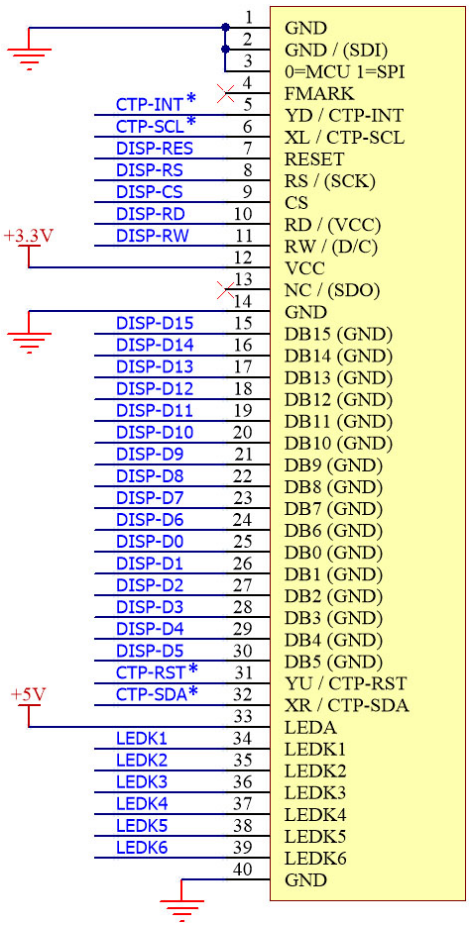


The definition of viewing angle

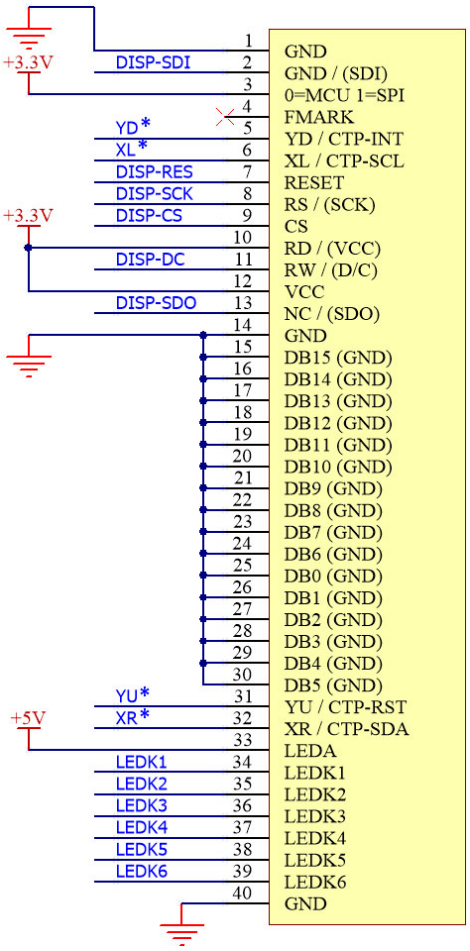
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10. Interface Descriptions

10.1. LCD Interface



MCU Mode



SPI Mode

***** This has no connection (NC) for Non-touch displays.

PIN NO.	SYMBOL	DESCRIPTION	REMARK
1	GND	Ground	
2	GND/(SDI)	Ground for MCU mode/ Serial Data Input signal for SPI mode	
3	MODE	MCU-16bit /SPI mode selection pin	Note 1
4	FMARK	Tearing effect output signal	
5	YD/ CTP_INT	The touch panel Y bottom pin for RTP / Interrupt signal from CTP/No Connection (NC) for Non touch Version	Note 3
6	XL/ CTP_SCL	The touch panel X left pin for RTP/ I2C SCL for CTP / No Connection (NC) for Non touch Version	Note 3
7	RESET	Reset input signal	
8	RS/SCL	Instruction Register/Data Register selection pin for MCU mode / SPI Serial Clock for SPI mode	
9	CS	Chip select input pin	
10	RD/(VCC)	Read signal for MCU mode / Tie to VCC for SPI mode	

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PIN NO.	SYMBOL	DESCRIPTION	REMARK
11	WR/(D/C)	Write signal for MCU mode / Data or Command selection for SPI mode	
12	VCC	Power supply (Logic)	
13	NC/(SDO)	No Connect for MCU mode / Serial Data Output for SPI mode	
14	GND	Ground	
15	DB15/(GND)	Databus DB15 for MCU mode / Tie to GND for SPI mode	Note 2
16	DB14/(GND)	Databus DB14 for MCU mode / Tie to GND for SPI mode	Note 2
17	DB13/(GND)	Databus DB13 for MCU mode / Tie to GND for SPI mode	Note 2
18	DB12/(GND)	Databus DB12 for MCU mode / Tie to GND for SPI mode	Note 2
19	DB11/(GND)	Databus DB11 for MCU mode / Tie to GND for SPI mode	Note 2
20	DB10/(GND)	Databus DB10 for MCU mode / Tie to GND for SPI mode	Note 2
21	DB9/(GND)	Databus DB9 for MCU mode / Tie to GND for SPI mode	Note 2
22	DB8/(GND)	Databus DB8 for MCU mode / Tie to GND for SPI mode	Note 2
23	DB7/(GND)	Databus DB7 for MCU mode / Tie to GND for SPI mode	Note 2
24	DB6/(GND)	Databus DB6 for MCU mode / Tie to GND for SPI mode	Note 2
25	DB0/(GND)	Databus DB0 for MCU mode / Tie to GND for SPI mode	Note 2
26	DB1/(GND)	Databus DB1 for MCU mode / Tie to GND for SPI mode	Note 2
27	DB2/(GND)	Databus DB2 for MCU mode / Tie to GND for SPI mode	Note 2
28	DB3/(GND)	Databus DB3 for MCU mode / Tie to GND for SPI mode	Note 2
29	DB4/(GND)	Databus DB4 for MCU mode / Tie to GND for SPI mode	Note 2
30	DB5/(GND)	Databus DB5 for MCU mode / Tie to GND for SPI mode	Note 2
31	YU/ CTP_RST	The touch panel Y up pin for RTP / Reset pin for CTP / No Connection (NC) for Non touch Version	Note 3
32	XR/ CTP_SDA	The touch panel X right pin for RTP / I2C SDA Pin for CTP / No Connection (NC) for Non touch Version	Note 3
33	LEDA	Anode of LED Backlight	
34	LEDK1	Cathode1 of LED Backlight	
35	LEDK2	Cathode2 of LED Backlight	
36	LEDK3	Cathode3 of LED Backlight	
37	LEDK4	Cathode4 of LED Backlight	
38	LEDK5	Cathode5 of LED Backlight	
39	LEDK6	Cathode6 of LED Backlight	
40	GND	Ground	

Note

1. Interface Mode Selection (pin 3)

Value	Interface	Remark
0	MCU 16bit	Databus:DB0~DB15
1	SPI	4 wire SPI Mode

2. Pins 15 to 30 are grounded on SPI mode.
3. Pins 5,6,31 and 32 are only applicable to touchscreen displays (4DLCD-xxxxxxxx-RTP/CTP).

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10.2. CTP Interface

The Capacitive Touch is driven by a **Focaltech FT5446** capacitive touch driver IC, which utilizes an I2C interface, and is capable of 5-point touch.

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11. Backlight Example Circuit

The backlight circuit can be easily driven off a 5V supply. The voltage needs to be higher than the forward voltage of the LEDs and other drops in the circuit, which can be found in the Interface Descriptions section. A 3.3V supply typically does not meet this requirement, so a 5V supply is normally used.

In this circuit, a simple transistor is used to switch each of the LEDs on, where each LED is current-limited through its resistor. The transistor base can be PWM'ed if required (Label BACKLIGHT below), to give dimming control. An example PWM frequency that could be used is 3Khz, however, it is up to the Designer to determine what is suitable for the application.

The resistance feeding each LED can be calculated using Ohms Law, V=I x R.

We want to calculate R, so R=V/I

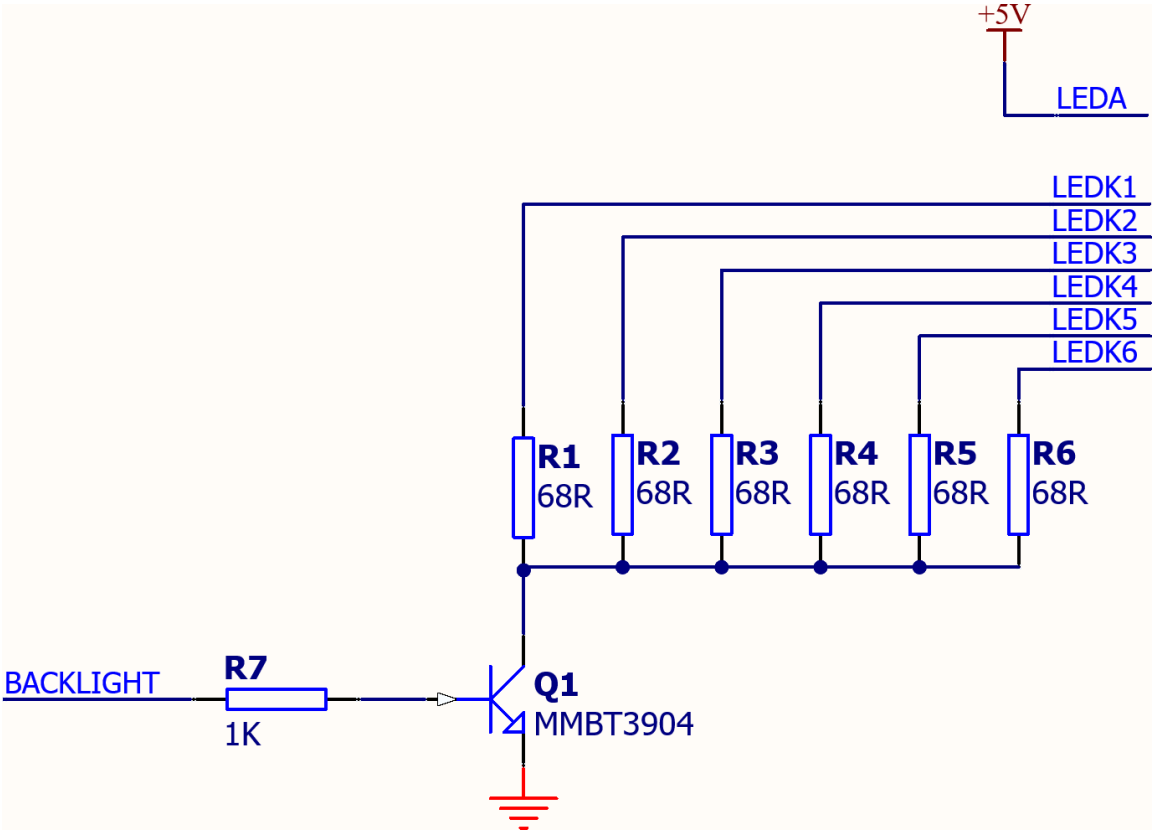
Supply Voltage = 5V

LED Forward Voltage = 3.2V (see the Interface Descriptions section)

Estimated voltage drop over transistor = 0.3V

Target current per LED = 20mA to 25mA (use 22mA in this example) (See the Interface Descriptions section)

Formula is then R = (5V-3.2V-0.3V) / 0.022A = 68ohms for each LED (LEDK1 to LEDK6)



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12. Initialisation Code

```
//*********** Hardware reset ***********//

LCD_RESET = 1;
Delayms(15);
LCD_RESET = 0;
Delayms(120);
LCD_RESET = 1;
Delayms(120);

//******* Start Initial Sequence *******//

write_reg(0xCF);
write_dat(0x00);
write_dat(0x83);
write_dat(0x30);
write_reg(0xED);
write_dat(0x64);
write_dat(0x03);
write_dat(0X12)
write_dat(0X81)
write_reg(0xCB);
write_dat(0x39);
write_dat(0x2C);
write_dat(0x00);
write_dat(0x34);
write_dat(0x02);
write_reg(0xEA);
write_dat(0x00);
write_dat(0x00);
write_reg(0xE8);
write_dat(0x85);
write_dat(0x10);
write_dat(0x79);
write_reg(0xC0); // Power control
write_dat(0x23); // VRH[5:0]
write_reg(0xC1); // Power control
write_dat(0x11); // SAP[2:0];BT[3:0]
write_reg(0xC2);
write_dat(0x11);
write_reg(0xC5); // VCM control
write_dat(0x3D);
write_dat(0x30);
write_reg(0xC7); // VCM control2
write_dat(0XAA);
write_reg(0x3A);
write_dat(0x55);
write_reg(0x36); // Memory Access Control
write_dat(0x08);
write_reg(0xB1);
write_dat(0x00);
write_dat(0x11);
write_reg(0xB6); // Display Function Control
write_dat(0x0A);
```

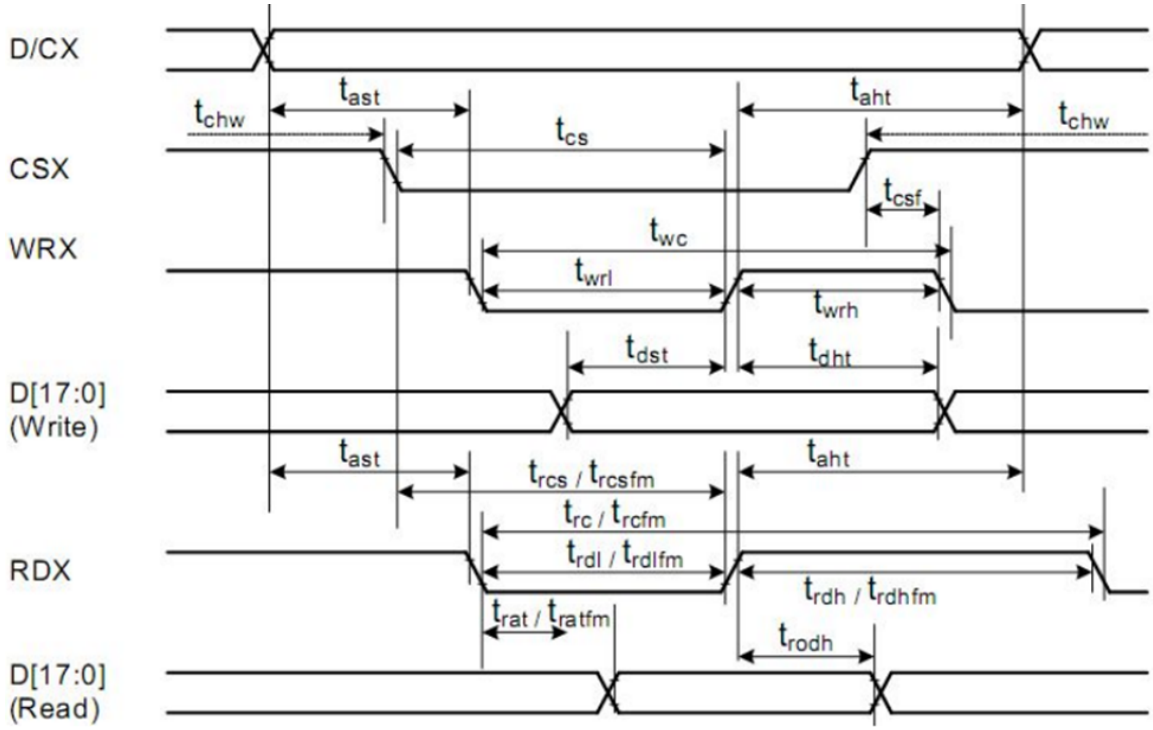

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```
write_dat(0xA2);
write_reg(0xF2); // 3Gamma Function Disable
write_dat(0x00);
write_reg(0XF7);
write_dat(0x20);
write_reg(0XF1);
write_dat(0x01);
write_dat(0x30);
write_reg(0x26);
write_dat(0x01);
write_reg(0xE0); // Set Gamma
write_dat(0x0F);
write_dat(0x3F);
write_dat(0x2F);
write_dat(0x0C);
write_dat(0x10);
write_dat(0x0A);
write_dat(0x53);
write_dat(0XD5);
write_dat(0x40);
write_dat(0x0A);
write_dat(0x13);
write_dat(0x03);
write_dat(0x08);
write_dat(0x03);
write_dat(0x00);
write_reg(0XE1); // Set Gamma
write_dat(0x00);
write_dat(0x00);
write_dat(0x10);
write_dat(0x03);
write_dat(0x0F);
write_dat(0x05);
write_dat(0x2C);
write_dat(0XA2);
write_dat(0x3F);
write_dat(0x05);
write_dat(0x0E);
write_dat(0x0C);
write_dat(0x37);
write_dat(0x3C);
write_dat(0x0F);
write_reg(0x11); // Exit Sleep
Delayms(120);
write_reg(0x29); // Display on
```


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13. LCD Timing Details

13.1. Timing Chart



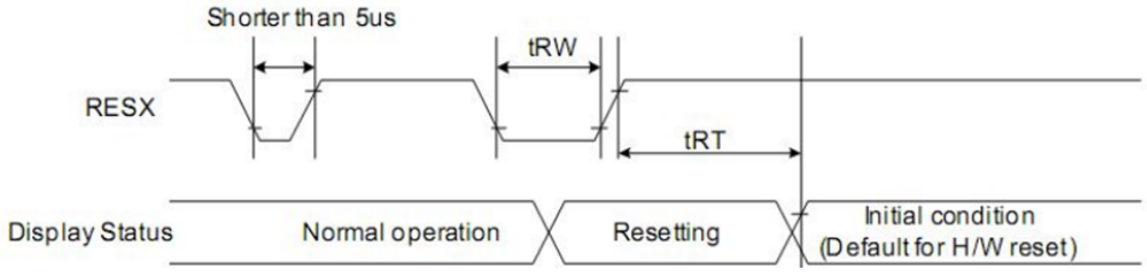
SIGNAL	SYMBOL	PARAMETER	MIN	MAX	UNIT
DCX	tast	Address setup time	0	-	ns
	taht	Address hold time (Write/Read)	0	-	ns
CSX	tchw	CSX "H" pulse width	0	-	ns
	tcs	Chip Select setup time	15	-	ns
	trcs	Chip Select setup time (Read ID)	45	-	ns
	trcsfm	Chip Select setup time (Read FM)	355	-	ns
	tcsf	Chip Select Wait time (Write/Read)	10	-	ns
WRX	twc	Write cycle	66	-	ns
	twrh	Write Control Pulse H duration	15	-	ns
	twrl	Write Control Pulse L duration	15	-	ns
RDX(FM)	trcfm	Read cycle (FM)	450	-	ns
	trdhfm	Read Control Pulse H duration (FM)	90	-	ns
	trdlfm	Read Control Pulse L duration (FM)	355	-	ns
RDX(ID)	trc	Read cycle (ID)	160	-	ns
	trdh	Read Control Pulse H duration	90	-	ns
	trdl	Read Control Pulse L duration	45	-	ns
D[17:0]	tdst	Write data setup time	10	-	ns
	tdht	Write data hold time	10	-	ns
	trat	Read access time	-	40	ns
	Tratfm	Read access time	-	340	ns
	trod	Read output disable time	20	80	ns

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Note

Timing parameter (VDD=3.3V, GND=0V, Ta=25˚C)

13.2. Reset Timing



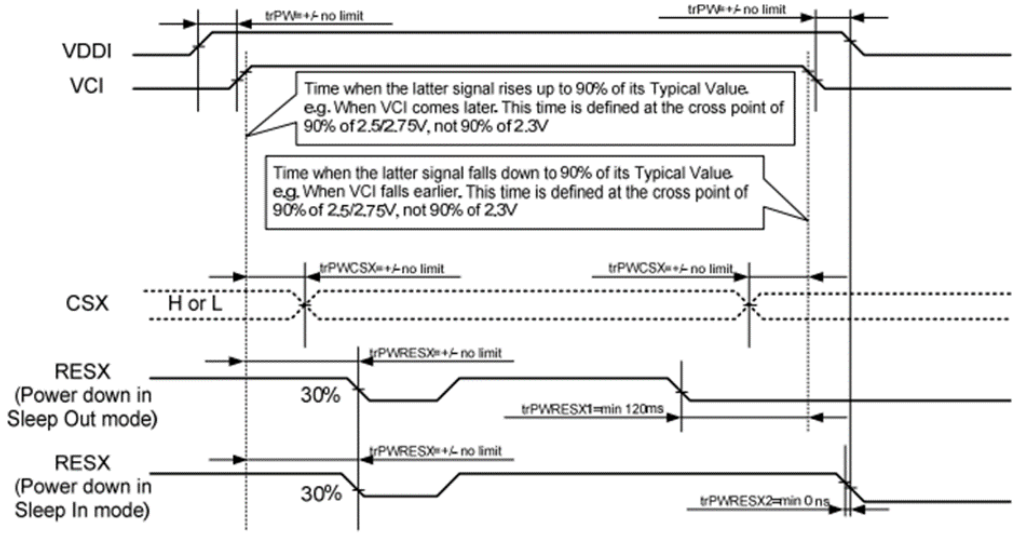
SIGNAL	SYMBOL	PARAMETER	MIN	MAX	UNIT
RESET	tRW	Reset low pulse width	10	-	us
	tRT	Reset complete time	-	5 (note1)	ms
			-	120 (note2)	ms

Note

1. When reset is applied during SLPIN mode.
2. When reset applied during SLPOUT mode.

13.3. Power On Sequence

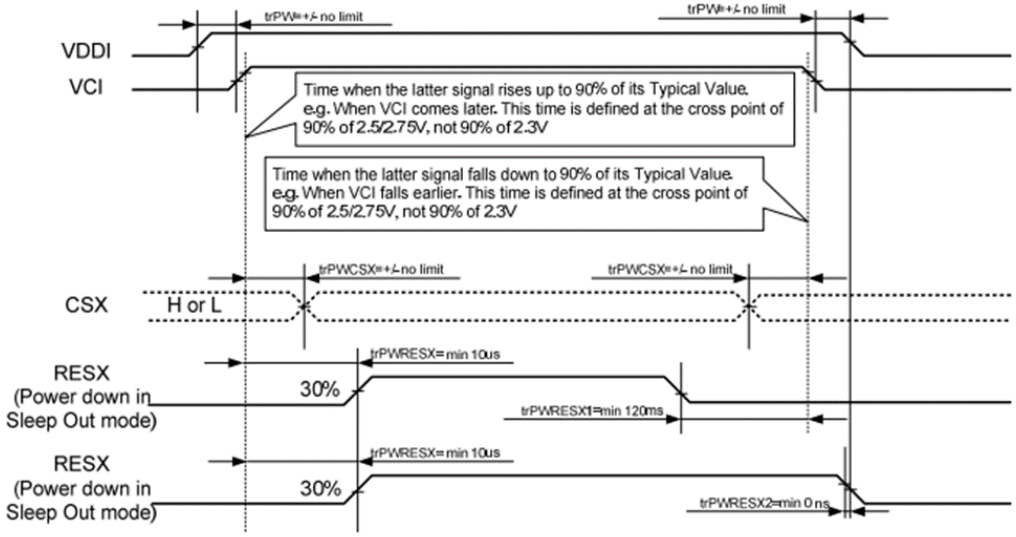
13.3.1. Case 1 - RES line is held High or Unstable by Host at Power ON



trPWRESX1 is applied to RESX falling in the Sleep Out Mode
trPWRESX2 is applied to RESX falling in the Sleep In Mode

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13.3.2. Case 2 - RES line is held Low by Host at Power ON



trPWRESX1 is applied to RESX falling in the Sleep Out Mode
trPWRESX2 is applied to RESX falling in the Sleep In Mode

13.4. Power-off Sequence - Uncontrolled Power Off

Uncontrolled power off is a situation where power is removed unexpectedly, e.g. a battery powering a device is disconnected without using the controlled power off sequence. There will not be any damage to the display module, nor will the display module cause any damage to the host. During an uncontrolled power-off event, ILI9341V will force the display to blank its content and there will not be any further abnormal visible effects on the display after 1 second of the power being removed. The display will remain blank until the Power On Sequence occurs.

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14. Reliability Test

Reliability Test

No.	SYMBOL	TEST CONDITION	REMARK
1	High Temperature Storage	80℃±2℃ 96H Restore 2H at 25℃ Power off	
2	Low Temperature Storage	-30℃±2℃ 96H Restore 2H at 25℃ Power off	
3	High Temperature Operation	70℃±2℃ 96H Power on	
4	Low Temperature Operation	-20℃±2℃ 96H Power on	
5	High Temperature & Humidity Operation	60℃±2℃ 90%RH 96H Power on	After test cosmetic and electrical defects should not happen.
6	Temperature Cycle	-20℃←→25℃←→70℃ 30min 5min 30min After 10 cycles, restore 2H at 25℃ Power off	
7	Vibration Test	10Hz~150Hz, 100m/s^2, 120min	
8	Shock Test	Half-sinewave, 300m/s^2, 11ms	

Note

The Displays are of the highest rated 'Grade A', which allows for 0-4 defective pixels. A defective pixel could be solid Black (Dead), White, Red, Green or Blue.

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15. Precautions for Using LCD Modules

15.1. Handing Precautions

- The display panel is made of glass and a polarizer. The glass is fragile. It tends to be chipped during handling, especially on the edges. Please avoid dropping or jarring. Please be careful not subject it to a mechanical shock by dropping it on impact.
- If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any of it in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degrade insulation between terminals (some cosmetics are determined by the polarizer).
- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizer with anything harder than an HB pencil lead (e.g., glass, tweezers, etc.). Do not put or attach anything to the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold temperatures will damage, stain or contaminate the polarizer. After products are tested at low temperatures they must be warmed up in a container before coming into contact with room-temperature air.
- If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten the cloth with one of the following solvents
    - Isopropyl alcohol
    - Ethyl alcohol Do not scrub hard as it might damage the display surface.
- Solvents other than those mentioned above may damage the polarizer. Especially the following.
    - Water
    - Ketone
    - Aromatic solvents Wipe off saliva or water drops immediately, contact with water over a long period may cause deformation or color fading. Avoid contact with oil and fat.
- Take necessary precautions to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or current flow in a high-humidity environment.
- Do not attempt to disassemble or process the LCD module.
- NC terminal should be open. Do not connect anything to it.
- If the logic circuit power is off, do not apply input signals.

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- Control Electro-Static Discharge. Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent the destruction of the elements by static electricity, ensure that an optimum work environment is maintained.
    - Before removing the LCM from its packing case or incorporating it into a set, be sure that the module and your body have the same electric potential. Be sure to ground your body when handling the LCD modules.
    - To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions. To reduce the generation of static electricity, please ensure that the air in the work environment is not too dry. Relative humidity of 50%-60% is recommended. As much as possible, make the electric potential of your work clothes and that of the workbench the ground potential.
    - The LCD module is coated with a film to protect the display surface. Be careful when peeling off this protective film since static electricity may be generated.
- Since the LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
    - Do not alter, modify or change the shape of the tab on the metal frame.
    - Do not make extra holes on the printed circuit board, modify its shape or change the positions of the components to be attached.
    - Do not damage or modify the pattern writing on the printed circuit board.
    - Do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
    - Do not drop, bend or twist the LCM.

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15.2. Storage Precautions

When storing the LCD modules, the following precautions are necessary.

- Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
- Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0℃ and 35℃, and keep the relative humidity between 40%RH and 60%RH.
- The polarizer surface should not come in contact with any other objects. We advise you to store them in the anti-static electricity container in which they were shipped. Some Liquid crystals solidify under low temperatures (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to low temperatures.
- If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from the destruction caused by static electricity etc., please avoid holding the following sections when handling the modules'
    - The exposed area of the printed circuit board
    - Terminal electrode sections

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16. Revision History

Document Revision

Revision Number	Date	Description
1.0	01/07/2019	Initial Version
1.1	22/08/2020	Minor update / fixes
1.2	24/06/2021	Added logic current consumption data, added backlight circuit example
1.3	20/01/2023	Modified datasheet for web-based documentation
1.4	02/04/2025	Remove irrelevant precaution related to mounting ears

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