



## NCV8537MN500GEVB: 5.0 V LDO Regulator Evaluation Board

The demoboard supports the LDO regulator NCV8537 in DFN10 package with fix output voltage 5.0 V. The ENABLE function allows turn the device to low consumption mode with quiescent current below 1  $\mu$ A. The Power Good pin allows output voltage monitoring. The additional CNR capacitor reduces the output noise.

Size: 45mm x 30mm x 18mm



### Features and Applications

#### Features

- High Accuracy Over Line and Load ( $\pm 0.9\%$  at 25°C)
- Operating Temperature Range: -40°C to 125°C.)
- Stable Output with Low Value Capacitors of any type and with no Minimum Load Current Requirement Incorporates Current Limiting and Reverse Bias Protection
- Low Noise (33 Vrms w/10 nF Cnr and 52 Vrms w/out Cnr)
- Low Shutdown Current (< 1 mA)
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- Thermal Shutdown Protection
- Current Limitation
- Requires Only 1.0  $\mu$ F Output Capacitance for Stability
- Power Good Output to Indicate the Regulator is Within Specified Limits

#### Applications

- Networking Systems, DSL/Cable Modems
- Audio Systems for Automotive Applications
- Navigation Systems
- Cable Set-Top Box
- Satellite Receivers
- Displays and Monitors

### Evaluation/Development Tool Information

Product	Status	Compliance	Short Description	Parts Used	Action
NCV8537MN500GEVB	Active		5.0 V LDO Regulator Evaluation Board	NCV8537MN500R2G	<a href="#">» Contact Local Sales Office</a> <a href="#">» Inventory</a>

### Technical Documents

Type	Document Title	Document ID/Size	Rev
Eval Board: BOM	NCV8537MN500GEVB Bill of Material ROHS Compliant	NCV8537MN500GEVB_BOM_ROHS.PDF - 573.0 KB	0
Eval Board: Gerber	NCV8537MN500GEVB Gerber Layout Files (Zip Format)	NCV8537MN500GEVB_GERBER.ZIP - 52.0 KB	0
Eval Board: Schematic	NCV8537MN500GEVB Schematic	NCV8537MN500GEVB_SCHEMATIC.PDF - 562.0 KB	0
Eval Board: Test Procedure	NCV8537MN500GEVB Test Procedure	NCV8537MN500GEVB_TEST_PROCEDURE.PDF - 1186.0 KB	0
Video	5.0 V LDO Regulator Evaluation Board - NCV8537MN500GEVB	WVD17606/D	

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