BFP460

Surface mount wideband silicon NPN RF bipolar transistor

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Product description

The BFP460 is a low noise device that is part of Infineon's established fourth generation RF bipolar transistor family. Its transition frequency f_T of 22 GHz, low current and high robustness characteristics make the device suitable for amplifiers. It remains cost competitive without compromising on ease of use.

Feature list

- Minimum noise figure NF_{min} = 1.1 dB at 1.8 GHz, 3 V, 5 mA
- High gain G_{ms} = 17.5 dB at 1.8 GHz, 3 V, 20 mA
- OIP_3 = 27.5 dBm at 1.8 GHz, 3 V, 20 mA
- High ESD robustness, typical 1.5 kV (HBM)

Product validation

Qualified for industrial applications according to the relevant tests of JEDEC JESD47, JESD22, and J-STD-020.
Qualified for industrial applications according to the relevant tests of AEC-Q 101.

Potential applications

- Amplifier for remote keyless entry (RKE)
- Broadband low noise amplifiers (LNAs) for CATV, DVB-T, DAB/DMB and FM/AM radio
- LNAs for sub-1 GHz ISM band applications

Device information

Table 1 Part information

Product name / Ordering code	Package	Pin configuration				Marking	Pieces / Reel
BFP460 / BFP460H6327XTSA1	SOT343	1 = E	2 = C	3 = E	4 = B	ABs	3000
BFP460 / BFP460H6433XTMA1							10000

***Attention*: ESD (Electrostatic discharge) sensitive device, observe handling precautions**

Table of contents

1 Absolute maximum ratings

Table 2 Absolute maximum ratings at T_A = 25 °C (unless otherwise specified)

Parameter	Symbol	Values		Unit	Note or test condition
		Min.	Max.		
Collector emitter voltage	V_{CEO}	–	4.5	V	Open base
			4.2		T_A = -55 °C, open base
Collector emitter voltage	V_{CES}		15		E-B short circuited
Collector base voltage	V_{CBO}		15		Open emitter
Emitter base voltage	V_{EBO}		1.5		Open collector
Base current	I_B		7	mA	–
Collector current	I_C		70		
Total power dissipation [1)]	P_{tot}		230	mW	$T_S \leq 92°C$
Junction temperature	T_J		150	°C	–
Ambient temperature	T_A	-55			
Storage temperature	T_{Stg}				

Attention**: **Stresses above the max. values listed here may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect device reliability. Exceeding only one of these values may cause irreversible damage to the integrated circuit.

[1] T_S is the soldering point temperature. T_S is measured on the collector lead at the soldering point of the PCB.

2 Thermal characteristics

Table 3 Thermal resistance

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Typ.	Max.		
Junction - soldering point	R_{thJS}	–	250	–	K/W	–

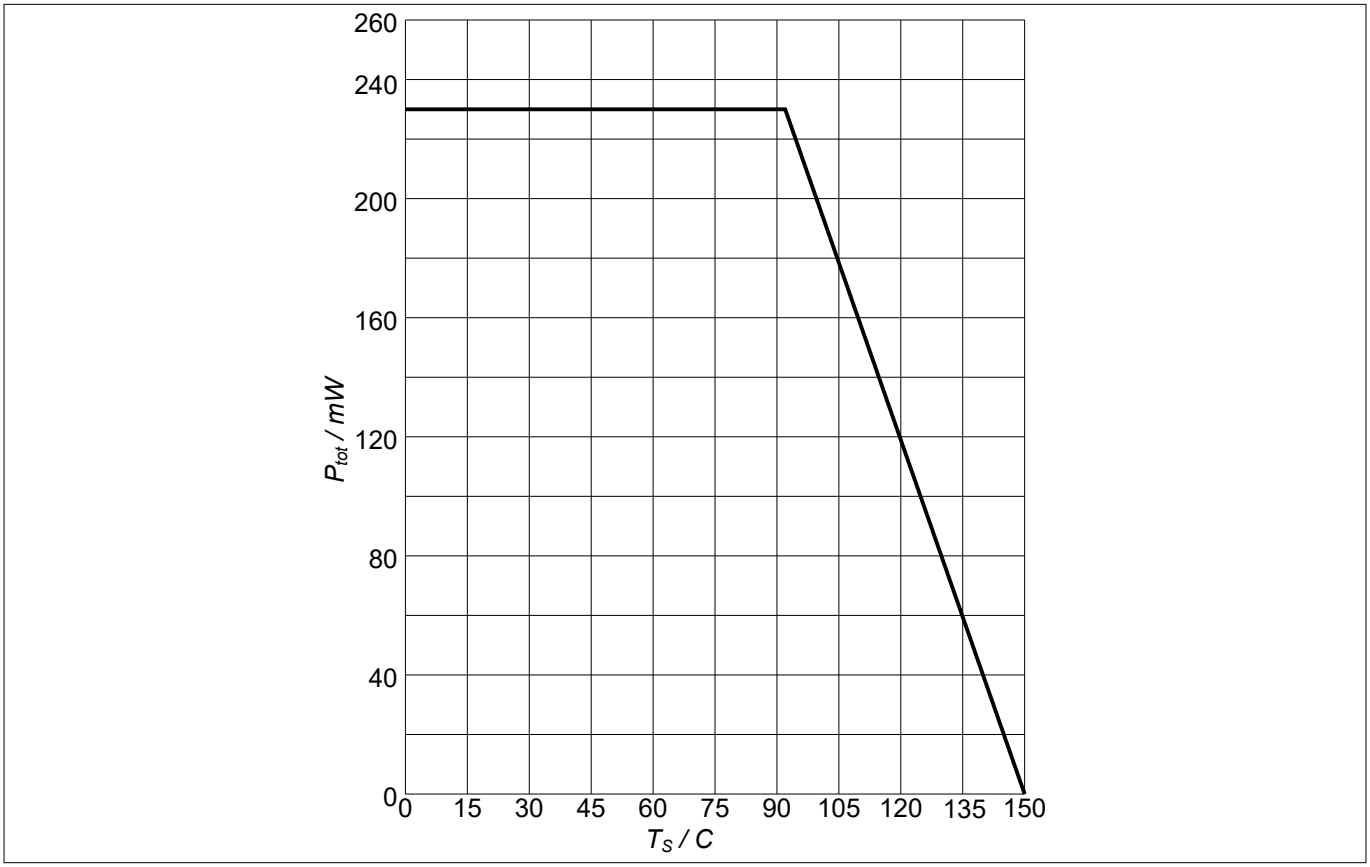


Figure 1 Total power dissipation P_{tot} = f(T_s)

3 Electrical characteristics

3.1 DC characteristics

Table 4 DC characteristics at T_A = 25 °C

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Typ.	Max.		
Collector emitter breakdown voltage	$V_{(BR)CEO}$	4.5	5.8	–	V	I_C = 1 mA, I_B = 0, open base
Collector emitter leakage current	I_{CES}	–	– 1 2	1000 [2] 30 [2] 40 [2]	nA	V_{CE} = 15 V, V_{BE} = 0, V_{CE} = 2 V, V_{BE} = 0, V_{CE} = 5 V, V_{BE} = 0, T_A = 85 °C [3] E-B short circuited
Collector base leakage current	I_{CBO}		1 –	30 [2] 30 [2]		V_{CB} = 2 V, I_E = 0, V_{CB} = 5 V, I_E = 0, open emitter
Emitter base leakage current	I_{EBO}		1	500 [2]		V_{EB} = 0.5 V, I_C = 0, open collector
DC current gain	h_{FE}	90	120	160		V_{CE} = 3 V, I_C = 20 mA, pulse measured

3.2 General AC characteristics

Table 5 General AC characteristics at T_A = 25 °C

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Typ.	Max.		
Transition frequency	f_T	16	22	–	GHz	V_{CE} = 3 V, I_C = 30 mA, f = 1 GHz
Collector base capacitance	C_{CB}	–	0.32	0.45	pF	V_{CB} = 3 V, V_{BE} = 0, f = 1 MHz, emitter grounded
Collector emitter capacitance	C_{CE}		0.28	–		V_{CE} = 3 V, V_{BE} = 0, f = 1 MHz, base grounded
Emitter base capacitance	C_{EB}		0.55			V_{EB} = 0.5 V, V_{CB} = 0, f = 1 MHz, collector grounded

2 Maximum values not limited by the device but by the short cycle time of the 100% test.
3 Verified by random sampling

3.3 Frequency dependent AC characteristics

Measurement setup is a test fixture with Bias T's in a 50 Ω system, T_A = 25 °C.

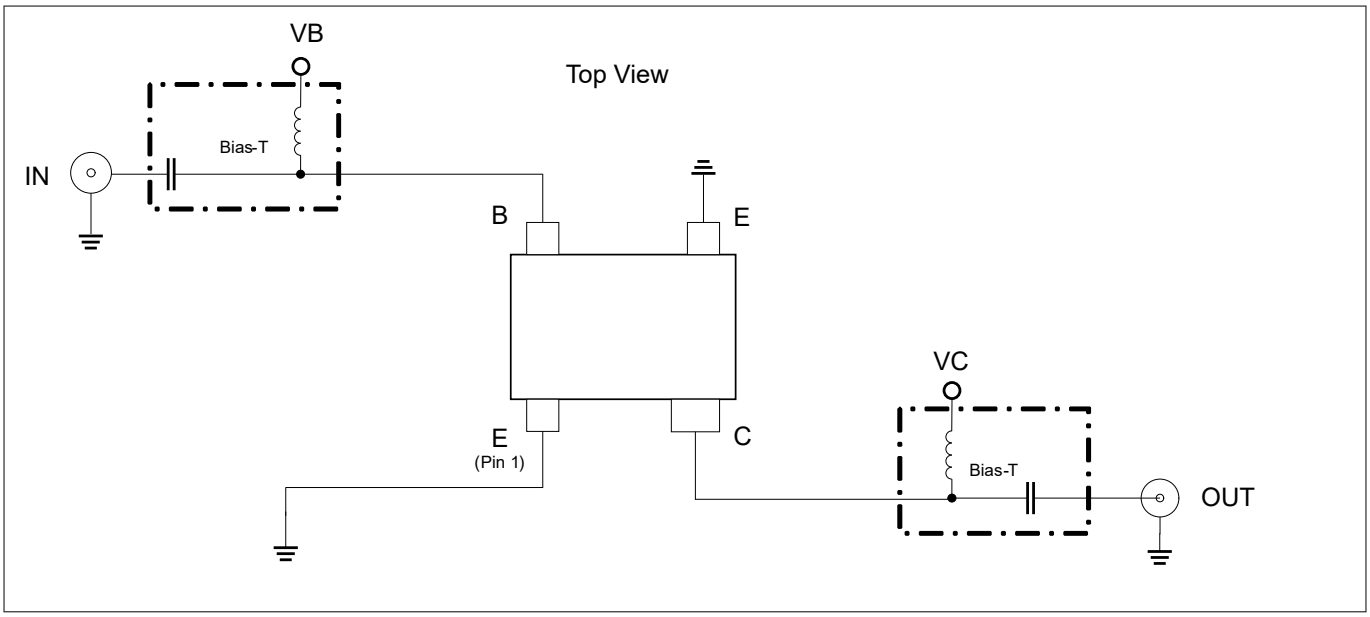


Figure 2 Testing circuit

Table 6 AC characteristics, f = 100 MHz

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Typ.	Max.		
Power gain		–		–	dB	
• Maximum power gain	G_{ms}		26.5			V_{CE} = 1.5 V, I_C = 3 mA
• Transducer gain	$\|S_{21}\|^2$		20			
Noise figure						
• Minimum noise figure	NF_{min}		0.7			V_{CE} = 2 V, I_C = 3 mA
Linearity					dBm	
• 3rd order intercept point at output	OIP_3		23.5			V_{CE} = 3 V, I_C = 20 mA,
• 1 dB compression point at output	OP_{1dB}		9.5			$Z_S = Z_L$ = 50 Ω

Table 7 AC characteristics, V_{CE} = 3 V, f = 1.8 GHz

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Typ.	Max.		
Power gain		–		–	dB	
• Maximum power gain	G_{ms}		17.5			I_C = 20 mA
• Transducer gain	$\|S_{21}\|^2$		15			
Noise figure						
• Minimum noise figure	NF_{min}		1.1			I_C = 5 mA

(table continues...)

Table 7 (continued) AC characteristics, V_{CE} = 3 V, f = 1.8 GHz

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Typ.	Max.		
Linearity					dBm	$Z_S = Z_L$ = 50 Ω,
• 3rd order intercept point at output	OIP_3		27.5			I_C = 20 mA,
• 1 dB compression point at output	OP_{1dB}		11.5			I_C = 20 mA,
			13			I_C = 35 mA

Table 8 AC characteristics, V_{CE} = 3 V, f = 3 GHz

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Typ.	Max.		
Power gain		–		–	dB	
• Maximum power gain	G_{ma}		12.5			I_C = 20 mA
• Transducer gain	$\|S_{21}\|^2$		10.5			
Noise figure						
• Minimum noise figure	NF_{min}		1.2			I_C = 5 mA

***Note**: $G_{ms} = |S_{21} / S_{12}|$ for $k < 1$; $G_{ma} = |S_{21} / S_{12}|(k-(k^2-1)^{1/2})$ for $k > 1$. In order to get the NF_{min} values stated in this chapter, the test fixture losses have been subtracted from all measured results. OIP_3 value depends on termination of all intermodulation frequency components. Termination used for this measurement is 50 Ω from 0.1 MHz to 6 GHz.*

3.4 Characteristic AC diagrams

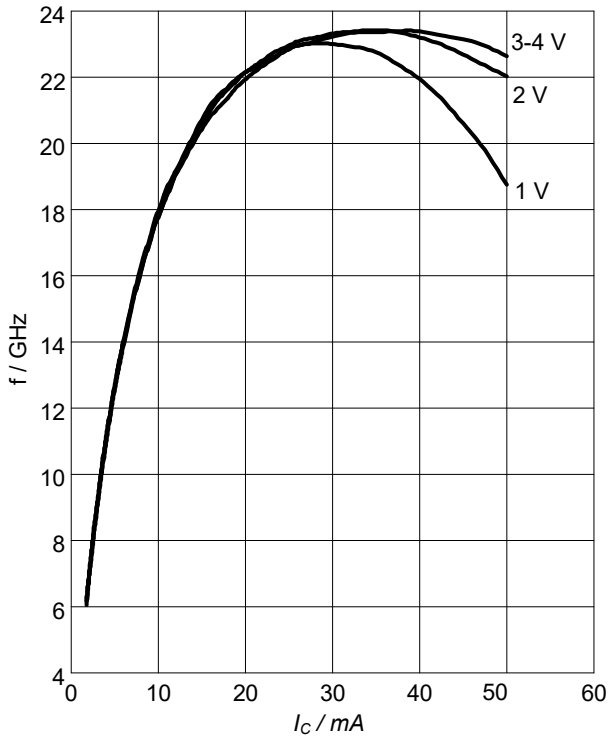


Figure 3 **Transition frequency f_T = f(I_C), f = 1 GHz, V_{CE} = parameter**

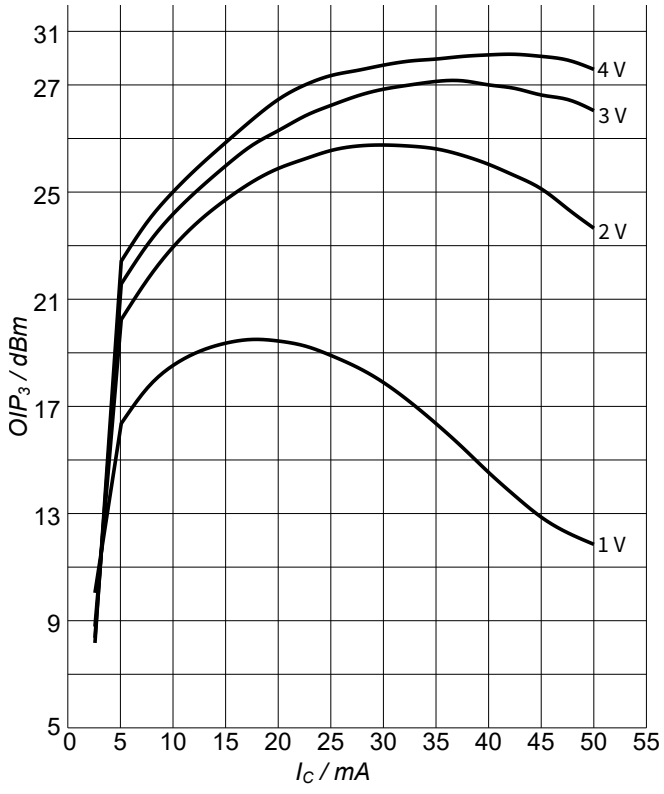


Figure 4 **3rd order intercept point OIP_3 = f(I_C), Z_S = Z_L = 50 Ω, f = 1.8 GHz, V_{CE} = parameter**

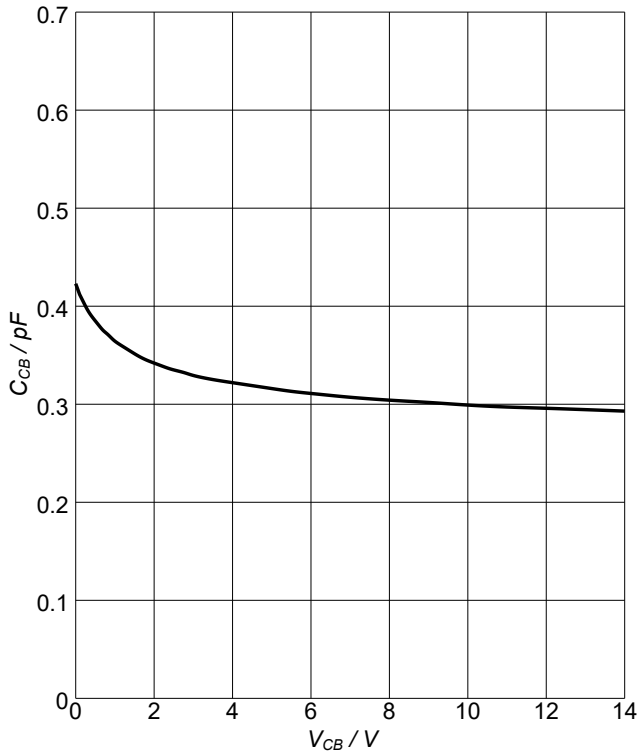


Figure 5 **Collector base capacitance $C_{CB} = f(V_{CB})$, $f = 1$ MHz**

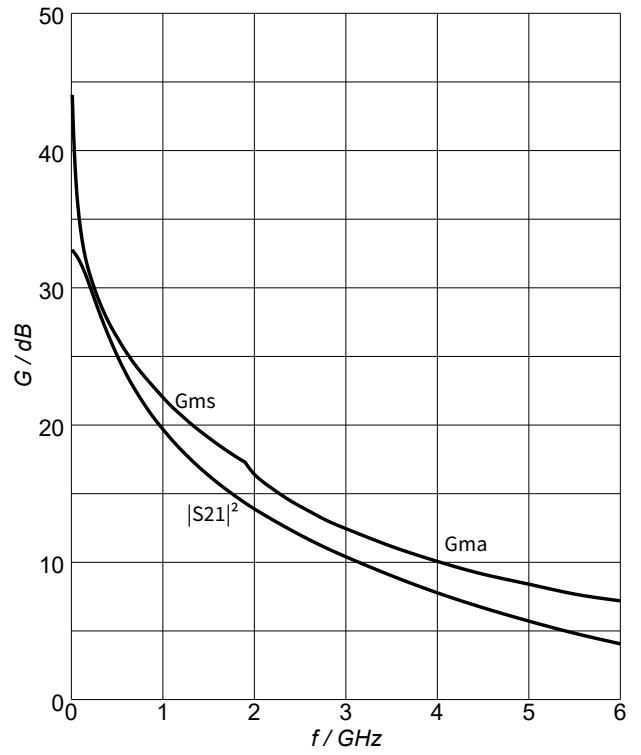


Figure 6 **Gain G_{ma}, G_{ms}, $|S_{21}|^2 = f(f)$, $V_{CE} = 3$ V, $I_C = 20$ mA**

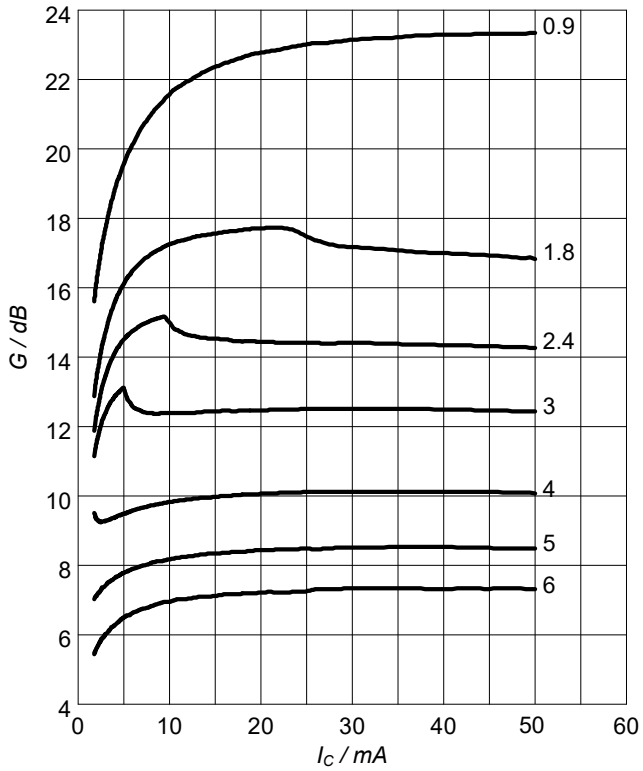


Figure 7 **Maximum power gain G_{max} = f(I_C), V_{CE} = 3 V, f = parameter in GHz**

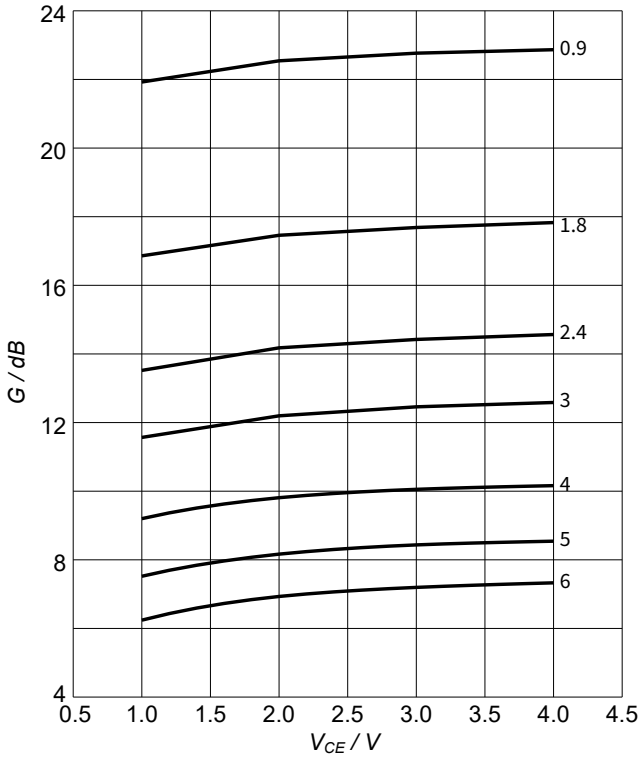


Figure 8 **Maximum power gain G_{max} = f(V_{CE}), I_C = 20 mA, f = parameter in GHz**

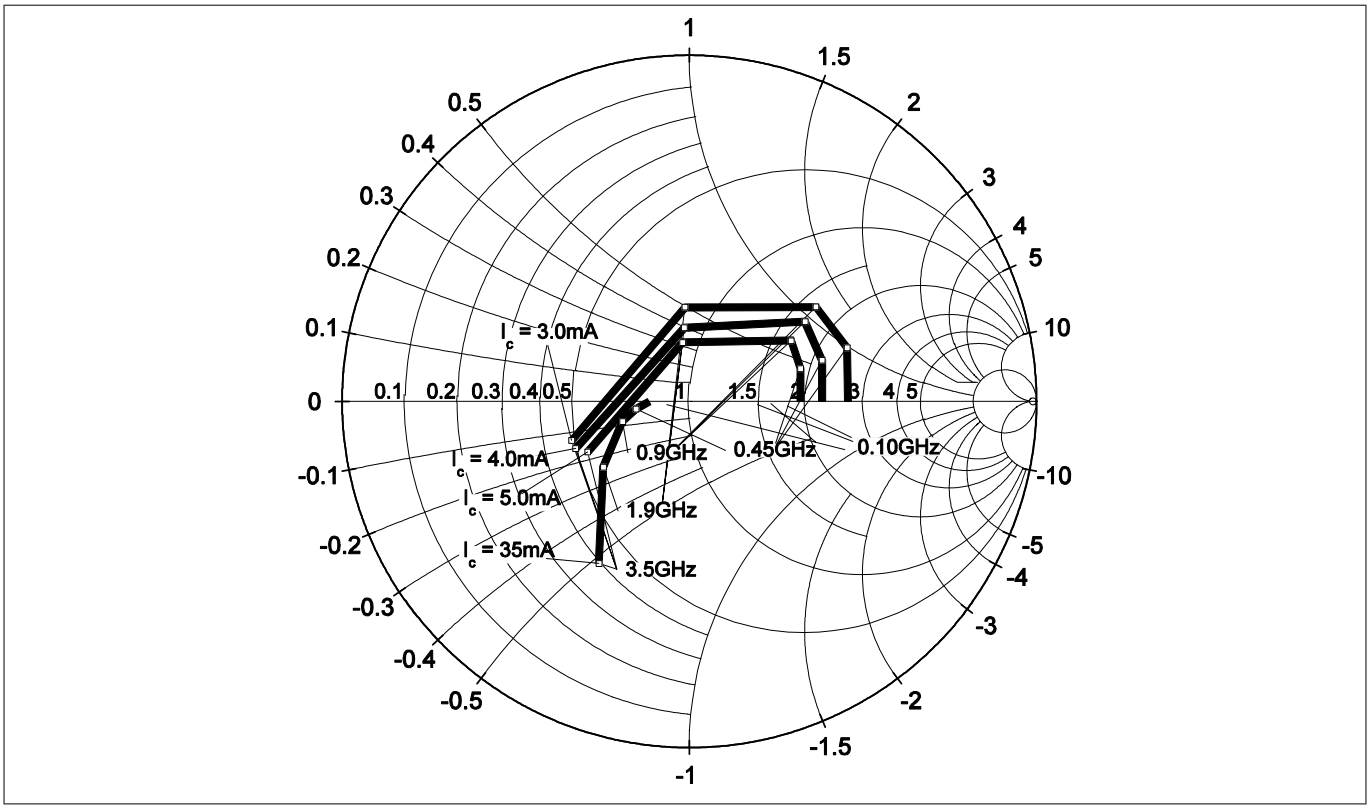


Figure 9 **Source impedance for minimum noise figure $Z_{S,opt}$ = f(f) , V_{CE} = 2 V, I_C = parameter**

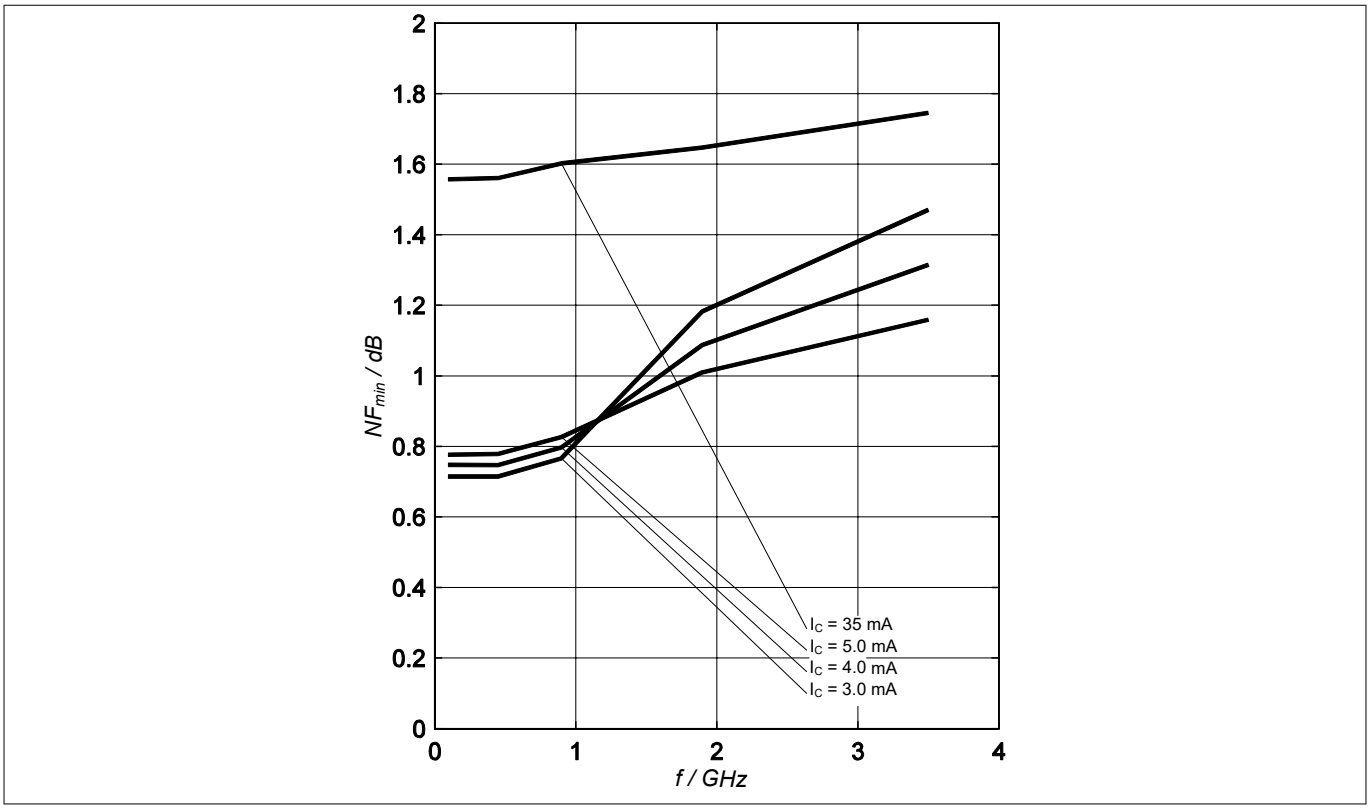


Figure 10 **Noise figure NF_{min} = f(f), V_{CE} = 2 V, Z_S = $Z_{S,opt}$, I_C = parameter**

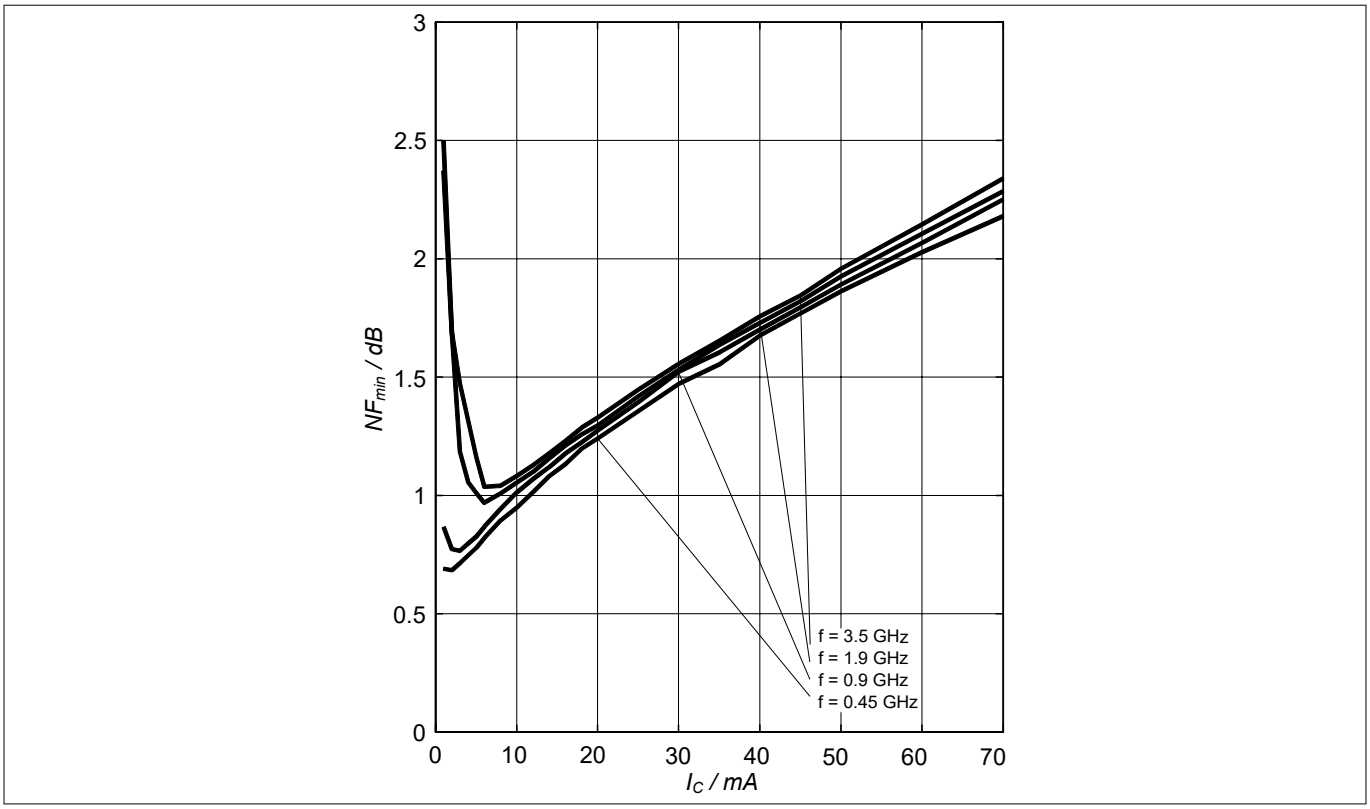
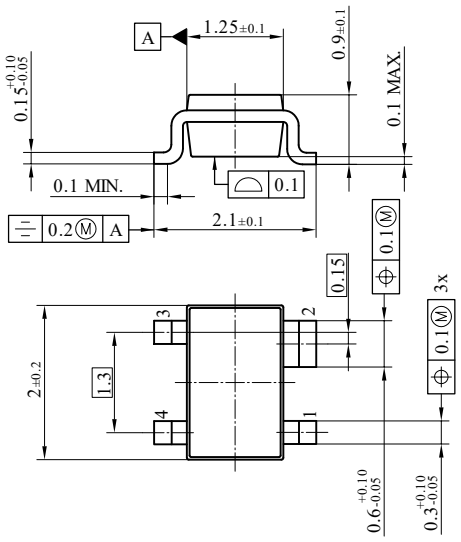


Figure 11 Noise figure NF_{min} = f(I_C), V_{CE} = 2 V, Z_S = $Z_{S,opt}$, f = parameter in GHz

***Note**: The curves shown in this chapter have been generated using typical devices but shall not be considered as a guarantee that all devices have identical characteristic curves. T_A = 25 °C.*

4 Package information SOT343



MOLD FLASH, PROTRUSION OR GATE BURRS OF 0.2 MM MAXIMUM PER SIDE ARE NOT INCLUDED
ALL DIMENSIONS ARE IN UNITS MM
THE DRAWING IS IN COMPLIANCE WITH ISO 128 & PROJECTION METHOD 1 [ ]

Figure 12 SOT343 package

***Note**: For package information including footprint, packing and assembly recommendation refer to:*

https://www.infineon.com/cms/en/product/packages/PG-SOT343/PG-SOT343-4-1

Revision history

Document version	Date of release	Description of changes
Revision 2.0	2019-01-25	New datasheet layout, typical curve removed.
Revision 2.1	2023-04-17	Updated product description and AC testing circuit figure.
Revision 2.2	2023-06-20	Updated figures writing style.
Revision 2.3	2024-07-01	Updated product validation

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