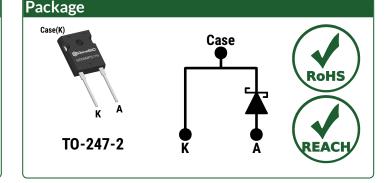
Silicon Carbide Schottky Diode



V _{RRM} =	1700 V
$I_{F(T_{c} = 164^{\circ}C)} =$	5 A
$I_{F(T_{c} = 164^{\circ}C)} = Q_{C} =$	41 nC

Features

- Gen4 Thin Chip Technology for Low V_F
- Enhanced Surge and Avalanche Robustness
- Superior Figure of Merit Q_C/I_F
- Low Thermal Resistance
- Low Reverse Leakage Current
- Temperature Independent Fast Switching
- Positive Temperature Coefficient of V_F
- Low V_F for High Temperature Operation



Advantages

- Improved System Efficiency
- High System Reliability
- Optimal Price Performance
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- Easy to Parallel without Thermal Runaway
- Improved System Efficiency

Applications

- EV Fast Chargers
- Solar Inverters
- Anti-Parallel / Free-Wheeling Diode
- Motor Drives
- High Frequency Rectifiers
- Switched Mode Power Supply (SMPS)
- Induction Heating and Welding
- Medical Imaging

Absolute Maximum Ratings (At T_c = 25°C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	Note
Repetitive Peak Reverse Voltage	V _{RRM}		1700	V	
		T _C = 100°C, D = 1	15		
Continuous Forward Current	IF	T _C = 135°C, D = 1	10	А	Fig. 4
		T _C = 164°C, D = 1	5		
Non-Repetitive Peak Forward Surge Current, Half Sine	I =	T _C = 25°C, t _P = 10 ms	50		
Wave	lf,sm	T _C = 150°C, t _P = 10 ms	40	A	
Repetitive Peak Forward Surge Current, Half Sine Wave		T _C = 25°C, t _P = 10 ms	30	Α	
Repetitive Feak Forward Surge Current, Hall Sille Wave	I _{F,RM}	T _C = 150°C, t _P = 10 ms	21	A	
Non-Repetitive Peak Forward Surge Current	I _{F,MAX}	T _C = 25°C, t _P = 10 μs	250	Α	
i ² t Value	∫i²dt	T _C = 25°C, t _P = 10 ms	12	A ² s	
Non-Repetitive Avalanche Energy	E _{AS}	L = 8 mH, I _{AS} = 5 A	100	mJ	
Diode Ruggedness	dV/dt	V _R = 0 ~ 1360 V	200	V/ns	
Power Dissipation	Ртот	T _C = 25°C	141	W	Fig. 3
Operating and Storage Temperature	Tj, Tstg		-55 to 175	°C	





Electrical Characteristics

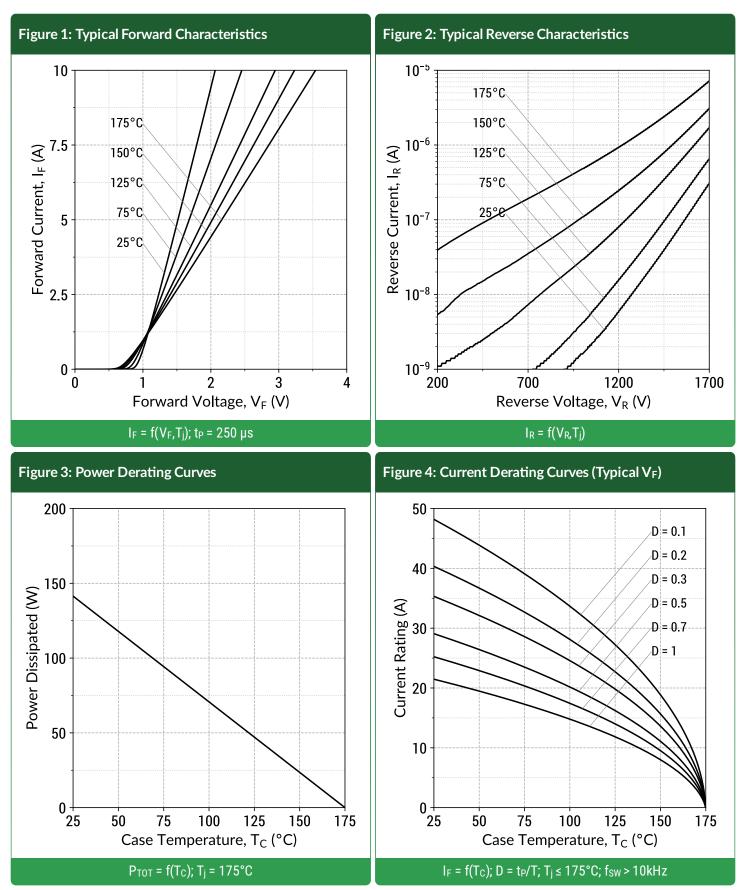
Deremeter	Symbol	Conditions -		Values		11	Note	
Parameter	Symbol			Min.	Тур.	Max.	Unit	Note
Diada Farward Valtaga	VF	I _F = 5 A, T _j = 25°C			1.5	1.8	۷	Fig. 1
Diode Forward Voltage	VF	I _F = 5 A, T _j = 175°C			2.1			
Reverse Current	la la	V _R = 1700 V, T _j = 25°C			1	20	μA	Fig. 2
	IR	V _R = 1700 V, T _j = 175°C			4			
Total Capacitive Charge	Qc		V _R = 600 V		28		nC	Fig. 7
	Qu	$dI_F/dt = 200 A/\mu s$ $V_R = 6$	V _R = 1200 V		41			
Switching Time	+-		V _R = 600 V		< 10		20	
	ts		V _R = 1200 V				ns	
Tatal Canaditanaa	С	V _R = 1 V, f :	= 1MHz		361		ъĘ	Fig. 6
Total Capacitance	U U	V _R = 1200 V, f = 1MHz			20	pF		Fig. 6

Thermal/Package Characteristics

Parameter	Svmbol	Conditions	Values			11	Noto
arameter	Symbol	Min. Typ. Max.	Max.	Unit	Note		
Thermal Resistance, Junction - Case	RthJC			1.06		°C/W	Fig. 9
Weight	WT			6.0		g	
Mounting Torque	Τ _M	Screws to Heatsink			1.1	Nm	

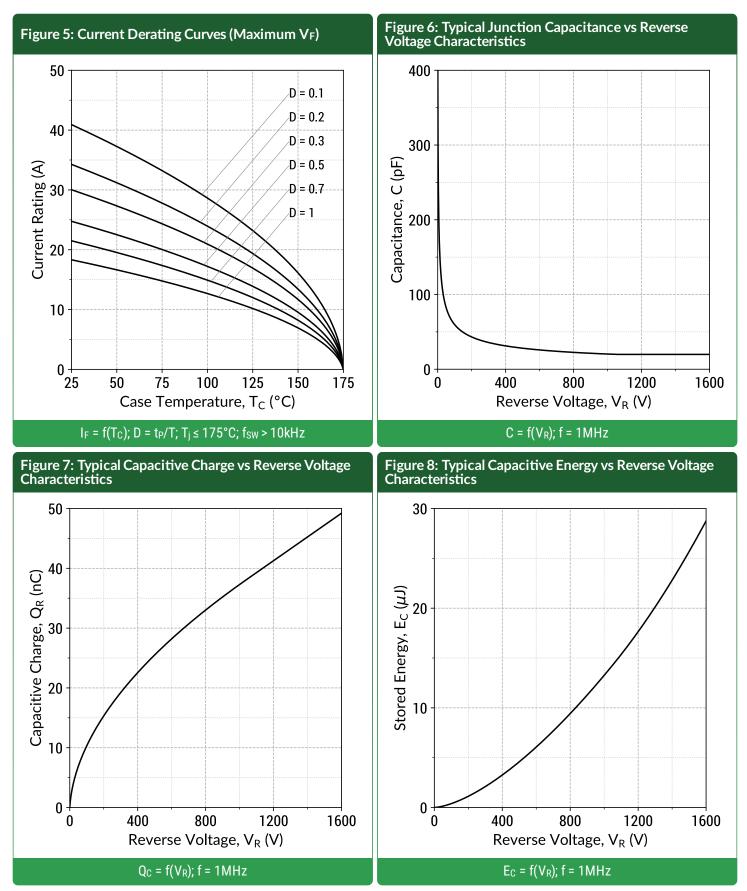




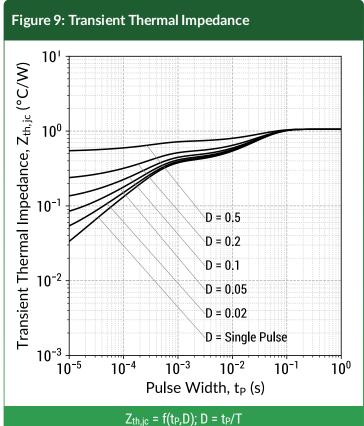


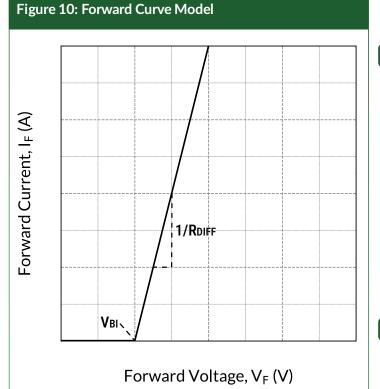
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 $I_F = f(V_F, T_j)$

Forward Curve Model Equation:

 $I_F = (V_F - V_{BI})/R_{DIFF} (A)$

Built-In Voltage (V_{BI}):

 $V_{BI}(T_j) = m \times T_j + n (V)$ m = -0.00126 (V/°C) n = 0.997 (V)

Differential Resistance (RDIFF):

 $\begin{aligned} & \mathsf{R}_{\mathsf{DIFF}}(\mathsf{T}_j) = \mathbf{a} \times \mathsf{T}_j^2 + \mathbf{b} \times \mathsf{T}_j + \mathbf{c} \ (\Omega) \\ & \mathbf{a} = 2.04\text{e-}06 \ (\Omega/^\circ\text{C}^2) \\ & \mathbf{b} = 0.000704 \ (\Omega/^\circ\text{C}) \\ & \mathbf{c} = 0.0912 \ (\Omega) \end{aligned}$

Forward Power Loss Equation:

 $P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$

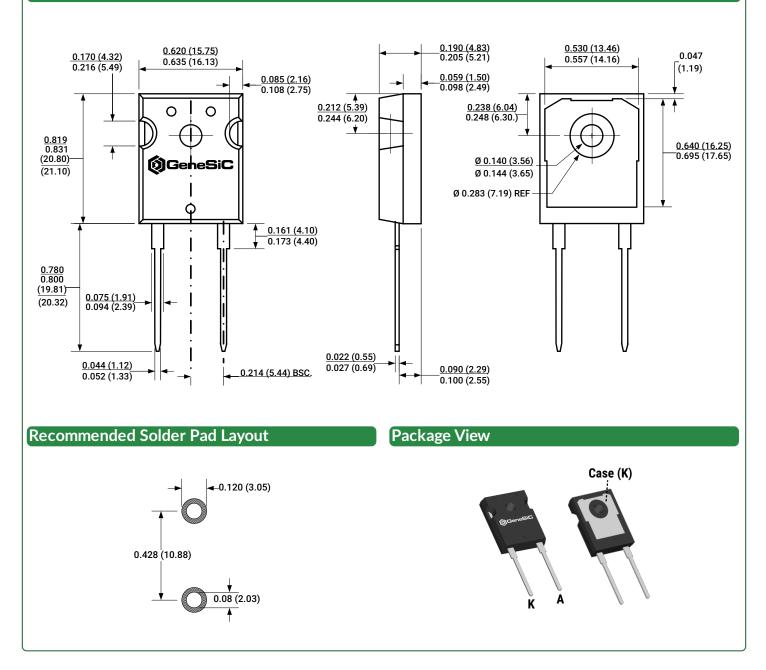


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Package Dimensions

TO-247-2 Package Outline



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.



GD05MPS17H 1700V 5A SiC Schottky MPS™ Diode



Compliance

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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Related Links

SPICE Models:	https://www.genesicsemi.	com/sic-schottky-mps/GD05	MPS17H/GD05MPS17H_SPICE.zip
	- J		

- PLECS Models: https://www.genesicsemi.com/sic-schottky-mps/GD05MPS17H/GD05MPS17H_PLECS.zip
- CAD Models: https://www.genesicsemi.com/sic-schottky-mps/GD05MPS17H/GD05MPS17H_3D.zip
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- Reliability: https://www.genesicsemi.com/reliability
- Compliance: https://www.genesicsemi.com/compliance
- Quality Manual: https://www.genesicsemi.com/quality

Revision History

- Rev 21/Jun: Updated with most recent test data
- Supersedes: Rev 20/Jul



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