

Features

- 16-Mbit SRAM
- 1-Gbyte or more microSD™ card
- Boot from user Flash, system memory or SRAM
- Both ISO/IEC 14443 type A and B smartcard support
- I²C compatible 64-Kbit serial interface EEPROM, ST MEMS and I/O expander
- IEEE 802.3-2002 compliant Ethernet connector
- Two CAN 2.0 A/B channels on the same DB connector
- RS-232 communication
- IrDA transceiver (up to board version MB786 C-07)
- USB OTG (HS and FS) with Micro-AB connector
- Inductor motor control connector
- I²S audio DAC, stereo audio jack for headset
- 3.2" 240x320 TFT color LCD with touch screen
- 4 colored LEDs
- Camera module and extension connector for ST camera plug-in
- Joystick with 4-direction control and selector
- Reset, wakeup, tamper and user button
- RTC with backup battery
- Extension connector for daughterboard or wrapping board
- JTAG, SW and trace debug support
- Embedded ST-LINK/V2
- Five 5V power supply options: power jack, USB FS connector, USB HS connector, ST-LINK/V2 or daughterboard



1. Picture is not contractual.

Description

The STM3221G-EVAL evaluation board is a complete demonstration and development platform for the STM32F2 Series and includes an STM32F217IGH6 high-performance ARM®Cortex®-M3 32-bit microcontroller with a cryptographic acceleration cell.

The full range of hardware features on the board helps users to evaluate all the peripherals (USB OTG HS, USB OTG FS, Ethernet, motor control, CAN, microSD™ card, smartcard, USART, Audio DAC, RS-232, IrDA transceiver up to board version MB786 C-07, SRAM, ST MEMS, EEPROM, and others) and to develop applications.

The embedded in-circuit ST-LINK/V2 provides debugger and programmer facilities for the STM32.

1 System requirements

- Windows® OS (XP, 7, 8)
- USB type A to type B or Mini-B cable

2 Development toolchains

- IAR EWARM (IAR Embedded Workbench®)
- Keil® MDK-ARM™
- GCC-based IDEs (free AC6: SW4STM32, Atollic® TrueSTUDIO®,...)

3 Demonstration software

Demonstration software is preloaded in the board-mounted Flash memory for easy demonstration of the device peripherals in standalone mode. For more information and to download the latest version, refer to STM3221G-EVAL demonstration software at the www.st.com website.

4 Ordering information

To order the STM3221G-EVAL evaluation board, refer to [Table 1](#):

Table 1. Ordering information

Order code	Target STM32
STM3221G-EVAL	STM32F217IGH6

5 Revision history

Table 2. Document revision history

Date	Revision	Changes
24-Mar-2011	1	Initial release.
2-Nov-2012	2	Added information in the description about the cryptographic acceleration cell. Changed the “STM32 F-2 series” in the description to “F2”.
03-Nov-2016	3	Updated IrDA transceiver version in Features and Description . Added sections: Section 1: System requirements , Section 2: Development toolchains

IMPORTANT NOTICE – PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2016 STMicroelectronics – All rights reserved