MMBT489LT1G

High Current Surface Mount NPN Silicon Switching Transistor for Load Management in Portable Applications

Features

 These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Rating	Symbol	Max	Unit
Collector-Emitter Voltage	V_{CEO}	30	Vdc
Collector-Base Voltage	V_{CBO}	50	Vdc
Emitter-Base Voltage	V_{EBO}	5.0	Vdc
Collector Current – Continuous	I _C	1.0	Α
Collector Current – Peak	I _{CM}	2.0	Α

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation (Note 1) @T _A = 25°C Derate above 25°C	P _D	310 2.5	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	403	°C/W
Total Device Dissipation (Note 2) @T _A = 25°C Derate above 25°C	P _D	710 5.7	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	176	°C/W
Total Device Dissipation (Single Pulse < 10 s)	P _{Dsingle}	575	mW
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

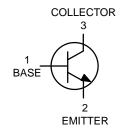
- 1. FR-4 @ Minimum Pad
- 2. FR-4 @ 1.0 X 1.0 inch Pad



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30 VOLTS, 2.0 AMPERES NPN TRANSISTOR





SOT-23 (TO-236) CASE 318 STYLE 6

MARKING DIAGRAM



N3 = Specific Device Code

M = Date Code*

■ = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBT489LT1G	SOT-23 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MMBT489LT1G

$\textbf{ELECTRICAL CHARACTERISTICS} \ (T_A = 25^{\circ}C \ unless \ otherwise \ noted)$

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS		•	•	
Collector – Emitter Breakdown Voltage (I _C = 10 mAdc, I _B = 0)	V _{(BR)CEO}	30	-	Vdc
Collector-Base Breakdown Voltage (I _C = 0.1 mAdc, I _E = 0)	V _{(BR)CBO}	50	_	Vdc
Emitter – Base Breakdown Voltage ($I_E = 0.1 \text{ mAdc}, I_C = 0$)	V _{(BR)EBO}	5.0	_	Vdc
Collector Cutoff Current (V _{CB} = 30 Vdc, I _E = 0)	I _{CBO}	-	0.1	μAdc
Collector–Emitter Cutoff Current (V _{CES} = 30 Vdc)	I _{CES}	-	0.1	μAdc
Emitter Cutoff Current (V _{EB} = 4.0 Vdc)	I _{EBO}	-	0.1	μAdc
ON CHARACTERISTICS		•	•	
DC Current Gain (Note 3) (I _C = 50 mA, V _{CE} = 5.0 V) (I _C = 0.5 A, V _{CE} = 5.0 V) (I _C = 1.0 A, V _{CE} = 5.0 V)	h _{FE}	300 300 200	900	
Collector – Emitter Saturation Voltage (Note 3) ($I_C = 1.0 \text{ A}, I_B = 100 \text{ mA}$) ($I_C = 0.5 \text{ A}, I_B = 50 \text{ mA}$) ($I_C = 0.1 \text{ A}, I_B = 1.0 \text{ mA}$)	V _{CE(sat)}	- - -	0.200 0.125 0.075	V
Base – Emitter Saturation Voltage (Note 3) (I _C = 1.0 A, I _B = 0.1 A)	V _{BE(sat)}	-	1.1	V
Base – Emitter Turn–on Voltage (Note 3) $(I_C = 1.0 \text{ mA}, V_{CE} = 2.0 \text{ V})$	V _{BE(on)}	-	1.1	V
Cutoff Frequency ($I_C = 100 \text{ mA}$, $V_{CE} = 5.0 \text{ V}$, $f = 100 \text{ MHz}$	f _T	100	-	MHz
Output Capacitance (f = 1.0 MHz)	C _{obo}	_	15	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

^{3.} Pulsed Condition: Pulse Width = 300 μsec, Duty Cycle ≤ 2%

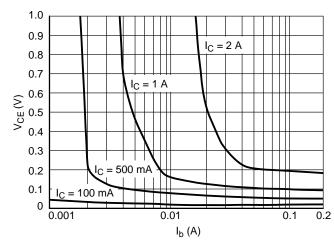


Figure 1. V_{CE} versus I_b

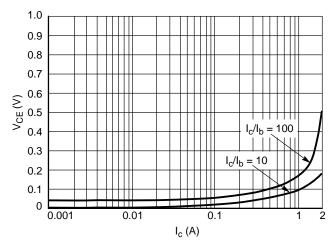


Figure 2. V_{CE} versus I_c

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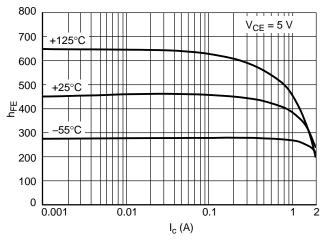


Figure 3. h_{FE} versus I_c

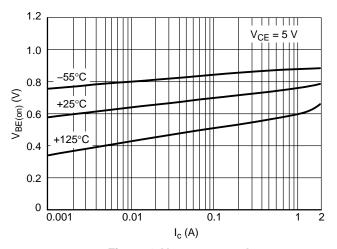


Figure 4. $V_{BE(on)}$ versus I_c

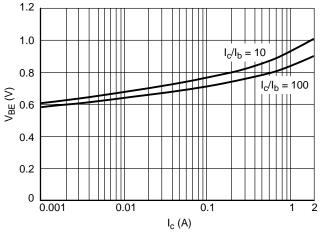


Figure 5. $V_{\text{BE(sat)}}$ versus I_{c}

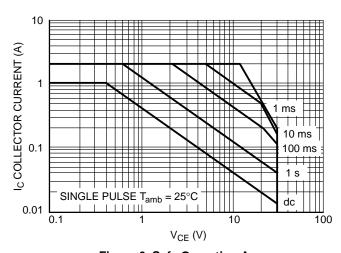


Figure 6. Safe Operating Area

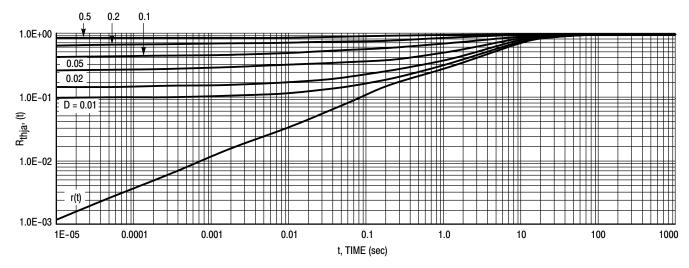


Figure 7. Normalized Thermal Response

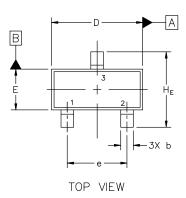


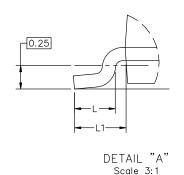


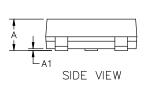
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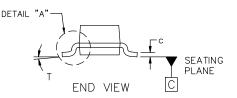
DATE 14 AUG 2024

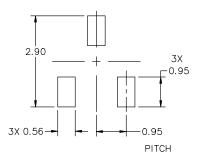
MAX











Α 0.89 1.00 1.11 Α1 0.01 0.06 0.10 0.37 0.50 b 0.44 0.08 0.20 0.14 С D 2.80 2.90 3.04 Ε 1.20 1.30 1.40 1.78 1.90 2.04 е L 0.30 0.43 0.55 L1 0.35 0.54 0.69 HE 2.10 2.40 2.64 Τ O° ___ 10°

MILLIMETERS

MIN

NOM

NOTES:

DIM

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M. 2018.
- PER ASME Y14.5M, 2018.
 2. CONTROLLING DIMENSIONS:
 MILLIMETERS.
- 3. MAXIMUM LEAD THICKNESS
 INCLUDES LEAD FINISH. MINIMUM
 LEAD THICKNESS IS THE
 MINIMUM THICKNESS OF THE
 BASE MATERIAL
- BASE MATERIAL.

 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

M = Date Code

■ = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

RECOMMENDED MOUNTING FOOTPRINT

* For additional information on our Pb—Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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DATE 14 AUG 2024

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR		NODE D CONNECTION ATHODE	
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	STYLE 11: STYLE 12: PIN 1. ANODE PIN 1. CA 2. CATHODE 2. CA 3. CATHODE-ANODE 3. AN	ATHODE PIN 1. SOURCE ATHODE 2. DRAIN	STYLE 14: PIN 1. CATHODE 2. GATE 3. ANODE
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE			STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT	STYLE 23: STYLE 24: PIN 1. ANODE PIN 1. GAT 2. ANODE 2. DR/ 3. CATHODE 3. SOU	TE PIN 1. ANODE AIN 2. CATHODE	STYLE 26: PIN 1. CATHODE 2. ANODE 3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE			

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