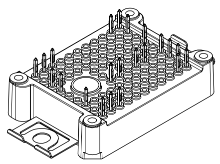
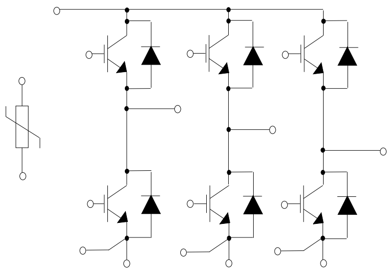
Datasheet

ACEPACK™ 1 sixpack topology, 650 V, 50 A trench gate field-stop IGBT M series, soft diode and NTC



ACEPACK™ 1



Features

- ACEPACK™ 1 power module
  - DBC Cu Al_2O_3 Cu
- Sixpack topology
  - 650 V, 50 A IGBTs and diodes
  - Soft and fast recovery diode
- Integrated NTC

Applications

- Inverters
- Industrial
- Motor drives

Description

This power module is a sixpack topology in an ACEPACK™ 1 package with NTC, integrating the advanced trench gate field-stop technologies from STMicroelectronics. This new IGBT technology represents the best compromise between conduction and switching loss, to maximize the efficiency of any converter system up to 20 kHz.

Product status
A1P50S65M2-F

Product summary	
Order code	A1P50S65M2-F
Marking	A1P50S65M2-F
Package	ACEPACK™ 1
Leads type	Press fit contact pins

1 Electrical ratings

1.1 IGBT

Limiting values at T_j = 25 °C, unless otherwise specified.

Table 1. Absolute maximum ratings of the IGBT

Symbol	Parameter	Value	Unit
V_{CES}	Collector-emitter voltage (V_{GE} = 0)	650	V
I_C	Continuous collector current (T_c = 100 °C)	50	A
I_{CP}(1)	Pulsed collector current (t_p = 1 ms)	100	A
V_{GE}	Gate-emitter voltage	±20	V
P_{TOT}	Total power dissipation of each IGBT (T_C = 25 °C, T_J = 175 °C)	208	W
T_{JMAX}	Maximum junction temperature	175	°C
T_{Jop}	Operating junction temperature range under switching conditions	-40 to 150	°C

1. *Pulse width limited by maximum junction temperature.*

Table 2. Electrical characteristics of the IGBT

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)CES}$	Collector-emitter breakdown voltage	I_C = 1 mA, V_{GE} = 0 V	650			V
$V_{CE(sat)}$ (terminal)	Collector-emitter saturation voltage	V_{GE} = 15 V, I_C= 50 A		1.95	2.3	V
		V_{GE} = 15 V, I_C = 50 A, T_J = 150 °C		2.3		
$V_{GE(th)}$	Gate threshold voltage	V_{CE} = V_{GE}, I_C = 1 mA	5	6	7	V
I_{CES}	Collector cut-off current	V_{GE} = 0 V, V_{CE} = 650 V			100	µA
I_{GES}	Gate-emitter leakage current	V_{CE} = 0 V, V_{GE} = ± 20 V			± 500	nA
C_{ies}	Input capacitance	V_{CE} = 25 V, f = 1 MHz, V_{GE} = 0 V		4150		pF
C_{oes}	Output capacitance			170		pF
C_{res}	Reverse transfer capacitance			80		pF
Q_g	Total gate charge	V_{CC} = 520 V, I_C = 50 A, V_{GE} = ±15 V		150		nC
$t_{d(on)}$	Turn-on delay time	V_{CC} = 300 V, I_C = 50 A, R_G = 6.8 Ω, V_{GE} = ±15 V, di/dt = 2400 A/µs		143		ns
t_r	Current rise time			16.5		ns
E_{on}(1)	Turn-on switching energy			0.140		mJ
$t_{d(off)}$	Turn-off delay time	V_{CC} = 300 V, I_C = 50 A, R_G = 6.8 Ω, V_{GE} = ±15 V, dv/dt = 7600 V/µs		112		ns
t_f	Current fall time			149		ns
E_{off}(2)	Turn-off switching energy			1.45		mJ

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	V_{CC} = 300 V, I_C = 50 A, R_G = 6.8 Ω, V_{GE} = ±15 V, di/dt = 2062 A/μs, T_J = 150 °C		148		ns
t_r	Current rise time			19.2		ns
E_{on}(1)	Turn-on switching energy			0.311		mJ
$t_{d(off)}$	Turn-off delay time	V_{CC} = 300 V, I_C = 50 A, R_G = 6.8 Ω, V_{GE} = ±15 V, dv/dt = 5800 V/μs, T_J = 150 °C		110		ns
t_f	Current fall time			221		ns
E_{off}(2)	Turn-off switching energy			1.98		mJ
t_{SC}	Short-circuit withstand time	$V_{CC} \leq$ 360 V, $V_{GE} \leq$ 15 V, $T_{Jstart} \leq$ 150 °C	6			μs
R_{THj-c}	Thermal resistance junction-to-case	Each IGBT		0.65	0.72	°C/W
R_{THc-h}	Thermal resistance case-to-heatsink	Each IGBT, λ_{grease} = 1 W/(m·°C)		0.79		°C/W

1. *Including the reverse recovery of the diode.*
2. *Including the tail of the collector current.*

1.2 Diode

Limiting values at T_j = 25 °C, unless otherwise specified.

Table 3. Absolute maximum ratings of the diode

Symbol	Parameter	Value	Unit
V_{RRM}	Repetitive peak reverse voltage	650	V
I_F	Continuous forward current at (T_C = 100 °C)	50	A
I_{FP}(1)	Pulsed forward current (t_p = 1 ms)	100	A
T_{JMAX}	Maximum junction temperature	175	°C
T_{Jop}	Operating junction temperature range under switching conditions	-40 to 150	°C

1. *Pulse width limited by maximum junction temperature.*

Table 4. Electrical characteristics of the diode

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_F (terminal)	Forward voltage	I_F = 50 A	-	1.85	2.65	V
		I_F = 50 A, T_J = 150 °C	-	1.65		
t_{rr}	Reverse recovery time	I_F = 50 A, V_R = 300 V, V_{GE} = ±15 V, di/dt = 2400 A/μs	-	142		ns
Q_{rr}	Reverse recovery charge		-	1.87		μC
I_{rrm}	Reverse recovery current		-	40		A
E_{rec}	Reverse recovery energy		-	0.41		mJ
t_{rr}	Reverse recovery time	I_F = 50 A, V_R = 300 V, V_{GE} = ±15 V, di/dt = 2062 A/μs, T_J = 150 °C	-	260		ns
Q_{rr}	Reverse recovery charge		-	5.2		μC
I_{rrm}	Reverse recovery current		-	58		A
E_{rec}	Reverse recovery energy		-	1.32		mJ

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$R_{THj\text{-}c}$	Thermal resistance junction-to-case	Each diode	-	1.0	1.1	°C/W
$R_{THc\text{-}h}$	Thermal resistance case-to-heatsink	Each diode, λ_{grease} = 1 W/(m·°C)	-	0.9		°C/W

1.3 NTC

Table 5. NTC temperature sensor, considered as stand-alone

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
R_{25}	Resistance	T = 25°C		5		kΩ
R_{100}	Resistance	T = 100°C		493		Ω
ΔR/R	Deviation of R_{100}		-5		+5	%
$B_{25/50}$	B-constant			3375		K
$B_{25/80}$	B-constant			3411		K
T	Operating temperature range		-40		150	°C

Figure 1. NTC resistance vs temperature

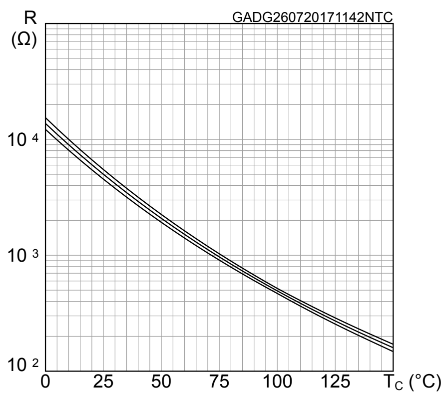
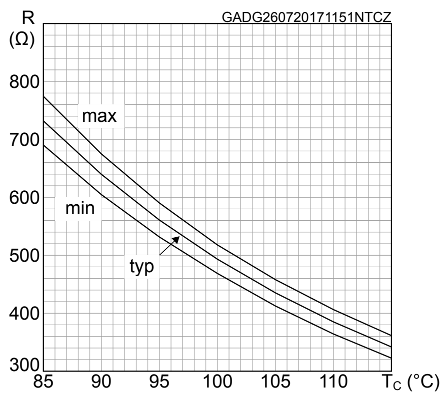


Figure 2. NTC resistance vs temperature, zoom



1.4 Package

Table 6. ACEPACK™ 1 package

Symbol	Parameter	Min.	Typ.	Max.	Unit
V_{isol}	Isolation voltage (AC voltage, t = 60 s)			2500	V
T_{stg}	Storage temperature	-40		125	°C
CTI	Comparative tracking index	200			
L_s	Stray inductance module P1 - EW loop		28.7		nH
R_s	Module single lead resistance, terminal-to-chip		3.9		mΩ

2 Electrical characteristics (curves)

Figure 3. IGBT output characteristics (V_{GE} = 15 V, terminal)

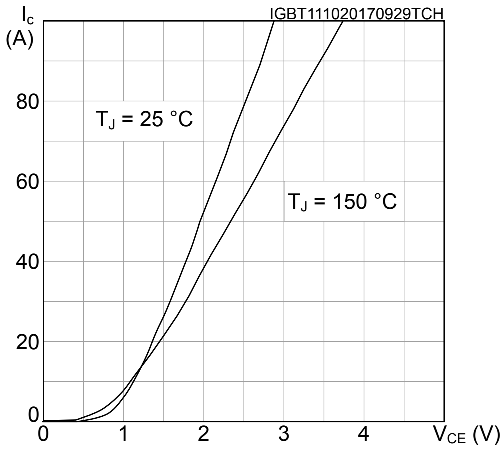


Figure 4. IGBT output characteristics (T_J = 150 °C, terminal)

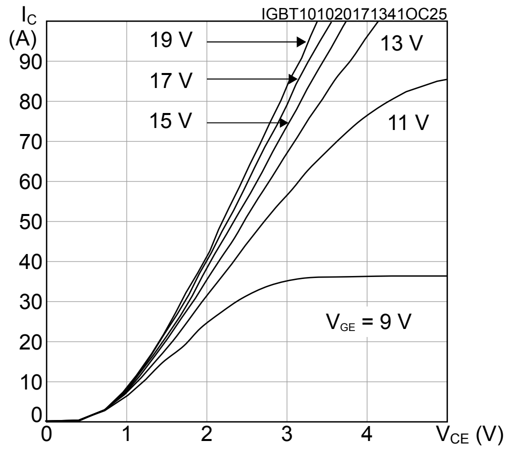


Figure 5. IGBT transfer characteristics (V_{CE} = 15 V, terminal)

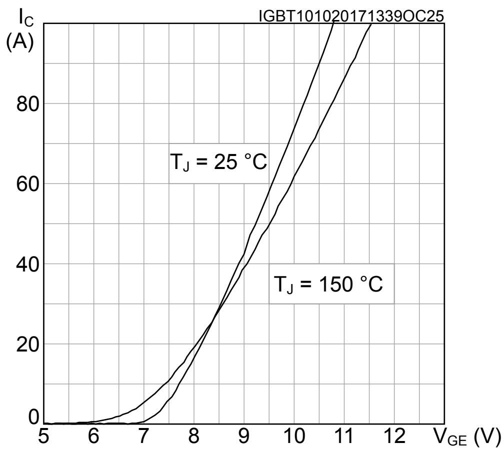


Figure 6. Switching energy vs gate resistance

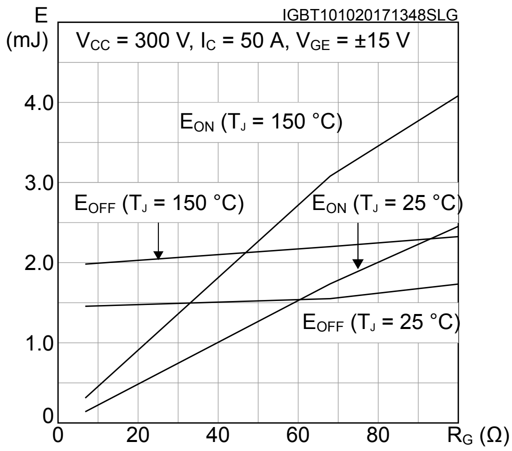


Figure 7. Switching energy vs collector current

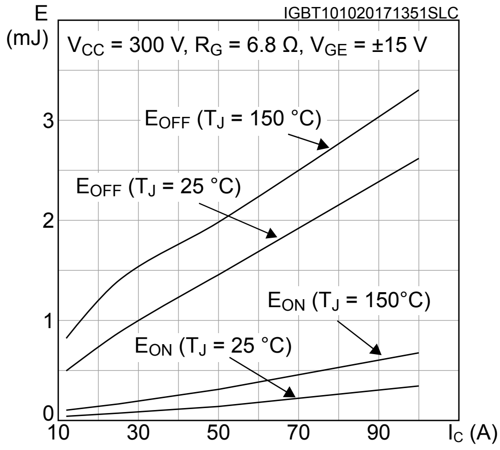


Figure 8. IGBT reverse biased safe operating area (RBSOA)

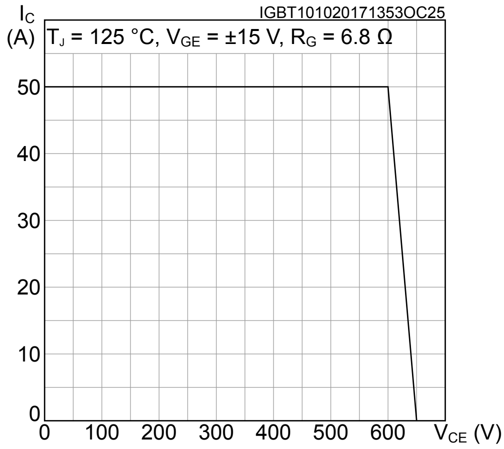


Figure 9. Diode forward characteristics

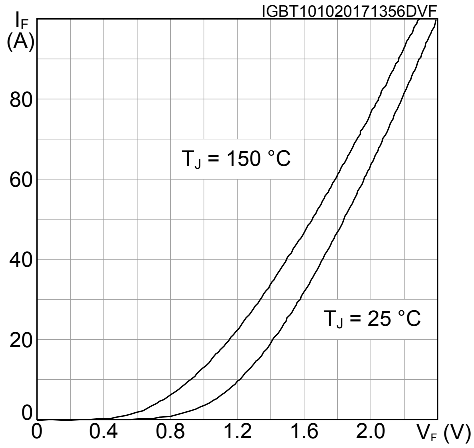


Figure 10. Diode reverse recovery energy vs diode current slope

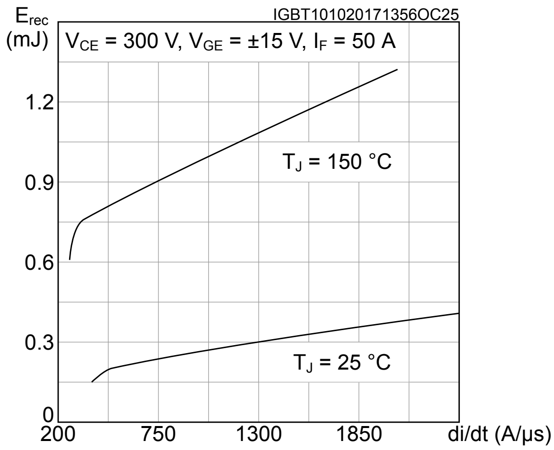


Figure 11. Diode reverse recovery energy vs forward current

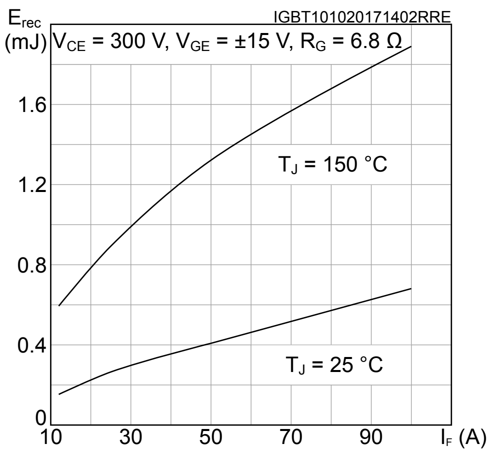


Figure 12. Diode reverse recovery energy vs gate resistance

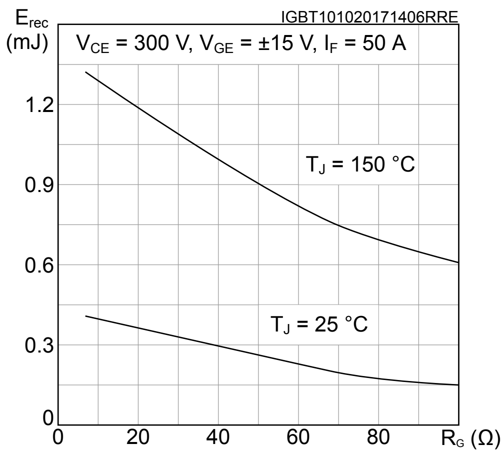


Figure 13. Inverter diode thermal impedance

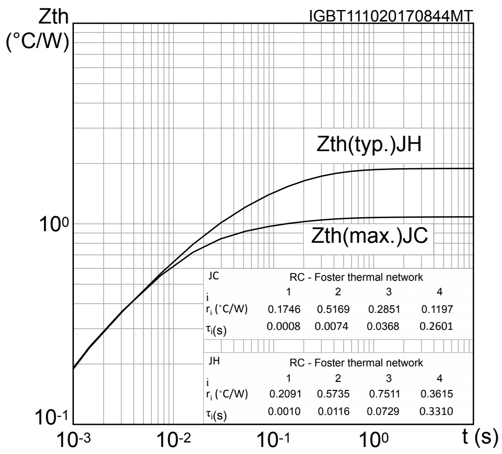
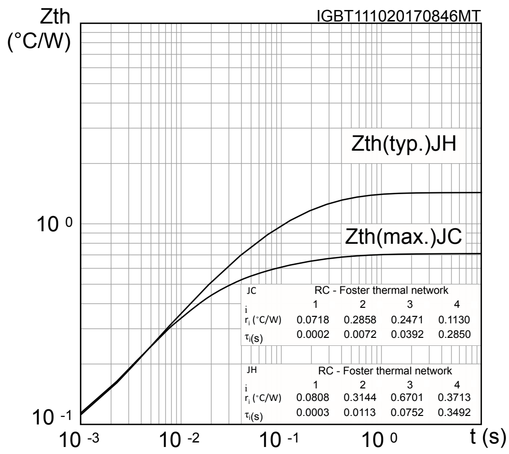


Figure 14. IGBT thermal impedance



3 Test circuits

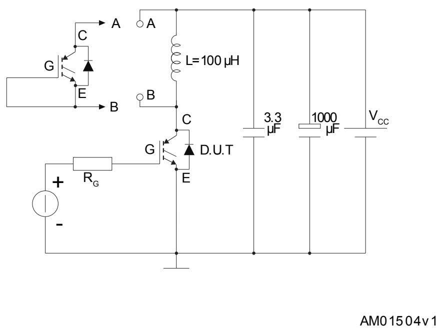


Figure 15. Test circuit for inductive load switching

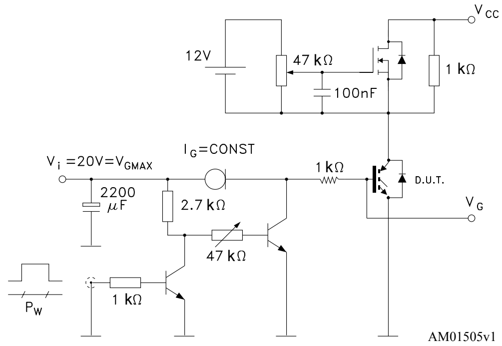


Figure 16. Gate charge test circuit

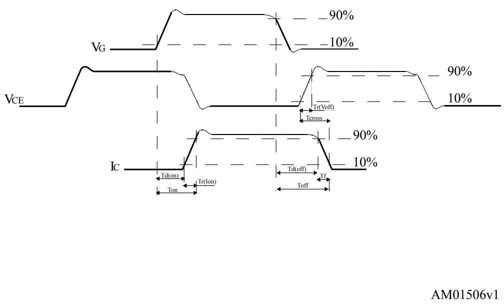


Figure 17. Switching waveform

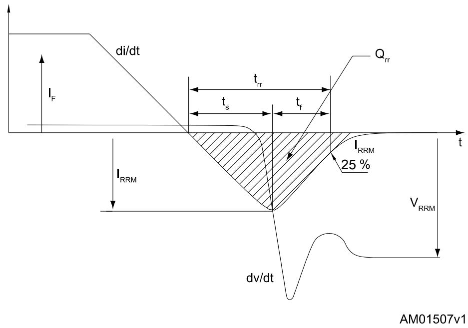


Figure 18. Diode reverse recovery waveform

4 Topology and pin description

Figure 19. **Electrical topology and pin description**

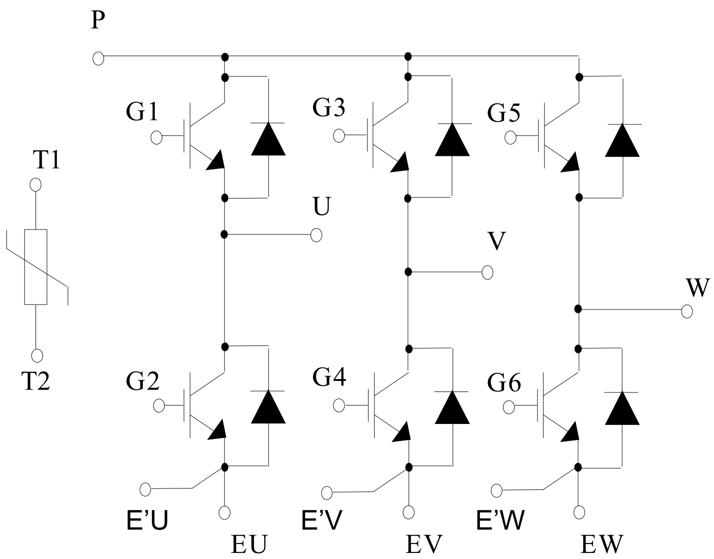
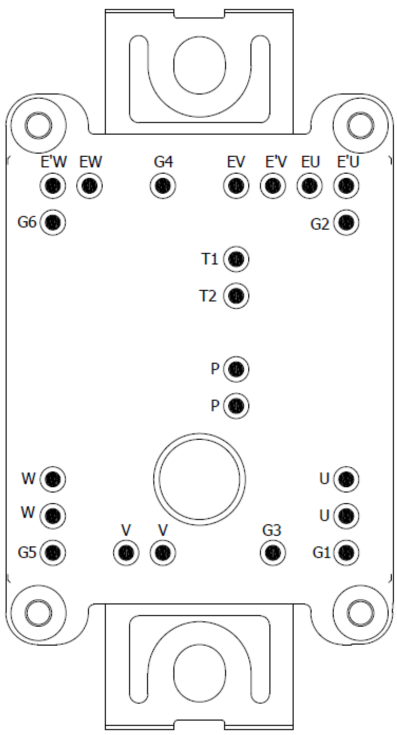


Figure 20. **Package top view with sixpack pinout**

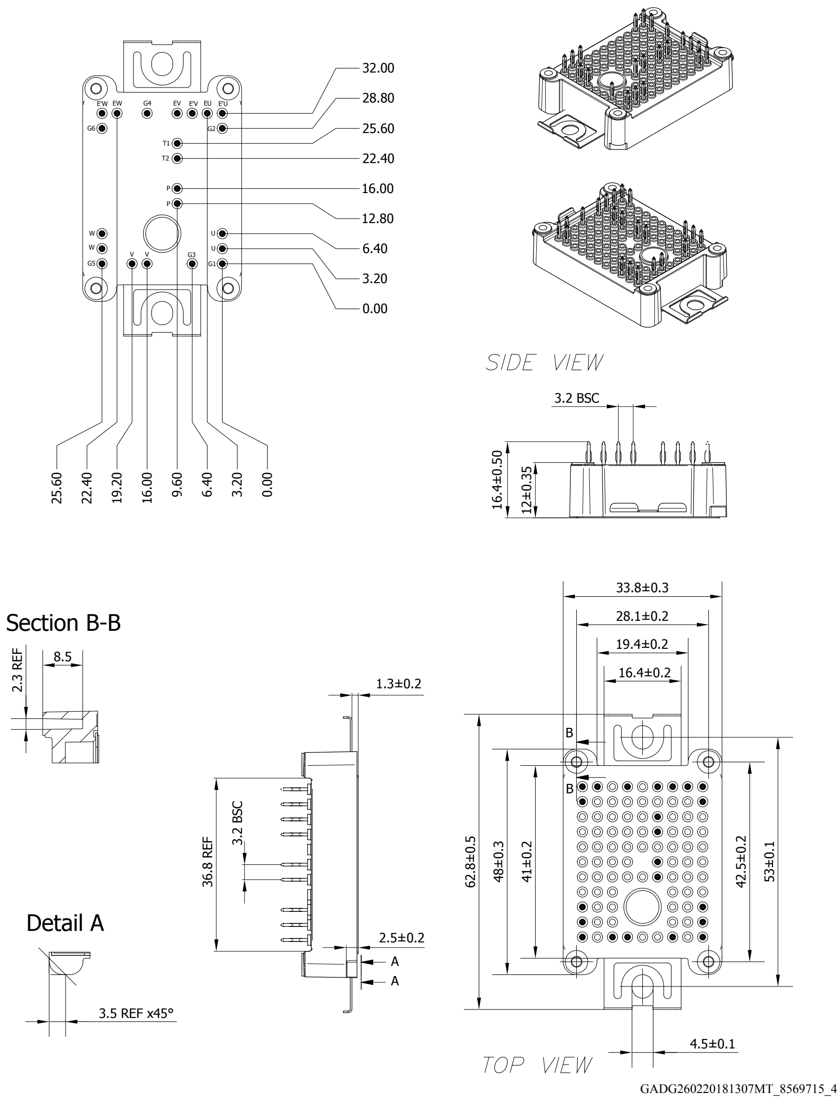


5 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

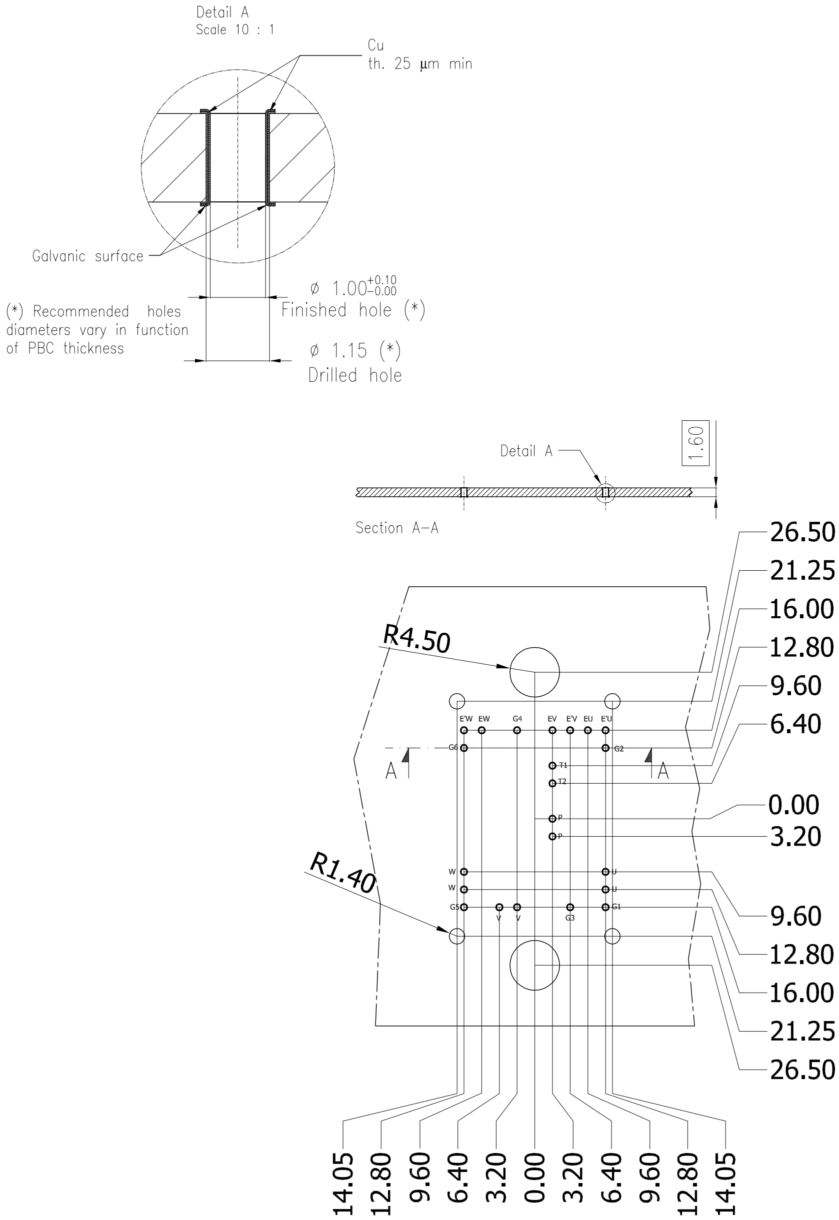
5.1 ACEPACK™ 1 sixpack press fit pins package information

Figure 21. **ACEPACK™ 1 sixpack press fit pins package outline (dimensions are in mm)**



- The lead size includes the thickness of the lead plating material.
- Dimensions do not include mold protrusion.
- Package dimensions do not include any eventual metal burrs.

Figure 22. **ACEPACK™ 1 sixpack press fit pins recommended PCB holes layout (dimensions are in mm)**



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Revision history

Table 7. Document revision history

Date	Revision	Changes
11-Oct-2017	1	Initial release.
01-Mar-2018	2	Removed maturity status indication from cover page. The document status is production data. Updated silhouette in cover page, Figure 13. Inverter diode thermal impedance, Figure 14. IGBT thermal impedance and Section 5.1 ACEPACK™ 1 sixpack press fit pins package information. Minor text changes.