TAIWAN SEMICONDUCTOR

3A, 600V Standard Surface Mount Rectifier

FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Low profile package
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen Free

APPLICATIONS

- DC to DC converter
- Switching mode converters and inverters
- General purpose

MECHANICAL DATA

- Case: SOD-128
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.027g (approximately)

KEY PARAMETERS

PARAMETER	VALUE	UNIT
I_F	3	A
V_{RRM}	600	V
I_{FSM}	50	A
$T_{J\,MAX}$	150	°C
Package	SOD-128	
Configuration	Single die	

SOD-128

Cathode —— Anode

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

PARAMETER		SYMBOL	S3JFS	UNIT
Marking code on the device			S3JFS	
Repetitive peak reverse voltage		V_{RRM}	600	V
Reverse voltage, total rms value		$V_{R(RMS)}$	420	V
Forward current		I_F	3	A
Surge peak forward current, single half sine-wave superimposed on rated load	t = 8.3ms	I_{FSM}	50	A
	t = 1.0ms		140	A
Junction temperature		T_J	-55 to +150	°C
Storage temperature		T_{STG}	-55 to +150	°C

Version: A2303

THERMAL PERFORMANCE

PARAMETER	SYMBOL	TYP	UNIT
Junction-to-lead thermal resistance	$R_{\Theta JL}$	14	°C/W
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	74	°C/W
Junction-to-case thermal resistance	$R_{\Theta JC}$	20	°C/W

Thermal Performance Note: Units mounted on PCB (5mm x 5mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T_A = 25°C unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage[1]	I_F = 1.5A, T_J = 25°C	V_F	0.95	-	V
	I_F = 3.0A, T_J = 25°C		1.03	1.10	V
	I_F = 1.5A, T_J = 125°C		0.84	-	V
	I_F = 3.0A, T_J = 125°C		0.94	-	V
Reverse current @ rated V_R[2]	T_J = 25°C	I_R	-	1	μA
	T_J = 125°C		7	-	μA
Junction capacitance	1MHz, V_R = 4.0V	C_J	14	-	pF

Notes:

(1) Pulse test with PW = 0.3ms

(2) Pulse test with PW = 30ms

ORDERING INFORMATION

ORDERING CODE	PACKAGE	PACKING
S3JFS	SOD-128	14,000 / Tape & Reel

Version: A2303

CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

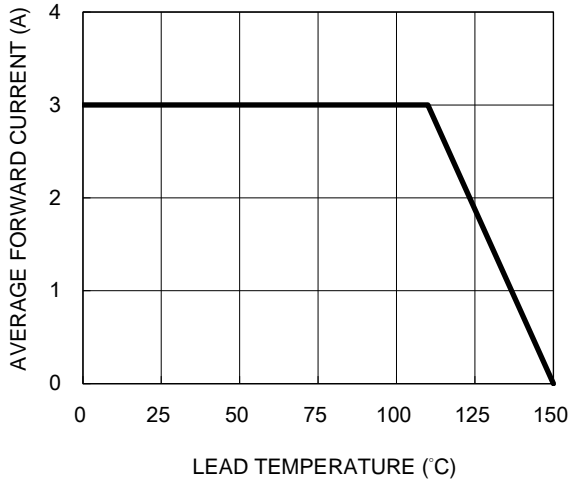**Fig.1 Forward Current Derating Curve**
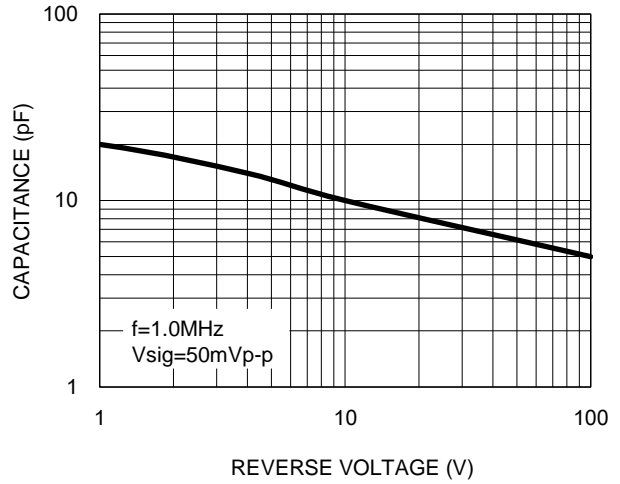

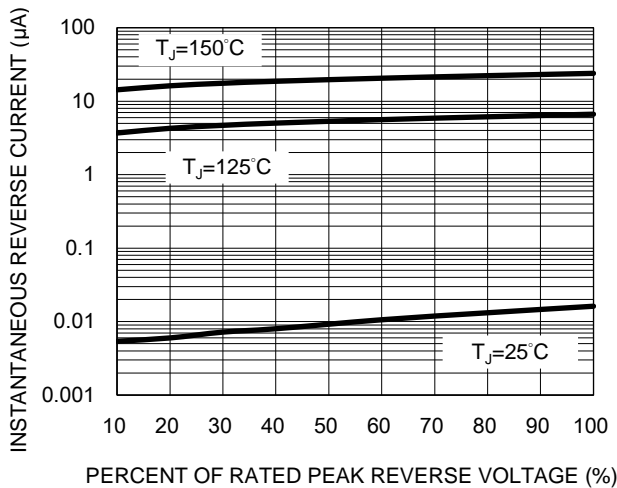
Fig.2 Typical Junction Capacitance

Fig.3 Typical Reverse Characteristics

Fig.4 Typical Forward Characteristics
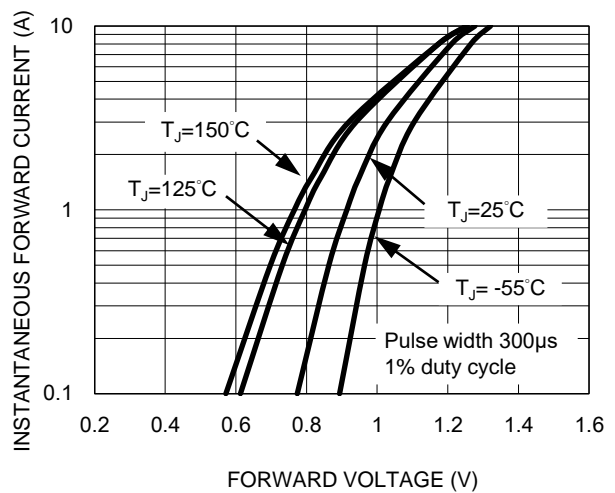
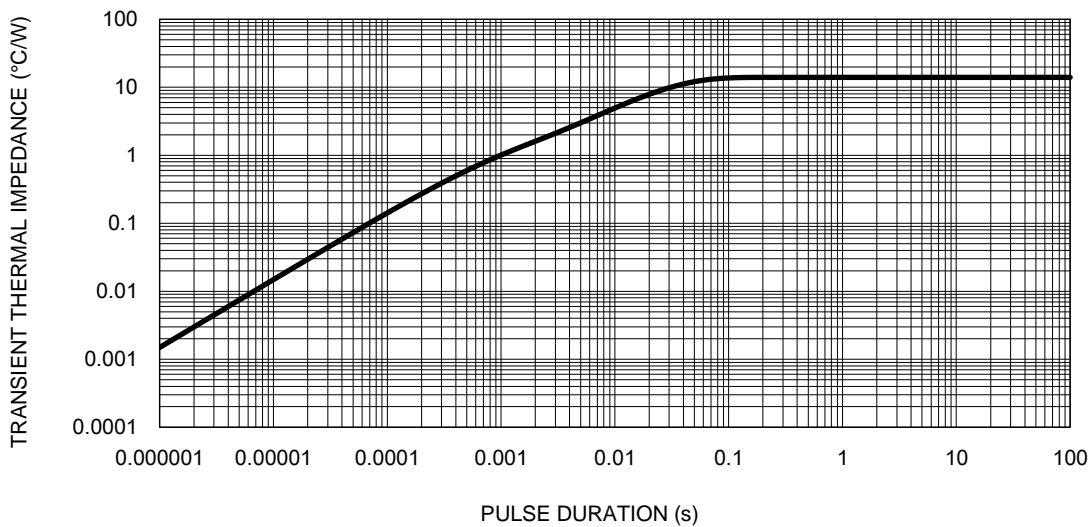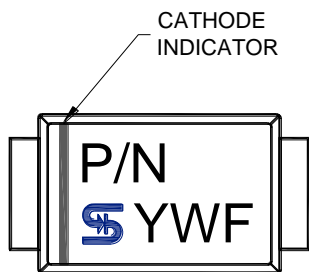**Fig.5 Typical Transient Thermal Impedance**

3

Version: A2303

PACKAGE OUTLINE DIMENSIONS

SOD-128

A

2.70
2.30 △4

4.00
3.60 B

△4

DETAIL A

C

DETAIL A
(SCALE 3:1)

0.22
0.10

1.10
0.90

SEATING PLANE

0.10
0.00 C

0.60   0.80
0.30   0.40

1.90
1.60

⊕ 0.13 Ⓜ C A B
2X

5.00
4.40

⊕ 0.13 Ⓜ C A B

4.10

2.30

1.20

SUGGESTED PAD LAYOUT

CATHODE
INDICATOR

P/N
⑤ YWF
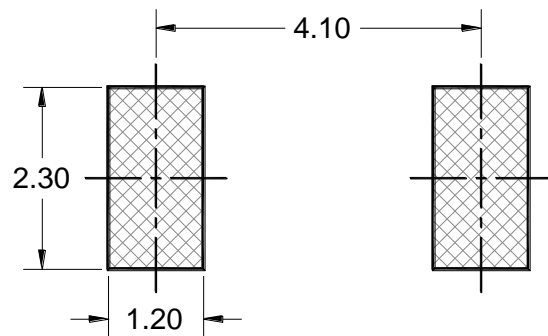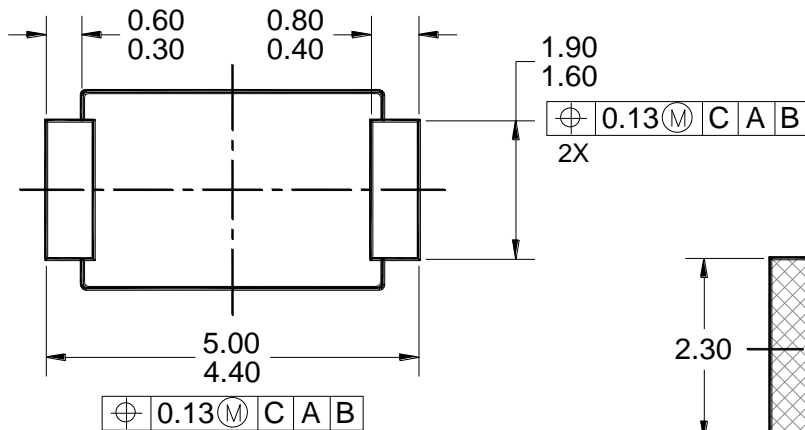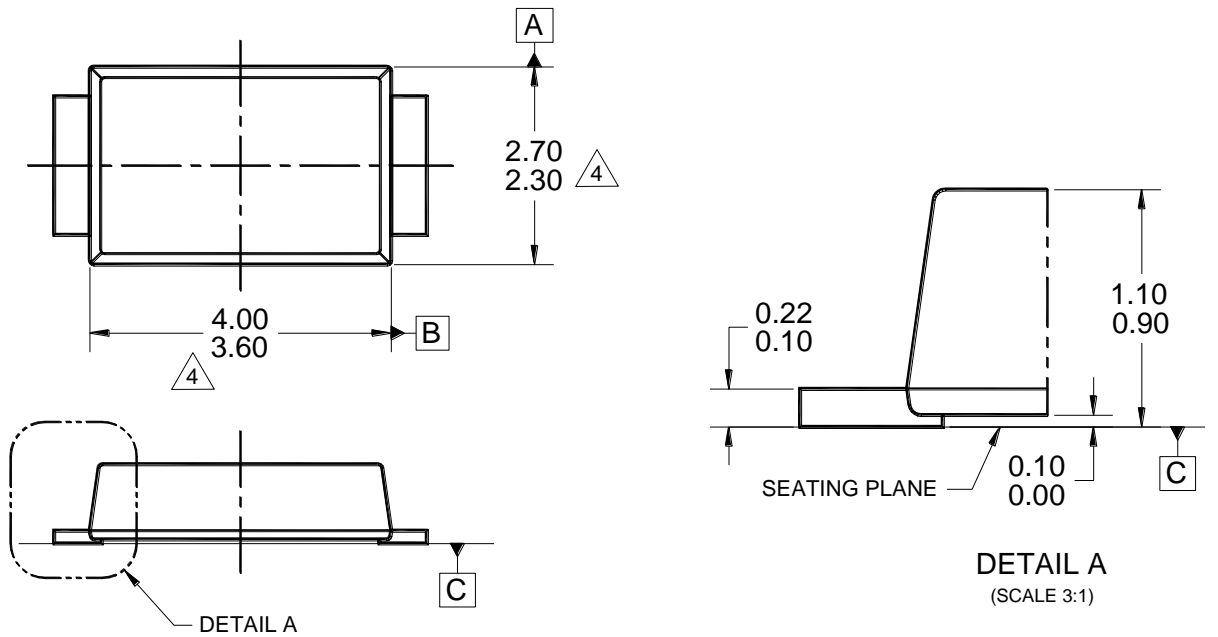
MARKING DIAGRAM

P/N = MARKING CODE
YW = DATE CODE
F = FACTORY CODE

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009.
3. PACKAGE OUTLINE REFERENCE: JEDEC DO-221, VARIATION AD, ISSUE B.
4. △4 MODED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH.
5. DWG NO. REF: HQ2SD07-SOD128-039 REV A.

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Version: A2303