

LH934A – AC LED



Introduction

Features

- Plastic Molded Lead Frame Type :
12.4mm(L), 11.4mm(W), 4.38mm(T)
- SMD Type : 1 Heat Pad and 4 Electrical Pad
- View Angle($\Delta\theta$) * : 136°
- High Power / Brightness Chip & Long Time Reliability

Applications

- Indoor & Outdoor lighting
- Direct AC power source plug-in (100~120Vac, 220~240Vac)

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1. Product Outline

1) Features

- Plastic Molded Lead Frame Type : 12.4mm(L), 11.4mm(W), 4.38mm(T)
- SMD Type : 1 Heat Pad and 4 Electrical Pad
- Beam View Angle($\Delta\theta$) * :136°
- High Power / Brightness Chip & Long Time Reliability

2) Applications

- Indoor & Outdoor lighting
- Direct AC power source plug-in (100~120Vac, 220~240Vac)

※ View Angle describes the spatial intensity distribution and is the difference between the angles corresponding to 50% of the maximum intensity. (Full Width Half Maximum)

2. Absolute Maximum Rating

Parameter	Value	Unit
RMS current*	29**(240Vac) / 58**(120Vac)	mA
Power Dissipation***	4.5	W
LED Junction Temperature (T _J)	125	°C
Operating Temperature Range (T _{OPR})	-40 ~ 85	°C
Storage Temperature (T _{STG})	-40 ~ 120	°C
ESD Sensitivity	± 3,000V HBM	-

*RMS (Root mean square) current indicates AC operation at 50~60Hz

** Maximum current that can be fed into LEDs depends on their configuration. Refer to p.12 and p.20

*** Average power dissipation only by the LED in AC operation. Power dissipation by any ballast component that is connected to the LED is not included.

3. Electro-optical Characteristics ($T_a = 25\text{ }^{\circ}\text{C}$)

CCT [K]	Minimum CRI	Luminous flux [lm]		
		I _F =22mA(rms) @ 220~240Vac* I _F =44mA(rms) @ 100~120Vac**		I _F =29mA(rms) @ 220~240Vac I _F =58mA(rms) @ 100~120Vac
		Min.	Typ.	Typ.
2700	80	220	280	355
3000		240	290	365
3500		240	290	365
4000		260	300	375
5000	70	280	360	460
6500		250	330	460
Power Dissipation***		3.3W		4.5W
Operating Frequency		50/60 Hz		

*Max 29mA (RMS) current is allowed by 220~240Vac configuration. Refer to [Resistor Table] on p.12.

**Max 58mA (RMS) current is allowed by 100~120Vac configuration. Refer to [Resistor Table] on p.12.

***Average power dissipation only by the LED in AC operation. Power dissipation by any ballast component that is connected to the LED is not included.

Notes :

- 1) SAMSUNG ELECTRONICS maintains a tolerance of ± 3.0 on CRI measurements.
- 2) SAMSUNG ELECTRONICS maintains a tolerance of $\pm 7\%$ on flux measurements.

4. Color Binning ($T_a = 25\text{ }^{\circ}\text{C}$)

Nominal CCT	Product Code	Color Rank	Chromaticity Bins
2700K	SPHWHTHAD605S0 <u>W0</u> U4	W0 (Whole Bin)	WB,WC,WD,WE,WF,WG,WH
	SPHWHTHAD605S0 <u>WU</u> U4	WU (Half Bin)	WB,WC,WD
	SPHWHTHAD605S0 <u>WP</u> U4	WP (M Sub Bin)	WB
3000K	SPHWHTHAD605S0 <u>VO</u> VZ	V0 (Whole Bin)	VB,VC,VD,VE,VF,VG,VH
	SPHWHTHAD605S0 <u>VU</u> VZ	VU (Half Bin)	VB,VC,VD
	SPHWHTHAD605S0 <u>VP</u> VZ	VP (M Sub Bin)	VB
3500K	SPHWHTHAD605S0 <u>UO</u> VZ	U0 (Whole Bin)	UB,UC,UD,UE,UF,UG,UH
	SPHWHTHAD605S0 <u>UU</u> VZ	UU (Half Bin)	UB,UC,UD
	SPHWHTHAD605S0 <u>UP</u> VZ	UP (M Sub Bin)	UB
4000K	SPHWHTHAD605S0 <u>TO</u> WZ	T0 (Whole Bin)	TB,TC,TD,TE,TF,TG,TH
	SPHWHTHAD605S0 <u>TU</u> WZ	TU (Half Bin)	TB,TC,TD
	SPHWHTHAD605S0 <u>TP</u> WZ	TP (M Sub Bin)	TB
5000K	SPHWHTHAD603S0 <u>R0</u> MZ	R0 (Whole Bin)	R1,R2,R3,R4,R5,R6,R7,R8
	SPHWHTHAD603S0 <u>RT</u> MZ	RT (M Sub Bin)	R1,R2,R3,R4
6500K	SPHWHTHAD603S0 <u>P0</u> LZ	P0 (Whole Bin)	P1,P2,P3,P4,P5,P6,P7,P8
	SPHWHTHAD603S0 <u>PT</u> MZ	PT (M Sub Bin)	P1,P2,P3,P4

5. Chromaticity region & Coordinates ($T_a = 25\text{ }^{\circ}\text{C}$)

< CIE 1931 Chromaticity diagram >

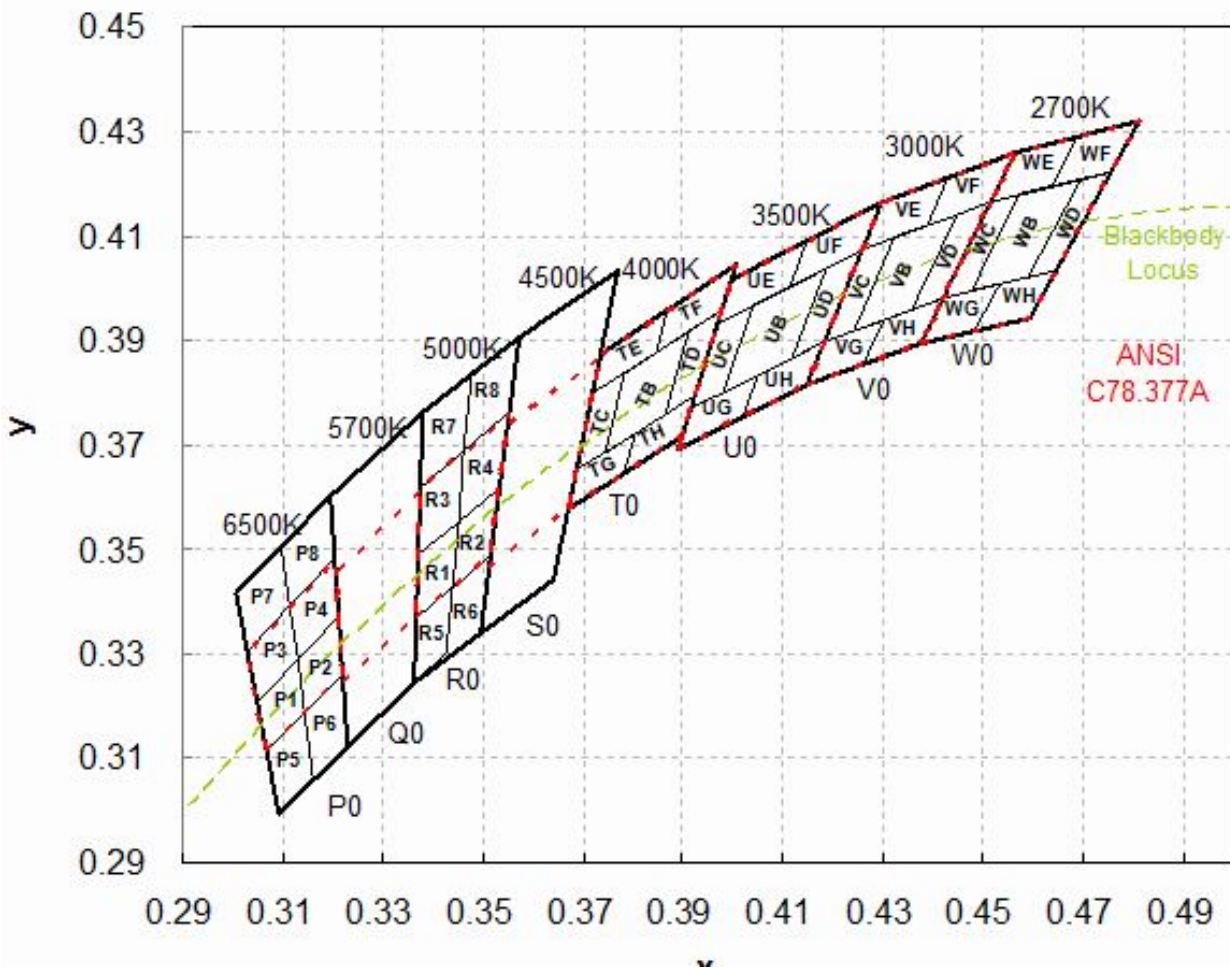


TABLE	Rank	CIE X	CIE Y	Rank	CIE X	CIE Y
2700K	WB	0.4697	0.4211	WF	0.4813	0.4319
		0.4576	0.4183		0.4688	0.4290
		0.4477	0.3998		0.4636	0.4196
		0.4591	0.4024		0.4758	0.4225
	WC	0.4576	0.4183	WG	0.4534	0.4012
		0.4515	0.4168		0.4420	0.3985
		0.4420	0.3985		0.4373	0.3893
		0.4477	0.3998		0.4483	0.3919
	WD	0.4758	0.4225	WH	0.4648	0.4038
		0.4697	0.4211		0.4534	0.4012
		0.4591	0.4024		0.4483	0.3919
		0.4648	0.4038		0.4593	0.3944
	WE	0.4688	0.4290	ANSI	0.4813	0.4319
		0.4562	0.4260		0.4562	0.4260
		0.4515	0.4168		0.4373	0.3944
		0.4636	0.4196		0.4593	0.4319

TABLE	Rank	CIE X	CIE Y	Rank	CIE X	CIE Y
3000K	VB	0.4451	0.4146	VF	0.4562	0.4260
		0.4324	0.4100		0.4431	0.4213
		0.4244	0.3922		0.4388	0.4122
		0.4361	0.3964		0.4515	0.4168
	VC	0.4324	0.4100	VG	0.4303	0.3944
		0.4261	0.4077		0.4185	0.3902
		0.4185	0.3902		0.4147	0.3814
		0.4244	0.3922		0.4260	0.3854
	VD	0.4515	0.4168	VH	0.4420	0.3985
		0.4451	0.4146		0.4303	0.3944
		0.4361	0.3964		0.4260	0.3854
		0.4420	0.3985		0.4373	0.3893
	VE	0.4431	0.4213	ANSI C78.377	0.4813	0.4319
		0.4299	0.4165		0.4562	0.4260
		0.4261	0.4077		0.4373	0.3893
		0.4388	0.4122		0.4593	0.3944

TABLE	Rank	CIE X	CIE Y	Rank	CIE X	CIE Y
3500K	UB	0.4188	0.4042	UF	0.4299	0.4165
		0.4042	0.3970		0.4148	0.4090
		0.3983	0.3803		0.4115	0.4005
		0.4118	0.3869		0.4261	0.4077
	UC	0.4042	0.3970	UG	0.4050	0.3837
		0.3969	0.3934		0.3916	0.3771
		0.3916	0.3771		0.3889	0.3690
		0.3983	0.3803		0.4018	0.3752
	UD	0.4261	0.4077	UH	0.4185	0.3902
		0.4188	0.4042		0.4050	0.3837
		0.4118	0.3869		0.4018	0.3752
		0.4185	0.3902		0.4147	0.3814
	UE	0.4148	0.4090	ANSI C78.377	0.4813	0.4319
		0.3996	0.4015		0.4562	0.4260
		0.3969	0.3934		0.4373	0.3944
		0.4115	0.4005		0.4593	0.4319

TABLE	Rank	CIE X	CIE Y	Rank	CIE X	CIE Y
4000K	TB	0.3914	0.3922	TF	0.4006	0.4044
		0.3784	0.3841		0.3871	0.3959
		0.3746	0.3688		0.3849	0.3880
		0.3865	0.3761		0.3979	0.3962
	TC	0.3784	0.3841	TG	0.3806	0.3726
		0.3720	0.3800		0.3687	0.3652
		0.3687	0.3652		0.3670	0.3578
		0.3746	0.3688		0.3784	0.3647
	TD	0.3979	0.3962	TH	0.3925	0.3798
		0.3914	0.3922		0.3806	0.3726
		0.3865	0.3761		0.3784	0.3647
		0.3925	0.3798		0.3898	0.3716
	TE	0.3871	0.3959	ANSI C78.377	0.4813	0.4319
		0.3736	0.3874		0.4562	0.4260
		0.3720	0.3800		0.4373	0.3944
		0.3849	0.3880		0.4593	0.4319

Table	Rank	CIE X	CIE Y	Rank	CIE X	CIE Y
5000K	R1	0.3452	0.3558	R5	0.3441	0.3428
		0.3371	0.3493		0.3366	0.3369
		0.3366	0.3369		0.3361	0.3245
		0.3441	0.3428		0.3428	0.3292
	R2	0.3533	0.3624	R6	0.3515	0.3487
		0.3452	0.3558		0.3441	0.3428
		0.3441	0.3428		0.3428	0.3292
		0.3515	0.3487		0.3495	0.3339
	R3	0.3464	0.3688	R7	0.3476	0.3835
		0.3376	0.3616		0.3381	0.3762
		0.3371	0.3493		0.3376	0.3616
		0.3452	0.3558		0.3464	0.3688
	R4	0.3551	0.3760	R8	0.3571	0.3907
		0.3464	0.3688		0.3476	0.3835
		0.3452	0.3558		0.3464	0.3688
		0.3533	0.3624		0.3551	0.3760

Table	Rank	CIE X	CIE Y	Rank	CIE X	CIE Y
6500K	P1	0.3131	0.3290	P5	0.3145	0.3187
		0.3048	0.3209		0.3068	0.3113
		0.3068	0.3113		0.3093	0.2993
		0.3145	0.3187		0.3162	0.3057
	P2	0.3213	0.3371	P6	0.3221	0.3261
		0.3131	0.3290		0.3145	0.3187
		0.3145	0.3187		0.3162	0.3057
		0.3221	0.3261		0.3231	0.3120
	P3	0.3117	0.3393	P7	0.3101	0.3509
		0.3028	0.3304		0.3005	0.3415
		0.3048	0.3209		0.3028	0.3304
		0.3131	0.3290		0.3117	0.3393
	P4	0.3205	0.3481	P8	0.3196	0.3602
		0.3117	0.3393		0.3101	0.3509
		0.3131	0.3290		0.3117	0.3393
		0.3213	0.3371		0.3205	0.3481

Notes:

SAMSUNG ELECTRONICS maintains ± 0.01 tolerance of CCx, CCy

6. Luminous Flux ($T_a = 25\text{ }^{\circ}\text{C}$)

Product Code	3.3W* Operation 22mA(rms) @ 220Vac 44mA(rms) @ 110Vac		4.5W** Operation 29mA(rms) @ 220Vac 58mA(rms) @ 110Vac	CCT
	Rank	Flux range [lm]	Flux range [lm]	
SPHWHTHAD605S0W0U4 SPHWHTHAD605S0WUU4 SPHWHTHAD605S0WPU4	U4	U1	220 ~ 240	2700K
		V1	240 ~ 260	
		W1	260 ~ 280	
		X1	280~	
SPHWHTHAD605S0V0VZ SPHWHTHAD605S0VUVZ SPHWHTHAD605S0VPVZ	VZ	V1	240 ~ 260	3000K
		W1	260~ 280	
		X1	280 ~ 300	
		01	300 ~	
SPHWHTHAD605S0U0VZ SPHWHTHAD605S0UUVZ SPHWHTHAD605S0UPVZ	VZ	V1	240 ~ 260	3500K
		W1	260 ~ 280	
		X1	280 ~ 300	
		01	300 ~	
SPHWHTHAD605S0V0WZ SPHWHTHAD605S0VUWZ SPHWHTHAD605S0VPWZ	WZ	W1	260 ~ 280	4000K
		X1	280 ~ 300	
		01	300 ~ 320	
		11	320 ~	
SPHWHTHAD603S0R0MZ SPHWHTHAD603S0RTMZ	MZ	M1	280 ~ 310	5000K
		N1	310 ~ 340	
		P1	340 ~ 370	
		Q1	370 ~	
SPHWHTHAD603S0P0LZ SPHWHTHAD603S0PTLZ	LZ	L1	250 ~ 280	6500K
		M1	280 ~ 310	
		N1	310 ~ 340	
		P1	340 ~	

* Reference binning is done at $I_F=22\text{mA(rms)}$, 3.3W.

** Luminous flux at 4.5W operation is calculated by extrapolation from measured luminous flux at 3.3W operation.

7. Vf Binning ($T_a = 25\text{ }^{\circ}\text{C}$)

Symbol	Condition	Rank		Min.	Typ.	Max.	Unit
Vf*	$I_F = 22\text{mA(rms)}$	S0	F1	185	-	195	Vac (rms)
			F3	195	-	200	
			F5	200	-	205	

※ Tolerance : $\pm 5\text{V}$

* The LED is directly connected to a test source without any additional components, when measured.
The test source imposes sinusoidal current waves at 60Hz (22mA rms) across the LED, and Vf is measured in RMS.

8. Resistor Table ($T_a = 25\text{ }^{\circ}\text{C}$)

Vin (RMS)	Vf Bin	Target PKG Power Dissipation*		
		3.3W @ $I_F=44\text{mA}$	4.0W @ $I_F=53\text{mA}$	4.5W @ $I_F=58\text{mA}$
100Vac	F1	330 Ω	240 Ω	200 Ω
	F3	300 Ω	230 Ω	190 Ω
	F5	270 Ω	220 Ω	180 Ω
110Vac	F1	560 Ω	430 Ω	360 Ω
	F3	510 Ω	410 Ω	360 Ω
	F5	460 Ω	390 Ω	360 Ω
120Vac	F1	800 Ω	620 Ω	560 Ω
	F3	750 Ω	620 Ω	545 Ω
	F5	700 Ω	620 Ω	530 Ω

Vin (RMS)	Vf Bin	Target PKG Power Dissipation*		
		3.3W @ $I_F=22\text{mA}$	4.0W @ $I_F=26.5\text{mA}$	4.5W @ $I_F=29\text{mA}$
220Vac	F1	2.3 K Ω	1.7 K Ω	1.5 K Ω
	F3	2.1 K Ω	1.65 K Ω	1.46 K Ω
	F5	1.9 K Ω	1.6 K Ω	1.43 K Ω
230Vac	F1	2.62 K Ω	2.1 K Ω	1.9 K Ω
	F3	2.56 K Ω	2.05 K Ω	1.85 K Ω
	F5	2.5 K Ω	2.0 K Ω	1.8 K Ω
240Vac	F1	3.1 K Ω	2.5 K Ω	2.2 K Ω
	F3	3.0 K Ω	2.5 K Ω	2.2 K Ω
	F5	2.9 K Ω	2.5 K Ω	1.2 K Ω

※ I_F tolerance : $\pm 14\%$

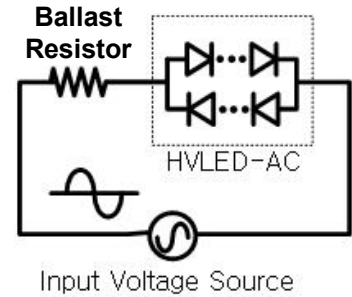
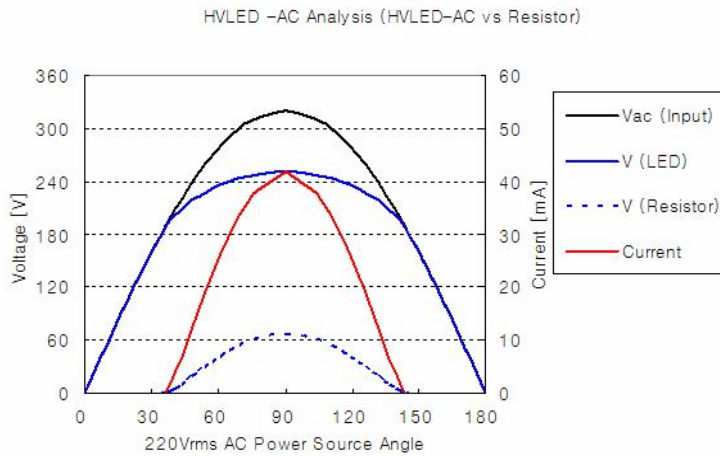
* Proper selection of resistor values should be made for LEDs to be driven at the desired current level with acceptable tolerance specified above. The table above summarizes recommended resistor values for the mains voltages by country, and the LED's Vf bin.

HV-AC LED can be wired in two types of configuration : one is serial connection to be applicable to the mains of 220~240Vac, and the other is parallel connection to the mains of 100~120Vac.

Each configuration is wired by foot print pattern, on which the LED is mounted. For the recommended foot print design, see "8.circuit design section" on p.21.

9. Typical Characteristic Graphs

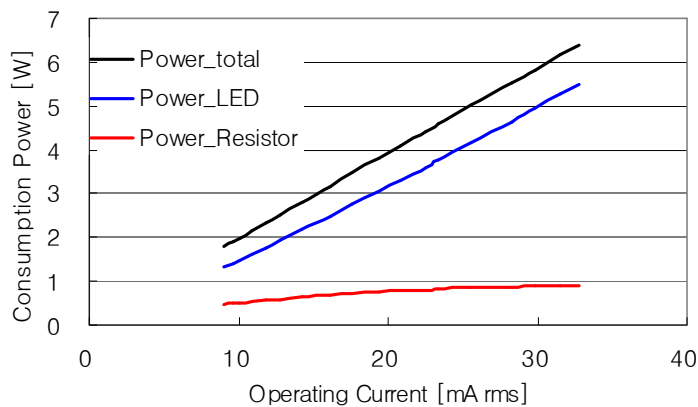
1) AC voltage operating characteristic



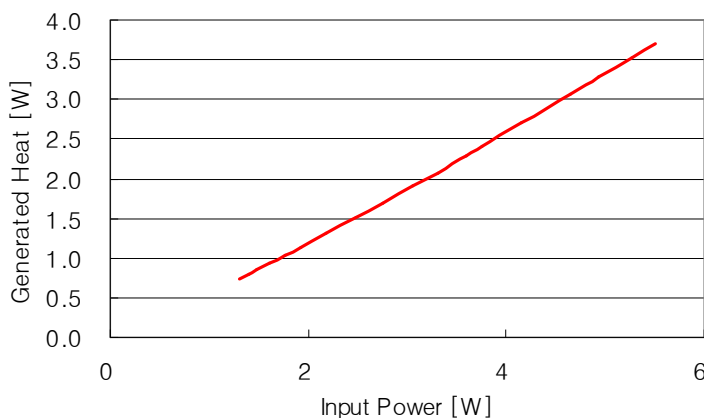
Total Power Consumption = Power_LED + Power_Resistor

Power_LED = Total Power - I^2R

< Power consumption vs. Operating current >



< LED Input Power vs. Generated Heat >



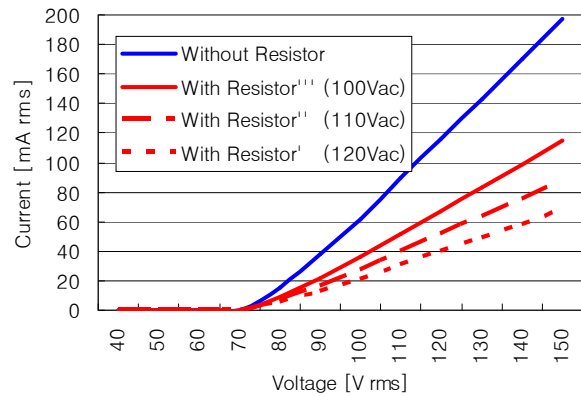
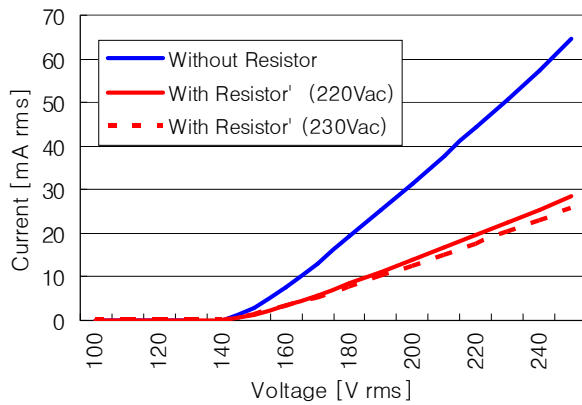
※ Total Thermal dissipation = LED + Resistor

Thermal dissipation of the LED is the vertical axis of the above graph.

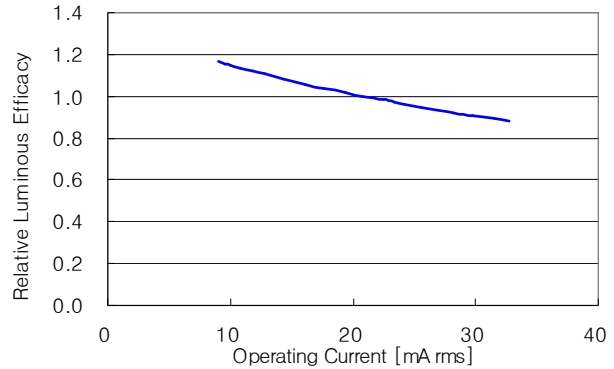
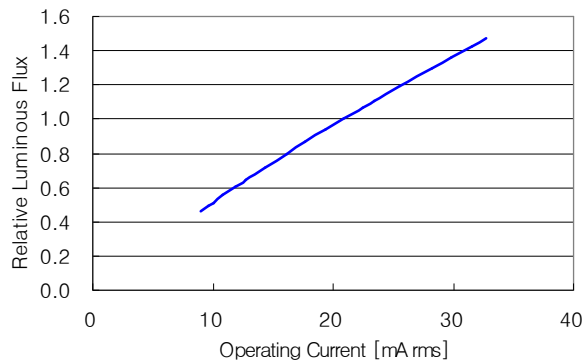
Thermal dissipation of the resistor is $\text{Current}^2 \times \text{Resistance}$.

Proper resistor value and type must be selected depending on the operating condition.

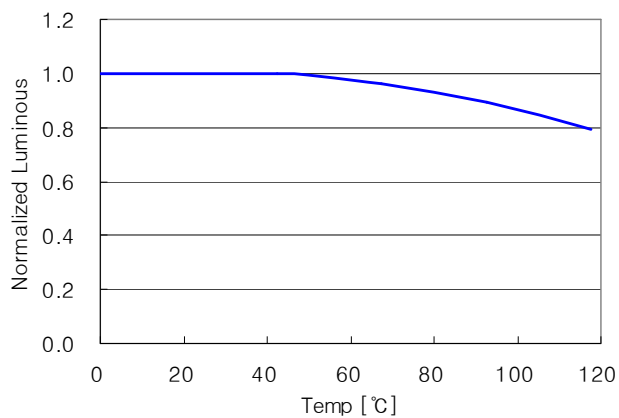
2) IV characteristic (operating in AC voltage, $T_a = 25\text{ }^{\circ}\text{C}$)



3) Optical characteristic (operating in AC voltage, $T_a = 25\text{ }^{\circ}\text{C}$)



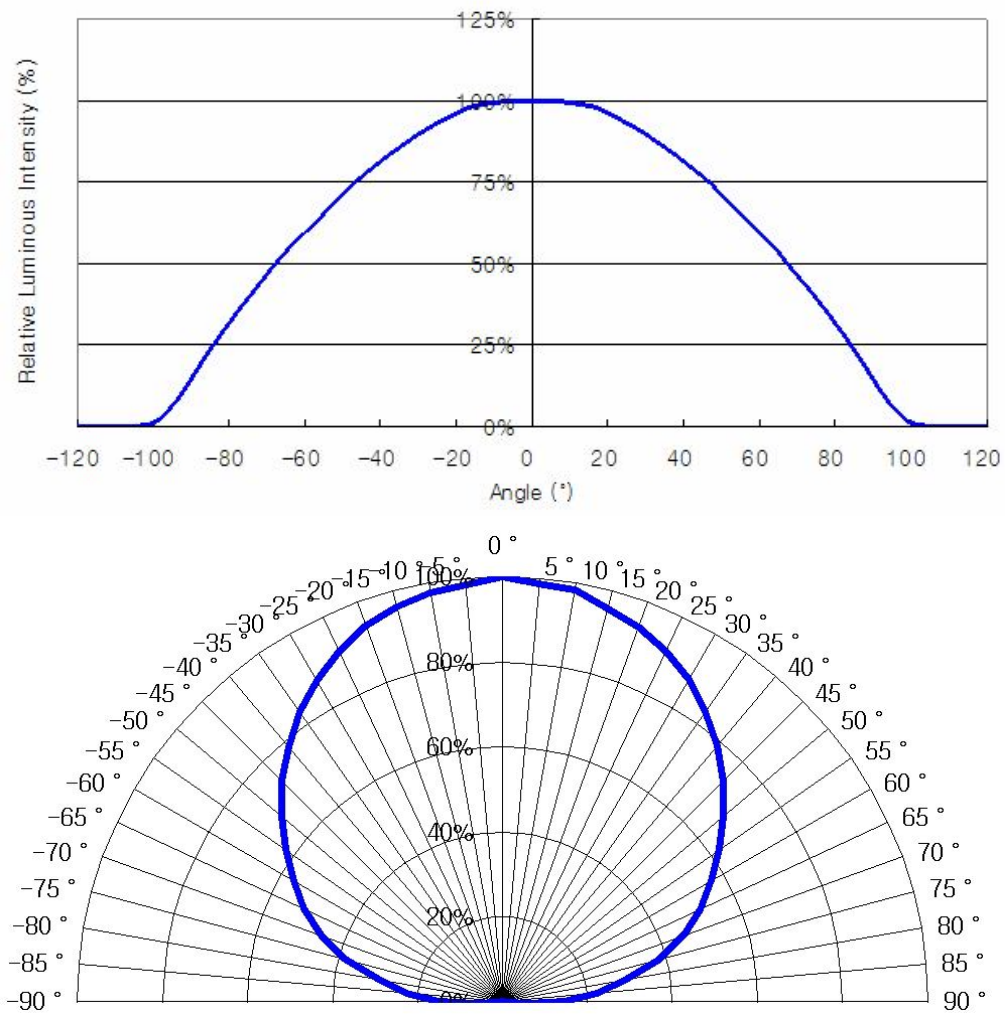
4) Thermal characteristic (operating in AC voltage, $T_a = 25\text{ }^{\circ}\text{C}$)



Temperature measurement point

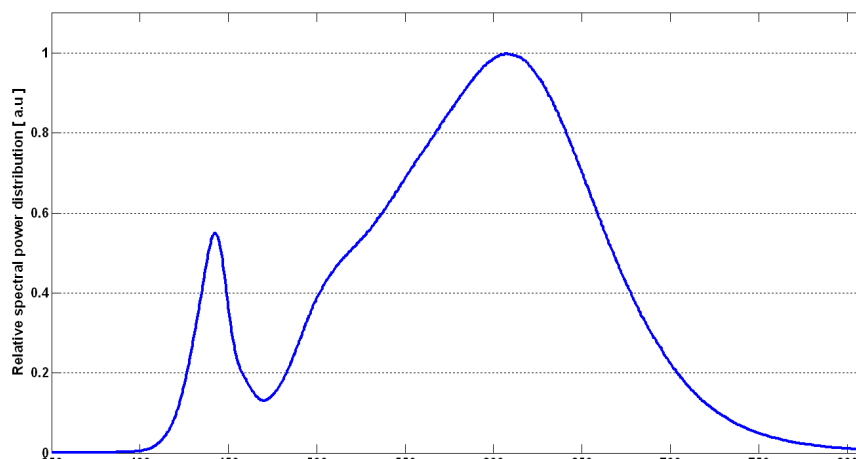
※Temperature is measured on bottom surface of metal PCB with ballast resistors mounted.

5) Typical Spatial Distribution

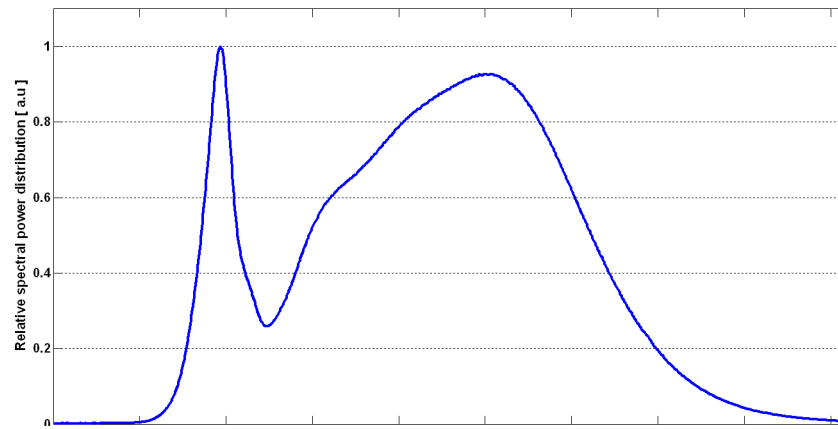


6) Spectrum Distribution

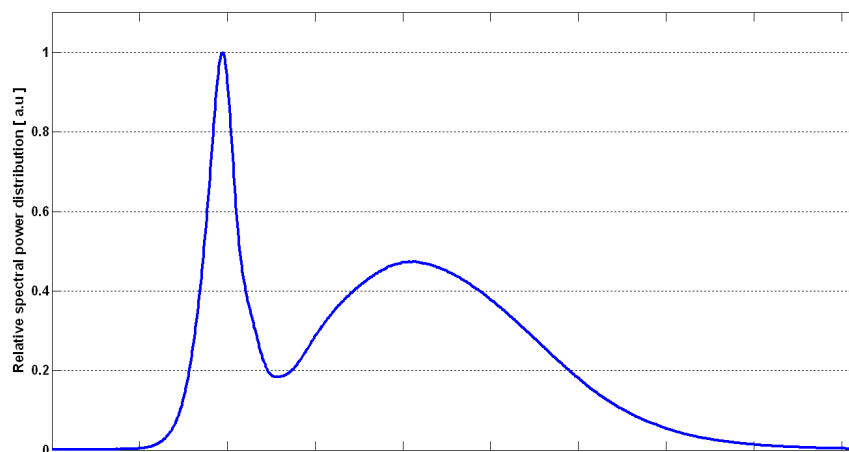
2700K, 3000K, 3500K



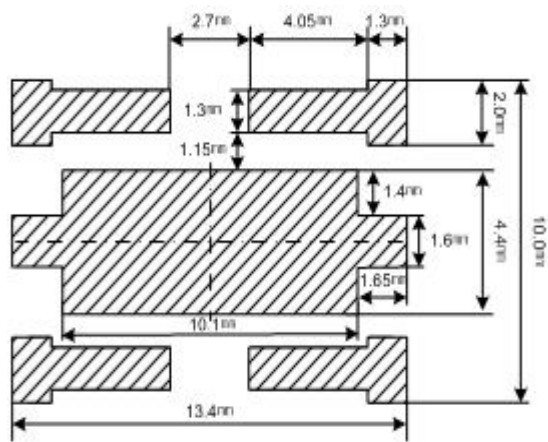
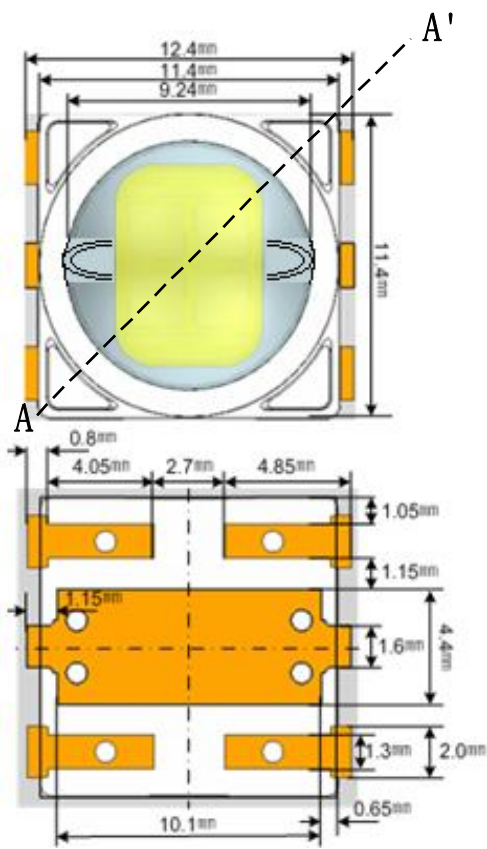
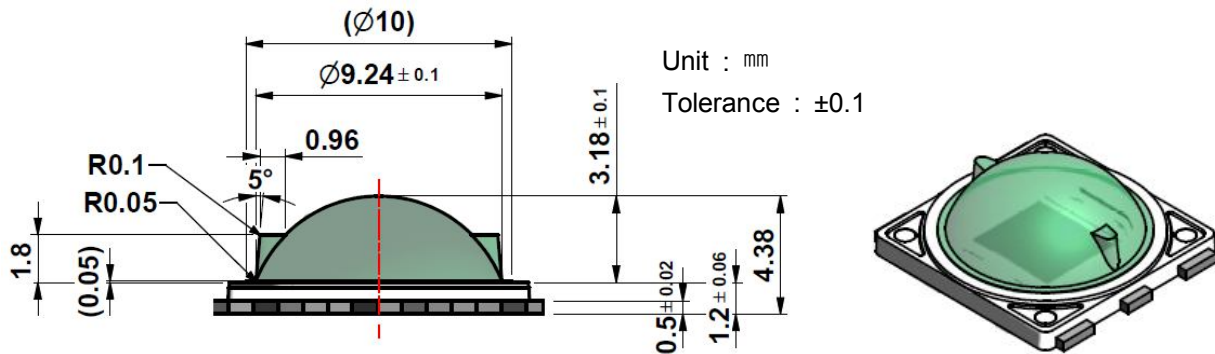
4000K



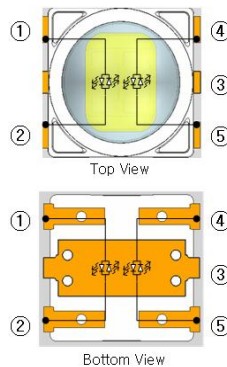
5000K, 6500K



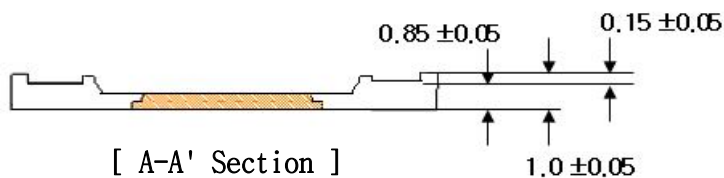
10. Outline Drawing and Pad Configuration



Recommended
PCB Solder Pad



Pad	Function
①	Bipolarity
②	Bipolarity
③	Thermal (Electrically Isolated)
④	Bipolarity
⑤	Bipolarity



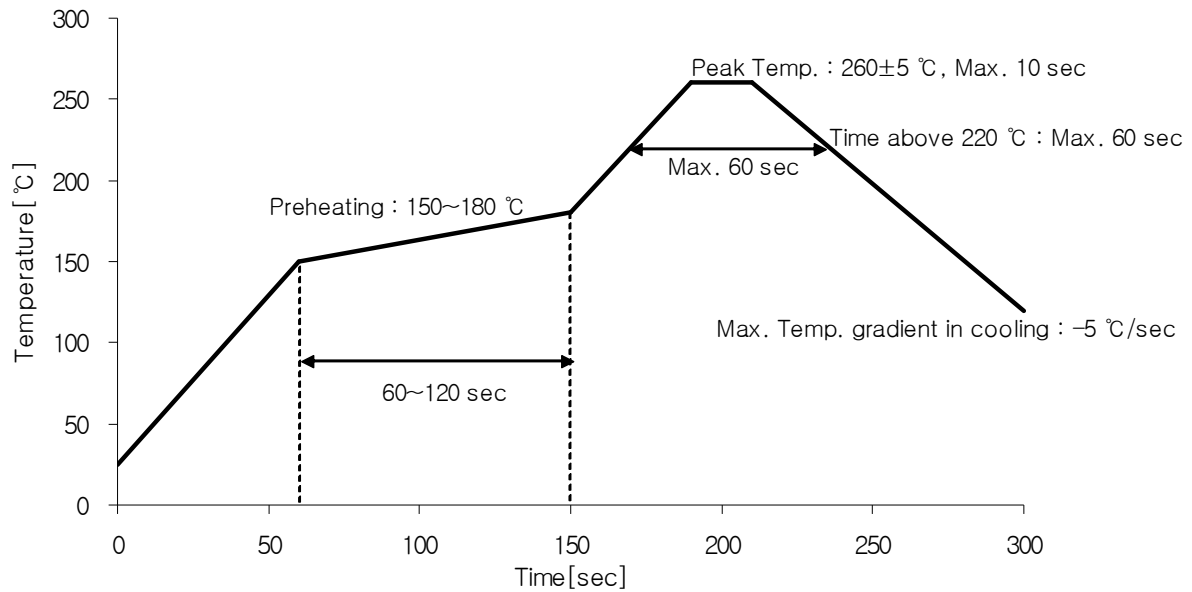
Pick and Place

1. Do not place pressure on the encapsulating resin
It is recommended to use a pick&place nozzle with inside diameter at 9.2mm
2. The maximum compressing force is 20N on the polymer

11. Solder Conditions

1) Reflow Conditions (Pb-Free)

Reflow Frequency : 2 time max.



2) For Manual Soldering

Not more than 5 seconds @Max. 300 °C, under soldering iron.

12. Reliability Test Items and Conditions

1) Test Items

Test Items	Test Conditions	Test Hours/Cycles
Room Temperature life test	25℃, IF = Max AC 25mA(rms)	1,000 h
High Temperature humidity life test	85℃, 85% RH, IF = Max AC 25mA(rms)	1,000 h
High Temperature life test	85℃, IF = Max AC 25mA(rms)	1,000 h
Low Temperature life test	-40℃, IF = Max AC 25mA(rms)	1,000 h
High Temperature Storage	120℃	1,000 h
Low Temperature Storage	-40℃	1,000 h
Thermal Shock	-40 / 120℃, each 30 min	200 cycles
Temperature humidity Cycle On/Off test	-40 / 85℃, each 20 min, 100 min transfer Power On/off each 5 min, AC 20 mA	100 cycles
Reflow (Pb-Free)	Peak 260±5℃ for 10 sec	3 times
ESD(HBM)	R1 : 10 MΩ , R2 : 1.5 kΩ , C : 100 pF	5 times (± 2 kV)
Surge	Line to Line	2 kV

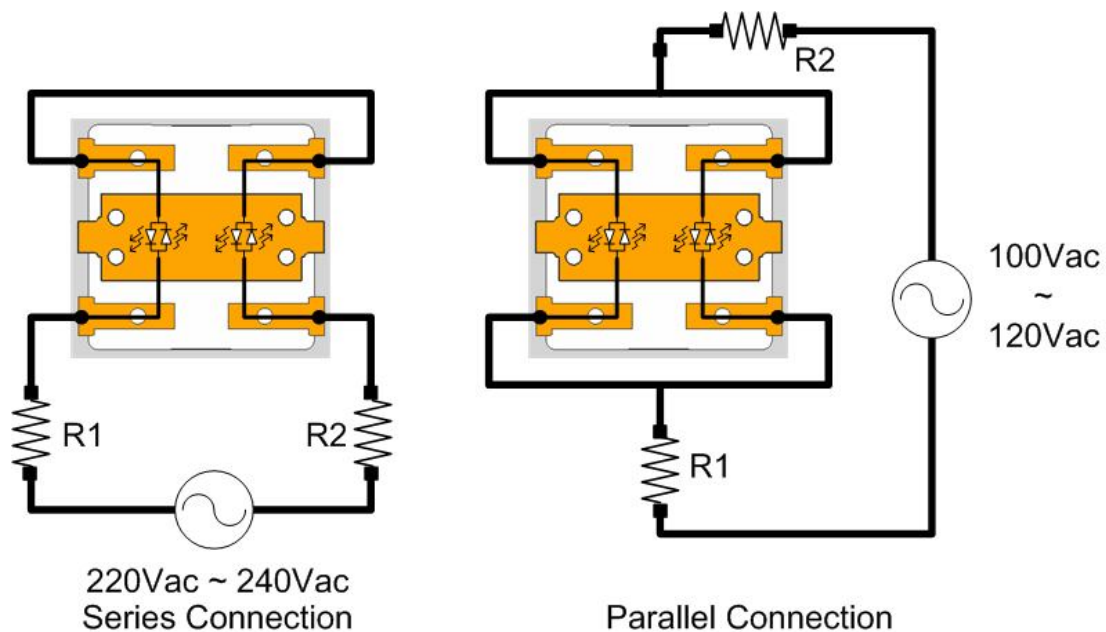
2) Criteria for Failure

Item	Symbol	Test Condition	Limit	
			Min	Max
Forward Voltage	V _F	I _F = 22 mA(rms)	-	U.S.L.*1.2
Luminous Flux	Φ _V	I _F = 22 mA(rms)	L.S.L.*0.7	-

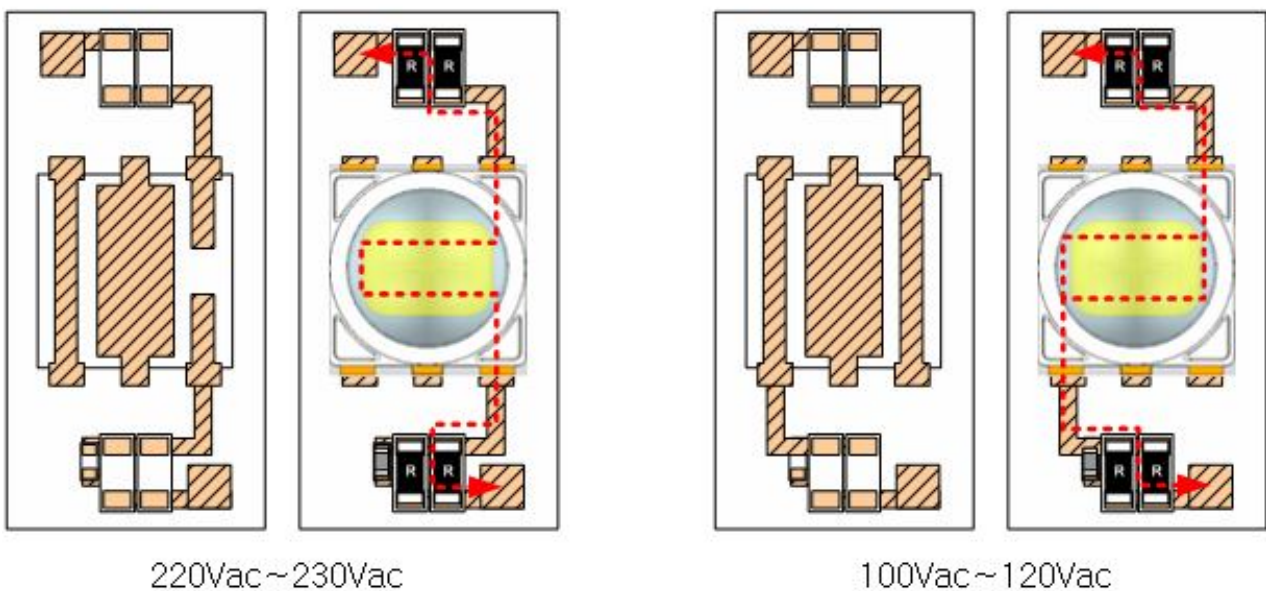
※ U.S.L : Upper Standard Level, L.S.L : Lower Standard Level

13. Circuit Design - Package and PCB

As illustrated below, two different configurations are possible depending on electric mains to which the LED to be connected.



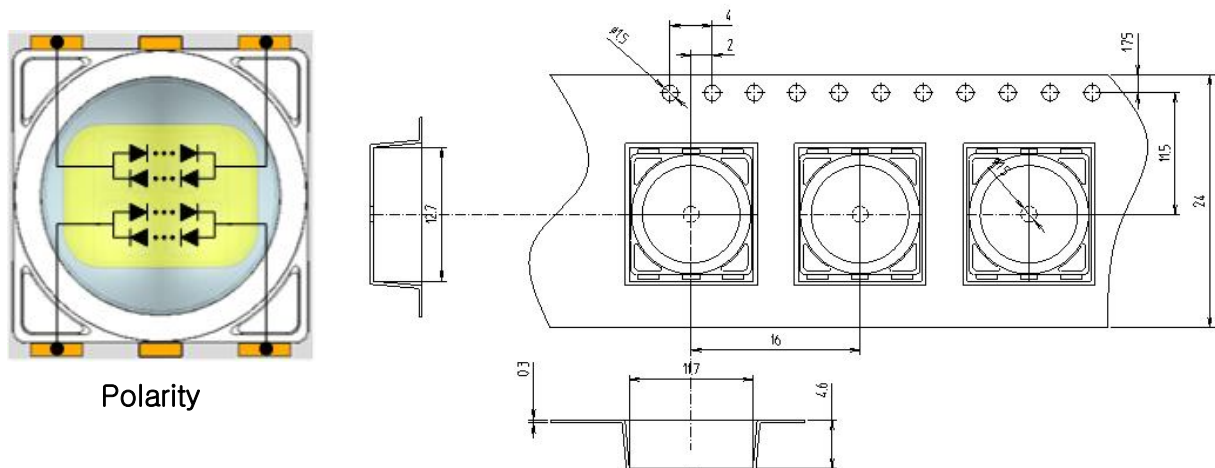
Schematic Circuit Connection (Example)



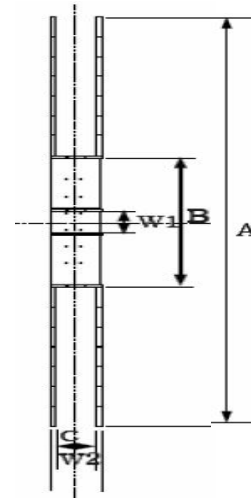
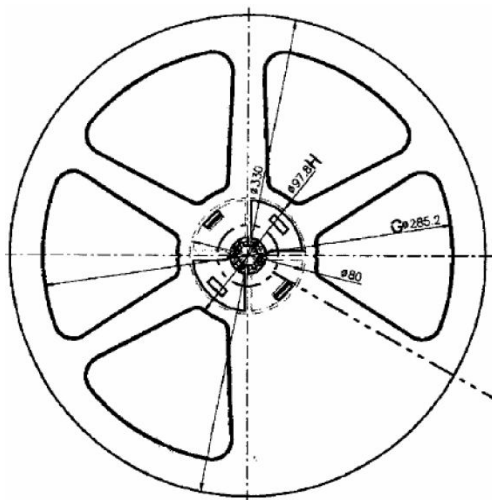
PCB Pattern Circuit (Example)

To improve protection against surge, two pairs of identical resistors connected in parallel are symmetrically added to the LED so that total equivalent resistance, the sum of R1 and R2, is equal to the value on p.12.

14. Taping Dimension



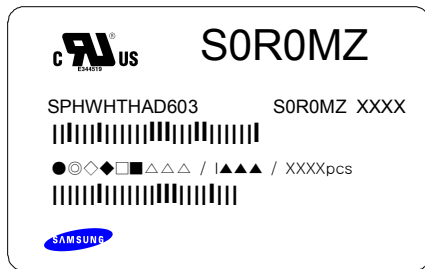
End			Start
More than 100 mm Unloaded tape	Mounted with LED	More than (100~200)mm Unloaded tape	Leading part more than (200~400)mm



Symbol	A	B	C	W1	W2
Dimension(mm)	330 ± 1	80 ± 1	25 ± 0.5	13 ± 0.3	29.5 ± 1

- (1) Quantity : 800 Pcs / 13" Reel.
- (2) Cumulative Tolerance : Cumulative Tolerance/10 pitches is less than ± 0.2 mm
- (3) Adhesion Strength of Cover Tape : Adhesion strength to be 0.1-0.7N when the cover tape is turned off from the carrier tape at 10 °C angle to be the carrier tape.
- (4) Packaging : P/N, Manufacturing data Code No. and quantity to be indicated on a damp proof Package

15. Label Structure



Rank Code

/S0/ : VF Rank (refer to page 3)

/R0/ : Chromaticity Coordinate Rank, CIE (refer to page 4)

/MZ/ : Luminous Flux (refer to page 4)

11. Lot Number

The Lot number is composed of the following characters

●◎◇◆□■△△△ / |▲▲▲ / 800PCS

● : Production Site (S:SAMSUNG LED, G:Gosin China)

◎ : L (LED)

◇ : Product State (A:Normality, B:Bulk, C:First Production, R:Reproduction, S:Sample)

◆ : Year (S:2008, T:2009, U:2010...)

□ : Month (1 ~ 9, A, B)

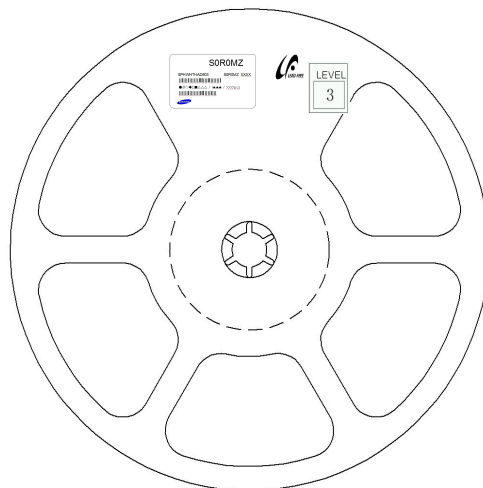
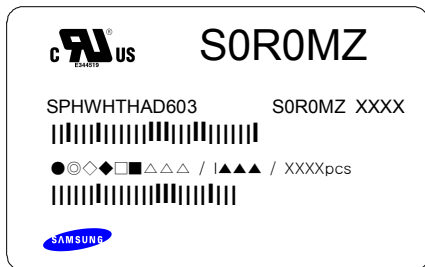
■ : Day (1 ~ 9, A, B ~ V)

△ : SAMSUNG LED Product Number (1 ~ 999)

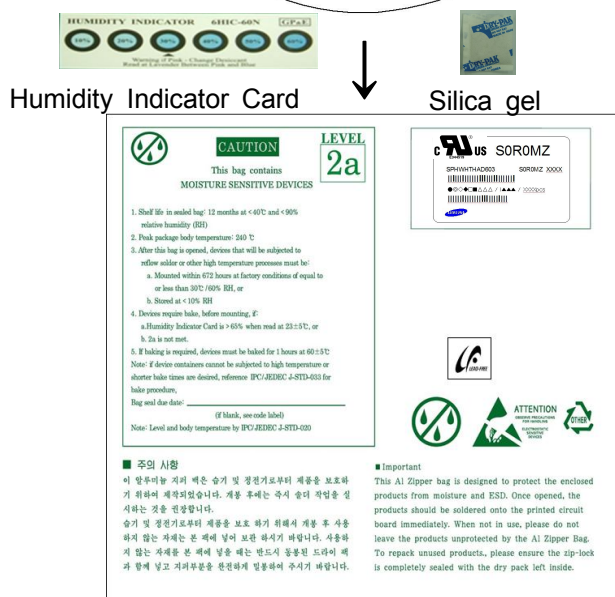
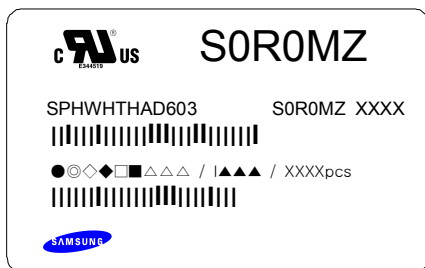
▲ : Reel Number (1 ~ 999)

16. Reel Packing Structure

1) Reel



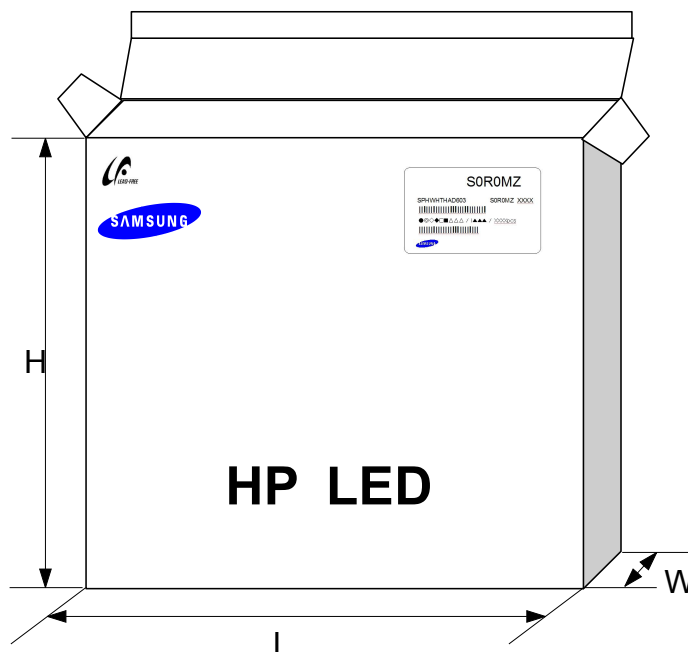
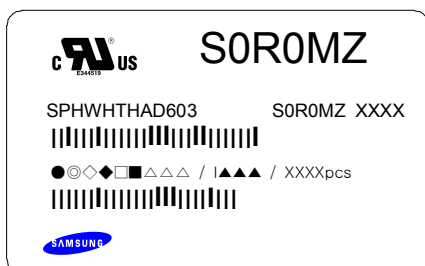
2) Aluminum Bag



3) Inner Box

Material : Paper(SW3B(B))

TYPE	SIZE(mm)		
	L	W	H
13inch	335	45	335

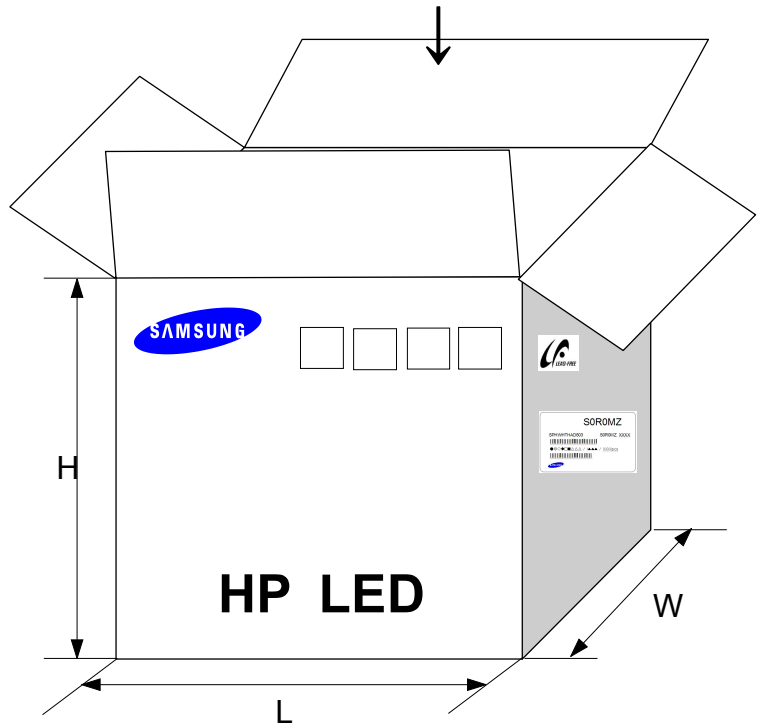
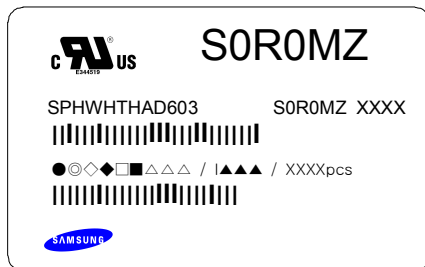




4) Carton Box

Material : Paper(SW3B(B))

TYPE	SIZE(mm)		
	L	W	H
13inch	350	350	350



18. Precaution for Use

- 1) For over-current-proof function, customers are recommended to apply resistors to prevent sudden change of the current caused by slight shift of the voltage.
- 2) This device should not be used in any type of fluid such as water, oil, organic solvent, etc. When washing is required, IPA is recommended to use.
- 3) When the LEDs illuminate, operating current should be decided after considering the ambient maximum temperature.
- 4) LEDs must be stored in a clean environment. If the LEDs are to be stored for 3 months or more after being shipped from SAMSUNG LED, they should be packed by a sealed container with nitrogen gas injected. (Shelf life of sealed bags : 12 months, temp. 0~40℃, 20~70%RH)
- 5) After storage bag is open, device subjected to soldering, solder reflow, or other high temperature processes must be:
 - a. Mounted within 168 hours (7days) at an assembly line with a condition of no more than 30℃/60%RH,
 - b. Stored at <10% RH.
- 6) Repack unused Products with anti-moisture packing, fold to close any opening and then store in a dry place.
- 7) Devices require baking before mounting, if humidity card reading is >60% at 23±5℃.
- 8) Devices must be baked for 24hours at 65±5℃, if baking is required.

- 9) The LEDs are sensitive to the static electricity and surge. It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs.

If voltage exceeding the absolute maximum rating is applied to LEDs, it may cause damage or even destruction to LED devices.

Damaged LEDs may show some unusual characteristics such as increase in leak current, lowered turn-on voltage, or abnormal lighting of LEDs at low current.

- 10) When handling LED with tweezers, the LED Should only be held by the polymer body, not by the encapsulant or LENS.
- 11) The use of appropriate nozzle for the LED recommended. For the recommended nozzle size, refer to the figure at the below.
Inner diameter of nozzle $\geq \Phi 9.2\text{mm}$
- 12) Do not stack assembled PCBs together. Since silicone is a soft material, abrasion between two PCB assembled with silicone encapsulated LED might cause catastrophic failure of the LEDs due to damage to encapsulant and wire and LED detachment.

19. Hazard Substance Analysis



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Issued Date: January 21, 2011

Page 1 of 5

To: SAMSUNG LED CO., LTD.
314, Maetan-dong
Yeongtong-gu
Suwon-city
GYEONGGI-DO 443-370
Korea

The following merchandise was submitted and identified by the client as :

SGS File No.	: AYAA11-02161
Product Name	: HV_AC LED PKG
Item No./Part No.	: N/A
Received Date	: Jan 18, 2011
Test Period	: Jan 19, 2011 to Jan 20, 2011
Test Performed	: SGS Testing Korea tested the sample(s) selected by applicant with following results
Test Results	: For further details, please refer to following page(s)
Comments	: By the applicant's specific request, the sampling and testing was performed only for the part indicated in the photo without disassembly.

Timothy Jeon
Jinhee Kim
Cindy Park
Jerry Jung/ Testing Person

SGS Testing Korea Co. Ltd.



Jeff Jang / Chemical Lab Mgr

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Test Report No. F690501/LF-CTSAYAA11-02161

Issued Date: January 21, 2011

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Sample No. : AYAA11-02161.001
Sample Description : HV_AC LED PKG
Item No./Part No. : N/A
Comments : Materials are Copper, Silicone.

Heavy Metals

Test Items	Unit	Test Method	MDL	Results
Cadmium (Cd)	mg/kg	With reference to IEC 62321:2008, ICP	0.5	N.D.
Lead (Pb)	mg/kg	With reference to IEC 62321:2008, ICP	5	N.D.
Mercury (Hg)	mg/kg	With reference to IEC 62321:2008, ICP	2	N.D.
Hexavalent Chromium (Cr VI)	mg/kg	With reference to IEC 62321:2008, UV-VIS	1	N.D.

Flame Retardants-PBBs/PBDEs

Test Items	Unit	Test Method	MDL	Results
Monobromobiphenyl	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Dibromobiphenyl	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Tribromobiphenyl	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Tetrabromobiphenyl	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Pentabromobiphenyl	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Hexabromobiphenyl	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Heptabromobiphenyl	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Octabromobiphenyl	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Nonabromobiphenyl	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Decabromobiphenyl	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Monobromodiphenyl ether	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Dibromodiphenyl ether	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Tribromodiphenyl ether	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Tetrabromodiphenyl ether	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Pentabromodiphenyl ether	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Hexabromodiphenyl ether	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Heptabromodiphenyl ether	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Octabromodiphenyl ether	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Nonabromodiphenyl ether	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.
Decabromodiphenyl ether	mg/kg	With reference to IEC 62321:2008, GC-MS	5	N.D.

NOTE: (1) N.D. = Not detected, (<MDL)

(2) mg/kg = ppm

(3) MDL = Method Detection Limit

(4) - = No regulation

(5) ** = Qualitative analysis (No Unit)

(6) * = Boiling-water-extraction:

Negative = Absence of CrVI coating

Positive = Presence of CrVI coating; the detected concentration in boiling-water-extraction solution is equal or greater than 0.02 mg/kg with 50 cm2 sample surface area.

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Test Report No. F690501/LF-CTSAYAA11-02161

Issued Date: January 21, 2011

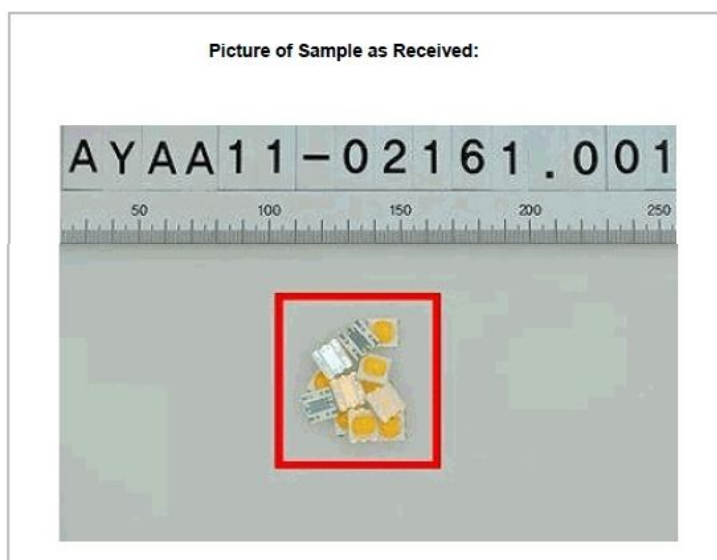
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Sample No. : AYAA11-02161.001
Sample Description : HV_AC LED PKG
Item No./Part No. : N/A
Comments : Materials are Copper, Silicone.

Halogen Contents

Test Items	Unit	Test Method	MDL	Results
Bromine(Br)	mg/kg	BS EN 14582:2007 , IC	30	N.D.
Chlorine(Cl)	mg/kg	BS EN 14582:2007 , IC	30	N.D.
Fluorine(F)	mg/kg	BS EN 14582:2007 , IC	30	N.D.
Iodine(I)	mg/kg	BS EN 14582:2007 , IC	50	N.D.

Picture of Sample as Received:



- NOTE:
- (1) N.D. = Not detected.(<MDL)
 - (2) mg/kg = ppm
 - (3) MDL = Method Detection Limit
 - (4) - = No regulation
 - (5) ** = Qualitative analysis (No Unit)
 - (6) * = Boiling-water-extraction:
 Negative = Absence of CrVI coating
 Positive = Presence of CrVI coating; the detected concentration in boiling-water-extraction solution is equal or greater than 0.02 mg/kg with 50 cm2 sample surface area.

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