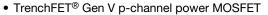
Vishay Siliconix

P-Channel 60 V (D-S) MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	-60			
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -10 \text{ V}$	0.007			
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -4.5 \text{ V}$	0.012			
Q _g typ. (nC)	31.7			
I _D (A) ^a	-90.9			
Configuration	Single			

FEATURES





 Very low R_{DS(on)} minimizes voltage drop and reduces conduction loss

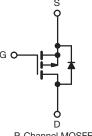
RoHS COMPLIANT HALOGEN **FREE**

• 100 % R_a and UIS tested

· Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

APPLICATIONS

- · Adapter and charger switch
- · Battery and circuit protection
- OR-ing
- · Load switch
- · Motor drive control



P-Channel MOSFET

ORDERING INFORMATION	
Package	PowerPAK SO-8
Lead (Pb)-free and halogen-free	SiR5607DP-T1-RE3
Lead (Pb)-free, halogen-free, BLR and IOL	SiR5607DP-T1-UE3

ABSOLUTE MAXIMUM RATING	S (T _A = 25 °C, u	nless other	wise noted)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V_{DS}	-60	V	
Gate-source voltage		V_{GS}	+20 / -20	V	
	T _C = 25 °C		-90.9		
Continuous drain current (T _J = 150 °C)	T _C = 70 °C	1	-72.7		
	T _A = 25 °C	I _D	-22.2 b, c		
	T _A = 70 °C		17.8 ^{b, c}	^	
Pulsed drain current (t = 100 μs)		I _{DM}	-250	A	
Continuous source-drain diode current	T _C = 25 °C	I _S	-94.6		
Continuous source-drain diode current	T _A = 25 °C		-5.7 ^{b, c}		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	-50		
Single pulse avalanche energy		E _{AS}	125	mJ	
	T _C = 25 °C		104		
Maximum nauvar disaination	T _C = 70 °C		66.6	W	
Maximum power dissipation		6.25 b, c	VV		
	T _A = 70 °C	1	4 b, c		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C	
Soldering recommendations (peak temperature) d, e			260		

THERMAL RESISTANCE RAT	INGS				
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient b	t ≤ 10 s	R _{thJA}	15	20	°C/W
Maximum junction-to-case (drain)	Steady state	Rthuc	0.9	1.2	C/VV

Notes

- a. T_C = 25 °C b. Surface mounted on 1" x 1" FR4 board
- See solder profile (www.vishav.com/doc?73257). The PowerPAK SO-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components Maximum under steady state conditions is 54 °C/W

Document Number: 62247

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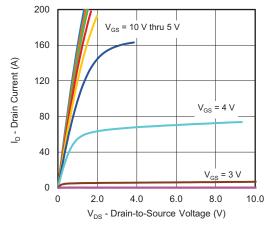
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static			•				
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-60	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	I _D = -10 mA	-	-34	-	>//06	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	-	5.1	-	mV/°C	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1.4	-	-2.6	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = +20 \text{ / } -20 \text{ V}$	-	=	100	nA	
Zero gate voltage drain current		V _{DS} = -60 V, V _{GS} = 0 V	-	-	-10		
	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V, T _J = 70 °C	-	-	-50	μA	
Darie de la contra del contra de la contra del la contra de la contra de la contra de la contra del la contra de la contra de la contra del la contra de la contra de la contra de la contra del la contra del la contra de la contra de la contra del la c	5	$V_{GS} = -10 \text{ V}, I_D = -20 \text{ A}$	-	0.0056	0.007	1	
Drain-source on-state resistance a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -20 \text{ A}$	-	0.0092	0.012	Ω	
Forward transconductance ^a	9 _{fs}	$V_{DS} = -15 \text{ V}, I_{D} = -20 \text{ A}$	-	52	-	S	
Dynamic ^b			•				
Input capacitance	C _{iss}		-	5020	-		
Output capacitance	C _{oss}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	2050	-	рF	
Reverse transfer capacitance	C _{rss}		-	85	-	"	
-	Qg	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -20 \text{ A}$	-	74	112		
Total gate charge			-	34.8	53	nC	
Gate-source charge	Q_{qs}	$V_{DS} = -30 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -20 \text{ A}$	-	18	-		
Gate-drain charge	Q _{gd}		-	10.1	-		
Gate resistance	R_g	f = 1 MHz	0.5	1.4	2.4	Ω	
Turn-on delay time	t _{d(on)}		-	18	36		
Rise time	t _r	V_{DD} = -30 V, R_L = 1.5 Ω , $I_D \cong$ -20 A,	-	9	18	1	
Turn-off delay time	t _{d(off)}	$V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$	-	40	80		
Fall time	t _f		-	9	18		
Turn-on delay time	t _{d(on)}		-	45	90	ns	
Rise time	t _r	V_{DD} = -30 V, R_L = 1.5 Ω , $I_D \cong$ -20 A,	-	320	640		
Turn-off delay time	t _{d(off)}	$V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$	-	30	60		
Fall time	t _f		-	20	40	1	
Drain-Source Body Diode Characteristi	cs		•				
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	-94.6		
Pulse diode forward current	I _{SM}		-	-	-250	A	
Body diode voltage	V_{SD}	I _S = -5 A, V _{GS} = 0 V	-	-0.76	-1.1	V	
Body diode reverse recovery time	t _{rr}		-	70	140	ns	
Body diode reverse recovery charge	Q _{rr}	I _F = -20 A, di/dt = 100 A/μs,	-	85	170	nC	
Reverse recovery fall time	t _a	T _J = 25 °C	-	36	-	İ	
Reverse recovery rise time	t _b		-	34	-	ns	

Notes

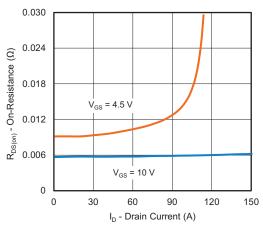
- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

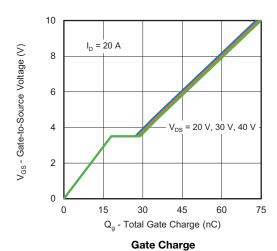


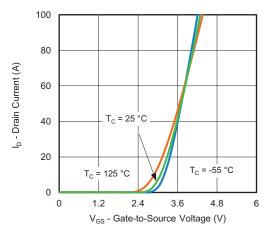


Output Characteristics

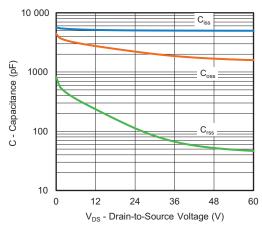


On-Resistance vs. Drain Current and Gate Voltage

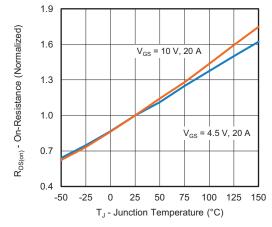




Transfer Characteristics

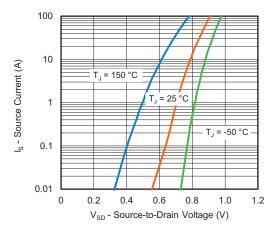


Capacitance

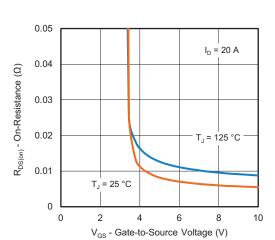


On-Resistance vs. Junction Temperature

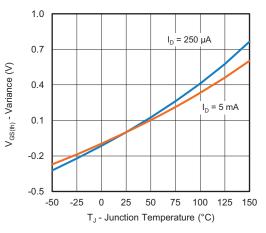




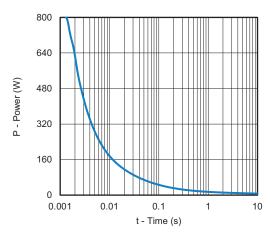
Source-Drain Diode Forward Voltage



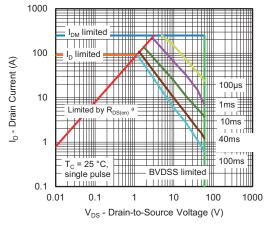
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient



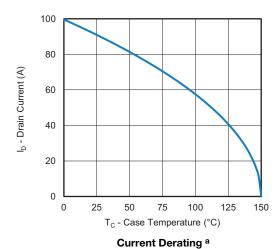
Safe Operating Area, Junction-to-Case

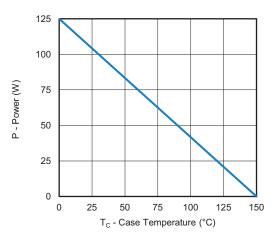
Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

S24-1131-Rev. C, 04-Nov-2024





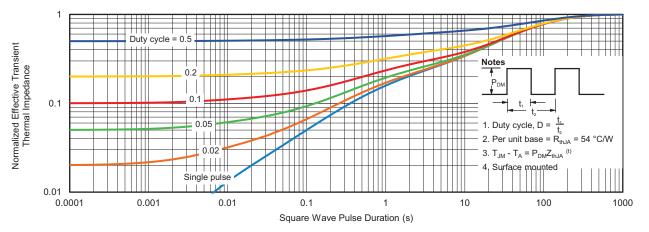


Power, Junction-to-Case

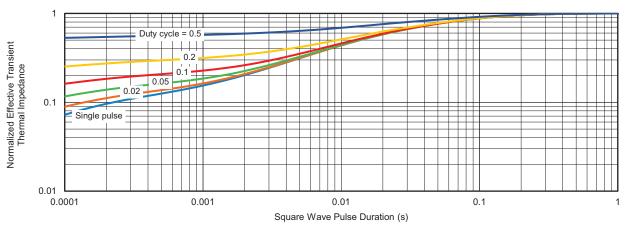
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient

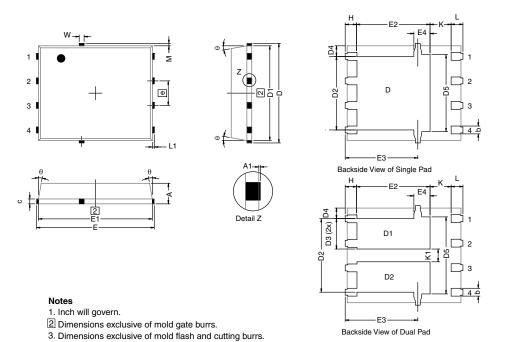


Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62247.

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PowerPAK® SO-8, (Single/Dual)



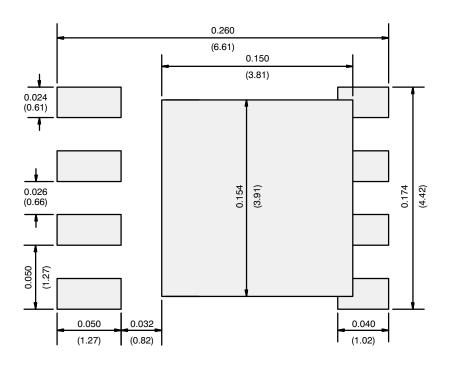
DIM	MILLIMETERS			INCHES			
DIM.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
Α	0.97	1.04	1.12	0.038	0.041	0.044	
A1		-	0.05	0	-	0.002	
b	0.33	0.41	0.51	0.013	0.016	0.020	
С	0.23	0.28	0.33	0.009	0.011	0.013	
D	5.05	5.15	5.26	0.199	0.203	0.207	
D1	4.80	4.90	5.00	0.189	0.193	0.197	
D2	3.56	3.76	3.91	0.140	0.148	0.154	
D3	1.32	1.50	1.68	0.052	0.059	0.066	
D4		0.57 typ. 0.0225 typ.					
D5		3.98 typ.		0.157 typ.			
E	6.05	6.15	6.25	0.238	0.242	0.246	
E1	5.79	5.89	5.99	0.228	0.232	0.236	
E2	3.48	3.66	3.84	0.137	0.144	0.151	
E3	3.68	3.78	3.91	0.145	0.149	0.154	
E4		0.75 typ.		0.030 typ.			
е		1.27 BSC		0.050 BSC			
K		1.27 typ.		0.050 typ.			
K1	0.56	=	-	0.022	-	=	
Н	0.51	0.61	0.71	0.020	0.024	0.028	
L	0.51	0.61	0.71	0.020	0.024	0.028	
L1	0.06	0.13	0.20	0.002	0.005	0.008	
θ	0°	-	12°	0°	-	12°	
W	0.15	0.25	0.36	0.006	0.010	0.014	
М		0.125 typ.			0.005 typ.		

Revison: 13-Feb-17 1 Document Number: 71655

DWG: 5881



RECOMMENDED MINIMUM PADS FOR PowerPAK® SO-8 Single



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

APPLICATION NOTE





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