

RGS40NL65DHRBTL

650V 20A Field Stop Trench IGBT

V _{CES}	650V
Ι _C	20A
V _{CE(sat) (Typ.)}	1.65V
P _D	177W

Features

- 1) Qualified to AEC-Q101
- 2) Low Collector Emitter Saturation Voltage
- 3) Short Circuit Withstand Time 8µs
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating ; RoHS Compliant

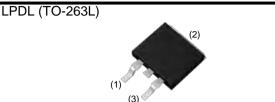
Application

General Inverter

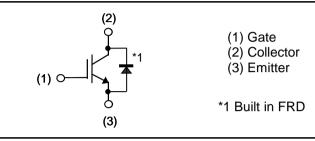
for Automotive and Industrial Use

Heater for Automotive

●Outline



Inner Circuit



Packaging Specifications

	Packaging	Taping
	Reel Size (mm)	330
Tuno	Tape Width (mm)	24
Туре	Basic Ordering Unit (pcs)	1,000
	Packing Code	TL
	Marking	RGS40NL65D

•Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V _{GES}	±30	V
Collector Current	$T_{\rm C} = 25^{\circ}{\rm C}$	Ι _C	42	А
Collector Current	$T_{\rm C} = 100^{\circ}{\rm C}$	Ι _C	28	Α
Pulsed Collector Current		I _{CP} ^{*1}	60	А
Diode Forward Current	$T_{\rm C} = 25^{\circ}{\rm C}$	١ _F	43	Α
	$T_{\rm C} = 100^{\circ}{\rm C}$	I _F	25	Α
Diode Pulsed Forward Current		I _{FP} ^{*1}	60	Α
$T_c = 25^{\circ}C$		P _D	177	W
Power Dissipation	$T_{\rm C} = 100^{\circ}{\rm C}$	P _D	88	W
Operating Junction Temperature		Tj	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

*1 Pulse width limited by T_{jmax.}

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•Thermal Resistance

Parameter	Symbol	Values			Linit
Farameter	Symbol	Min.	Тур.	Max.	Unit
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	0.85	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	1.55	°C/W

•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol Conditions -			Unit		
r arameter			Min.	Тур.	Max.	Unit
Collector - Emitter Breakdown Voltage	BV _{CES}	I_{C} = 10µA, V_{GE} = 0V	650	-	-	V
		$V_{CE} = 650V, V_{GE} = 0V,$				
Collector Cut - off Current	I_{CES}	T _j = 25°C	-	-	10	μA
		Tj = 175°C	-	0.1	-	mA
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 1.0mA$	5.0	6.0	7.0	V
		$I_{C} = 20A, V_{GE} = 15V,$				
Collector - Emitter Saturation Voltage	V _{CE(sat)}	T _j = 25°C	-	1.65	2.10	V
		T _j = 175°C	-	2.15	-	V



Doromotor	Symbol	Conditions		L I.a. it		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	V _{CE} = 30V,	-	881	-	pF
Output Capacitance	C _{oes}	$V_{GE} = 0V,$	-	55	-	
Reverse transfer Capacitance	C _{res}	f = 1MHz	-	7	-	
Total Gate Charge	Qg	V _{CE} = 400V,	-	28	-	
Gate - Emitter Charge	Q _{ge}	I _C = 20A,	-	7	-	nC
Gate - Collector Charge	Q _{gc}	V _{GE} = 15V	-	11	-	
Turn - on Delay Time	t _{d(on)}		-	24	-	
Rise Time	t _r	$I_{C} = 20A, V_{CC} = 400V,$ $V_{GE} = 15V, R_{G} = 10\Omega,$	-	12	-	ns
Turn - off Delay Time	t _{d(off)}	$V_{GE} = 15V, R_G = 10\Omega,$ $T_i = 25^{\circ}C$	-	87	-	
Fall Time	t _f	Inductive Load	-	89	-	
Turn - on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.56	-	mJ
Turn - off Switching Loss	E _{off}		-	0.49	-	
Turn - on Delay Time	t _{d(on)}		-	24	-	
Rise Time	t _r	$I_{C} = 20A, V_{CC} = 400V,$ $V_{GE} = 15V, R_{G} = 10\Omega,$	-	15	-	ns
Turn - off Delay Time	t _{d(off)}	$T_i = 175^{\circ}C$	-	104	-	
Fall Time	t _f	Inductive Load	-	114	-	
Turn - on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.60	-	~
Turn - off Switching Loss	E _{off}		-	0.65	-	mJ
Reverse Bias Safe Operating Area	RBSOA	$I_{C} = 60A, V_{CC} = 520V,$ $V_{P} = 650V, V_{GE} = 15V,$ $R_{G} = 50\Omega, T_{j} = 175^{\circ}C$	FULL SQUARE		-	
Short Circuit Withstand Time	t _{sc}	V _{CC} ≤ 360V, V _{GE} = 15V, T _j = 25°C	8	-	-	μs
Short Circuit Withstand Time	t _{sc} *2	V _{CC} ≤ 360V, V _{GE} = 15V, T _j = 150°C	6	-	-	μs

*2 Design assurance without measurement

•FRD Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			L Insit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
		I _F = 20A,				
Diode Forward Voltage	V _F	T _j = 25°C	-	1.45	1.9	V
		T _j = 175°C	-	1.6	-	
Diode Reverse Recovery Time	t _{rr}	$I_F = 20A,$ $V_{CC} = 400V,$ $di_F/dt = 200A/\mu s,$ $T_j = 25^{\circ}C$	-	93	-	ns
Diode Peak Reverse Recovery Current	I _{rr}		-	6.5	-	A
Diode Reverse Recovery Charge	Q _{rr}		-	0.33	-	μC
Diode Reverse Recovery Energy	E _{rr}		-	14	-	μJ
Diode Reverse Recovery Time	t _{rr}	I _F = 20A, V _{CC} = 400V, di _F /dt = 200A/μs, T _j = 175°C	-	124	-	ns
Diode Peak Reverse Recovery Current	I _{rr}		-	7.7	-	A
Diode Reverse Recovery Charge	Q _{rr}		-	0.58	-	μC
Diode Reverse Recovery Energy	E _{rr}		-	30	-	μJ



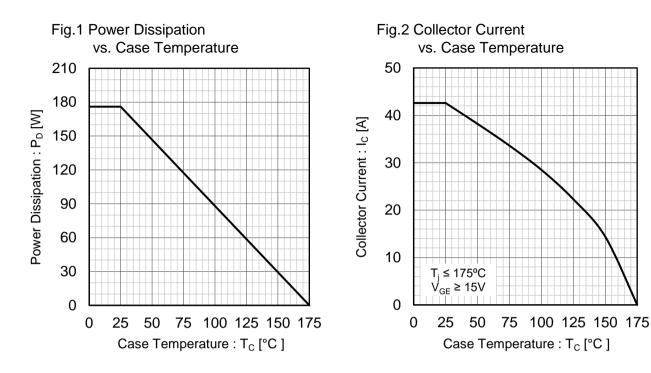
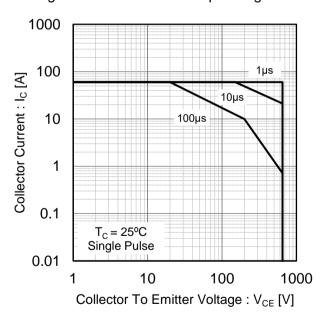
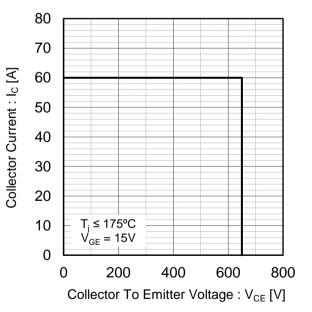


Fig.3 Forward Bias Safe Operating Area







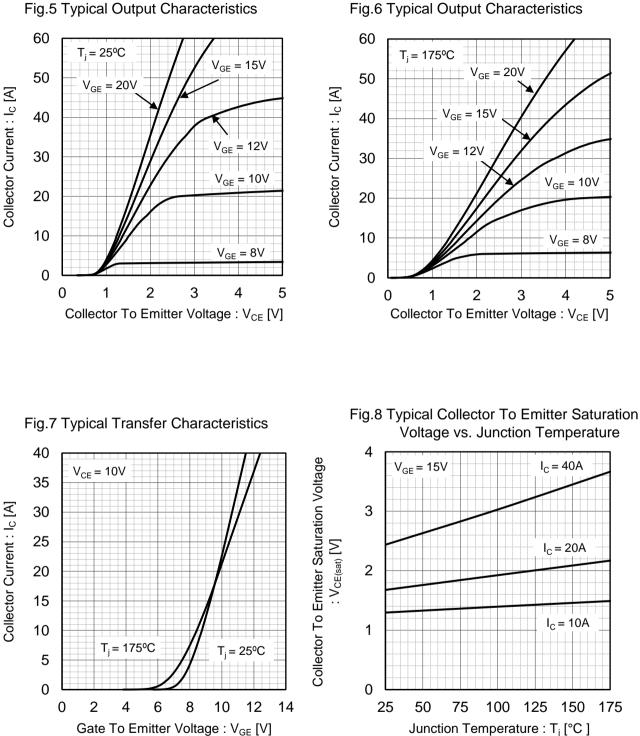
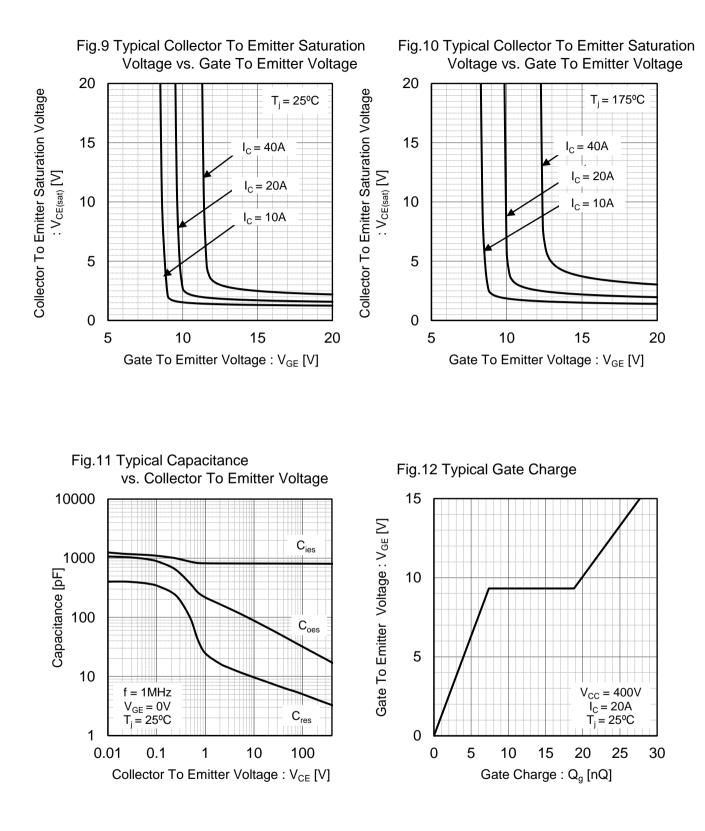
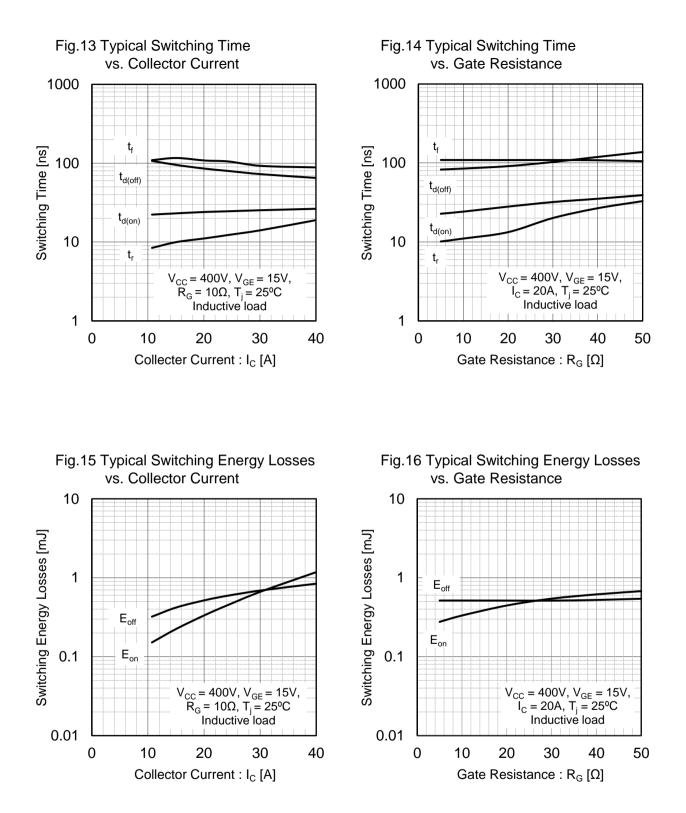
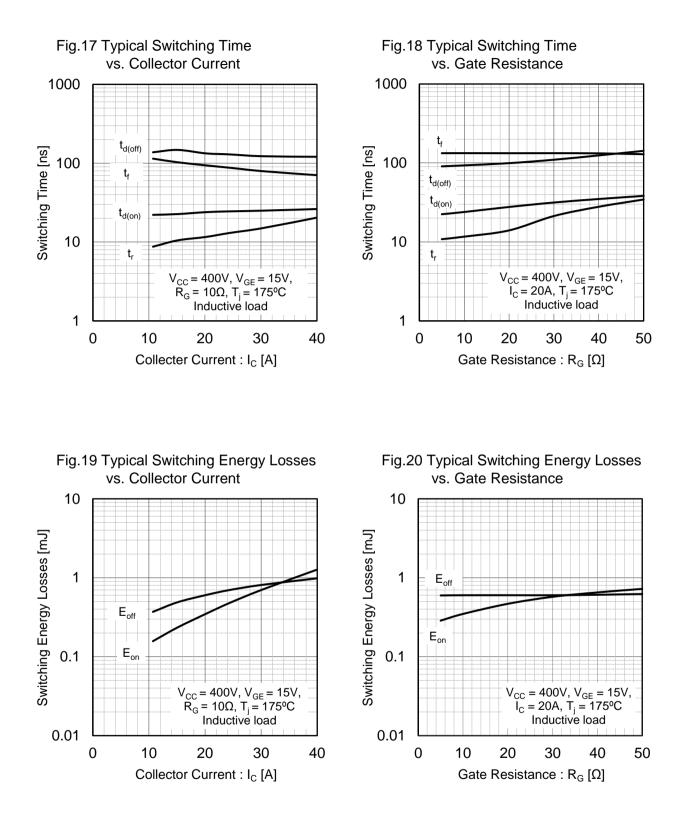


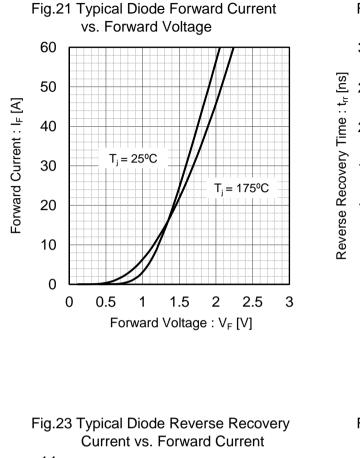
Fig.6 Typical Output Characteristics

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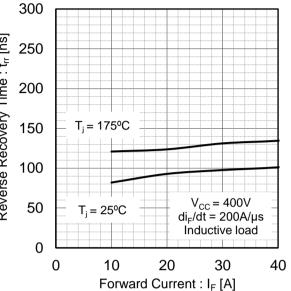












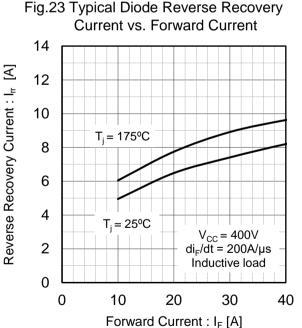
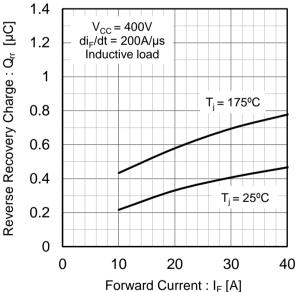


Fig.24 Typical Diode Rrverse Recovery Charge vs. Forward Current



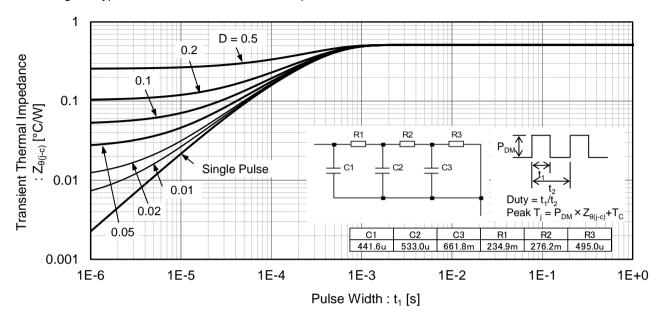
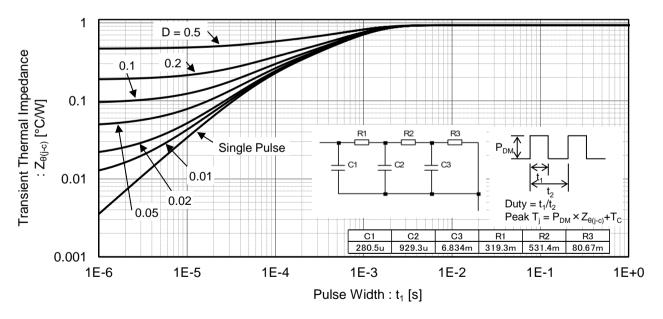


Fig.25 Typical IGBT Transient Thermal Impedance

Fig.26 Typical Diode Transient Thermal Impedance



Inductive Load Switching Circuit and Waveform

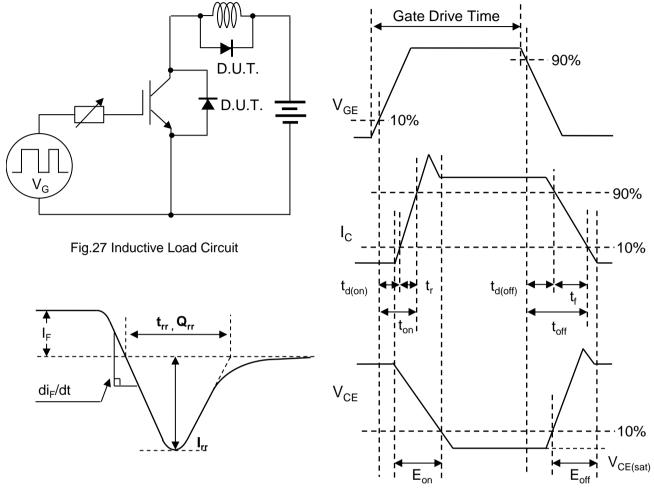


Fig.29 Diode Reverse Recovery Waveform

Fig.28 Inductive Load Waveform



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