

# MULTILAYER CERAMIC LOW PASS FILTER RCF-915.000-26000-2012-W-TR

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| Items                       |                 | Specification     | Unit |
|-----------------------------|-----------------|-------------------|------|
| Frequency Range             |                 | 902.000 ~ 928.000 | MHz  |
| Insertion Loss, max         |                 | 0.6               | dB   |
| Attonuation min             | 1804 ~ 1856 MHz | 30                | dB   |
| Attenuation, min            | 2706 ~ 2774 MHz | 30                | dB   |
| VSWR, max                   |                 | 2                 | -    |
| Impedance                   |                 | 50                | Ω    |
| Operating Temperature Range |                 | -40 ~ +85         | °C   |
| Storage Temperature Range   |                 | -40 ~ +85         | °C   |

#### **Dimension**

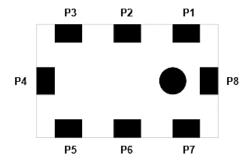
| Figure          | Symbol | Dimension (mm) |
|-----------------|--------|----------------|
| <u> </u>        | L      | 2.00 ± 0.15    |
|                 | W      | 1.25 ± 0.15    |
| Top view W      | Т      | 0.95 ± 0.15    |
| F T             | A      | 0.20 ± 0.15    |
|                 | В      | 0.30 ± 0.15    |
| Bottom view     | С      | 0.35 ± 0.15    |
| E               | D      | 0.65 ± 0.15    |
| D CBA Side view | E      | 0.20 ± 0.15    |
| Side view       | F      | 0.20 ± 0.15    |
|                 | G      | 0.475 ± 0.15   |
|                 | Н      | 0.30 ± 0.15    |



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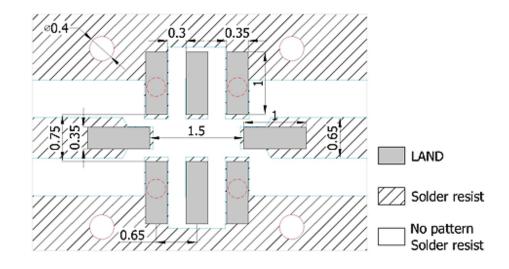
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#### **Pin Configuration**



| PIN | Definition     | PIN | Definition     |
|-----|----------------|-----|----------------|
| P1  | Ground         | P5  | Ground         |
| P2  | NC             | Р6  | NC             |
| Р3  | Ground         | Р7  | Ground         |
| P4  | Input / output | Р8  | Input / output |

#### **Soldering Pattern**



Unit: mm

Line width to be designed to match 50  $\Omega$  characteristic impedance, depending on PCB material and thickness.

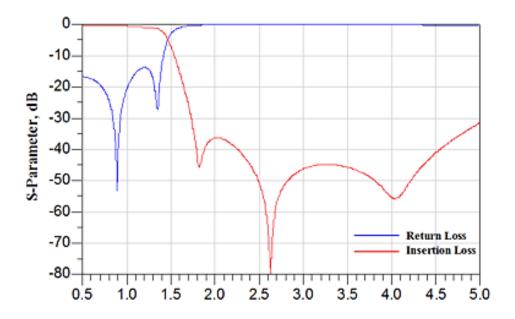
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#### **Frequency Characteristics**



#### APPROVAL

| DRAWN BY    | LP, October 28, 2017 |
|-------------|----------------------|
| APPROVED BY | JI, October 28, 2017 |
| REVISION    | A, Initial Release   |

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