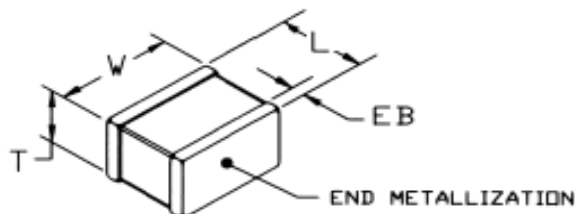


ROHS COMPLIANT



SPECIFICATIONS:

ELECTRICAL CHARACTERISTICS:

DIELECTRIC	X7R
RATED VOLTAGE	50 VDC
CAPACITANCE RANGE (EIA)	10 nF (103)
CAPACITANCE TOLERANCE	K ($\pm 10\%$)
TEST PARAMETERS	1 kHz ± 50 Hz @ 1.0 VRMS, 25°C
TEMPERATURE COEFFICIENT	$\pm 15\%$ (-55°C TO +125°C)
DISSIPATION FACTOR	< 3.5% @ 1kHz, 25°C
INSULATION RESISTANCE	> 500 Ohm-Farad OR 50 G-Ohms, whichever is less @ WVDC, 25°C (@ 125°C IR is 10% of 25°C requirement)
DIELECTRIC STRENGTH	> 2.5 x WVDC, 50 mA max

MECHANICAL CHARACTERISTICS:

LENGTH "L"	WIDTH "W"	THICKNESS "T"	ENDBAND "EB"
.062 $\pm$.010	.125 $\pm$.010	.050 MAX	.020 $\pm$.010

TERMINATION DESCRIPTION:

TERMINATION: **P** - PALLADIUM/SILVER

PACKAGING:

E - EMBOSSED 7" PLASTIC TAPE&REEL

MARKING DESCRIPTION

MARK: **4** - NO MARKING

				JOHANSON DIELECTRICS		15191 BLEDSOE ST SYLMAR, CA 91342		
				TITLE				
REV	DESCRIPTION		APPROVED	DATE	LOW INDUCTANCE CHIP CAPACITOR			
ENG. MGR.		DATE	Q.A. MGR.	DATE	PART NO. 500B18W103KP4E		DWG SCALE NONE	
DRN BY J. VARELA		DATE 12/08/16	CHKD BY	DATE	DWG. NO. 500B18W103KP4E		SHEET 1 OF 1	REV A