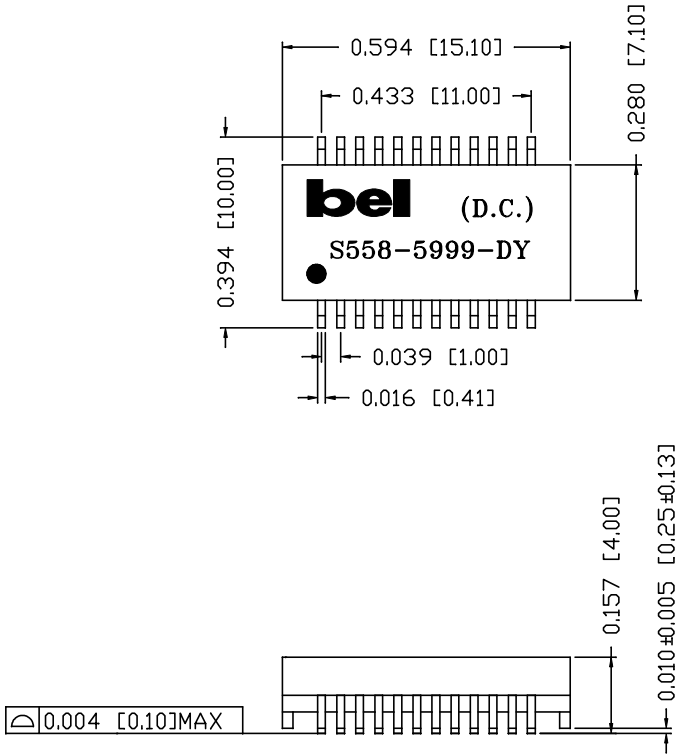
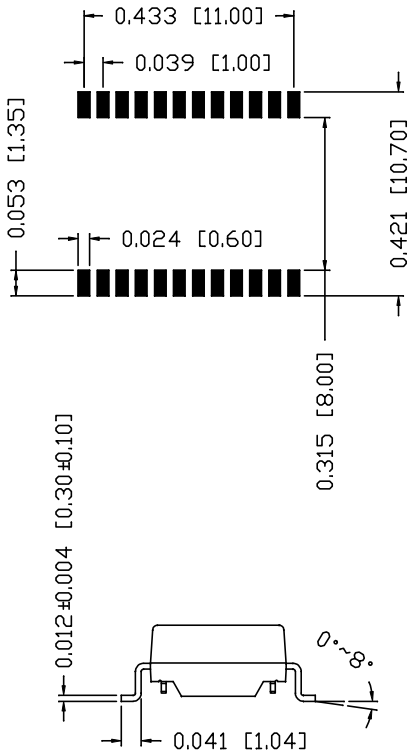


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RoHS



SUGGESTED PCB PAD LAYOUT



NOTES:

1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
2. PACKAGE CODE: "CQS001".

ORIGINATED BY
SHEA
DATE 2024-04-23
DRAWN BY
CHEN ZHIJUN
DATE 2024-04-23

TITLE
1000Base-T
Transformer module

PART NO. / DRAWING NO.
S558-5999-DY
FILE NAME
S558-5999-DY.DWG

STANDARD DIM.
TOL. IN INCH
.X
.XX ±0.01
.XXX ±0.005
[] METRIC DIM.
AS REF.
UNIT : INCH [mm]
SCALE : N/A
SIZE : A4

REV. : A PAGE : 1/2



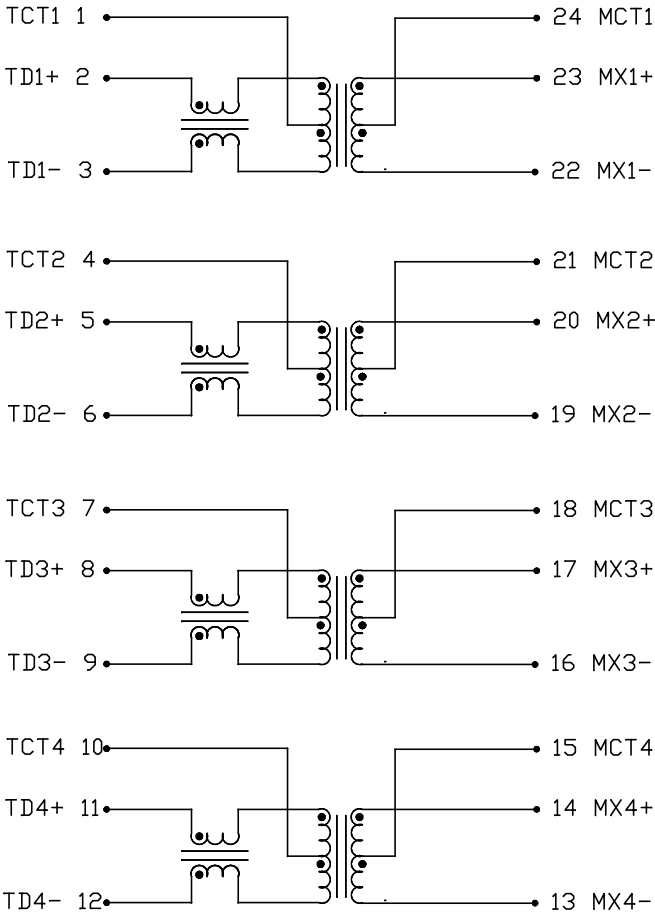
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ELECTRICAL CHARACTERISTICS @ 25°C

SCHEMATIC



TURNS RATIO:
 PIN(22-24-23) : (3-2-1)
 PIN(19-20-21) : (6-5-4)
 PIN(16-17-18) : (9-8-7)
 PIN(13-14-15) : (12-11-10)
 1CT:1CT±3% @100KHz, 100mV
 1CT:1CT±3% @100KHz, 100mV
 1CT:1CT±3% @100KHz, 100mV
 1CT:1CT±3% @100KHz, 100mV

POLARITY: PER DOT CONVENTION

OCL (0°C~70°C):
 PIN(22-23)=(19-20)=(16-17)=(13-14) @100kHz, 100mV WITH 8mA DC BIAS 350 H/MIN.

DCR RESISTANCE:
 PIN(3-2)=(6-5)=(9-8)=(12-11) 1.2 OHM MAX.

INSERTION LOSS: 1.0dB MAX. @ 1 TO 100MHz

RETURN LOSS:
 18.0dB MIN. @ 1 TO 30MHz
 14.4dB MIN. @ 40MHz
 13.1dB MIN. @ 50MHz
 12.0dB MIN. @ 60 TO 80MHz
 10.0dB MIN. @ 100MHz

DCMR:
 40.0dB MIN. @ 30MHz
 35.0dB MIN. @ 60MHz
 30.0dB MIN. @ 100MHz

CROSSTALK:
 40.0dB MIN. @ 30MHz
 35.0dB MIN. @ 60MHz
 30.0dB MIN. @ 100MHz

HIPOT:
 LEAKAGE CURRENT LESS THAN
 1mA AT 1500VAC FOR 2SEC.

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 SHEA
 DATE 2024-04-23
 DRAWN BY
 CHEN ZHIJUN
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TITLE
 1000Base-T
 Transformer module

PART NO. / DRAWING NO.
 S558-5999-DY
 FILE NAME
 S558-5999-DY.DWG

STANDARD DIM.
 TOL. IN INCH
 .X
 .XX
 .XXX
 [] METRIC DIM.
 AS REF.
 UNIT : INCH [mm]
 SCALE : N/A
 SIZE : A4

REV. : A PAGE : 2/2

 a bel group