



#### **DUAL N-CHANNEL ENHANCEMENT MODE MOSFET**

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> T <sub>A</sub> = +25°C	
20V	$0.55\Omega$ @ $V_{GS} = 4.5V$	540mA	

#### **Features**

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High-Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMN2004DWKQ</u>)

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Load Switch

#### **Mechanical Data**

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Alloy 42 Lead-Frame. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)





**SOT363** 

Top View Internal Schematic

Top View

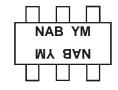
### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN2004DWK-7	SOT363	3.000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



NAB = Product Type Marking Code YM = Date Code Marking  $\overline{Y}$  or Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2006	2007		2020	2021	2022	2023	2024	2025	2026	2027	2028
Code	Т	U		Н		J	K	L	М	N	0	Р
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

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## Maximum Ratings (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

Chara	cteristic		Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	20	V
Gate-Source Voltage			V <sub>GSS</sub>	±8	V
Drain Current (Note 5)	Steady State	$T_A = +25$ °C $T_A = +85$ °C	I <sub>D</sub>	540 390	mA
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	1.5	A

# Thermal Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	200	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	625	°C/W
Operating and Storage Temperature Range	$T_{J_i}T_{STG}$	-55 to +150	°C

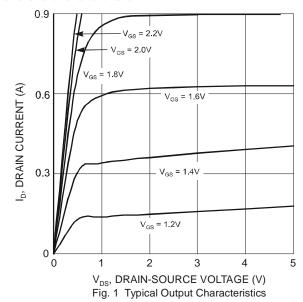
## Electrical Characteristics (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	$BV_{DSS}$	20	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μA	$V_{DS} = 16V$ , $V_{GS} = 0V$		
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±1	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.5		1.0	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$		
			0.4	0.55		$V_{GS} = 4.5V, I_D = 540mA$		
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	0.5	0.70	Ω	$V_{GS} = 2.5V, I_D = 500mA$		
			0.7	0.9		$V_{GS} = 1.8V, I_D = 350mA$		
Forward Transfer Admittance	Y <sub>fs</sub>	200		_	mS	$V_{DS} = 10V, I_D = 0.2A$		
Diode Forward Voltage (Note 7)	$V_{SD}$	0.5	_	1.4	V	$V_{GS} = 0V, I_{S} = 115mA$		
DYNAMIC CHARACTERISTICS (Note 7)								
Input Capacitance	C <sub>iss</sub>	_	36	150	pF			
Output Capacitance	Coss		5.7	25	pF	$V_{DS} = 16V, V_{GS} = 0V$ f = 1.0MHz		
Reverse Transfer Capacitance	$C_{rss}$		4.2	20	pF	1 = 1.01/11/12		
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	0.53	_				
Total Gate Charge (V <sub>GS</sub> = 8.0V)	Qg	_	0.95	_	nC	V 40V I 250mA		
Gate-Source Charge	Q <sub>gs</sub>	_	0.08	_	nc nc	$V_{DS} = 10V, I_{D} = 250mA$		
Gate-Drain Charge	$Q_{gd}$	_	0.07	_				
Turn-On Delay Time	t <sub>D(ON)</sub>	_	4.1	_	ns			
Turn-On Rise Time	t <sub>R</sub>	_	7.3	_	ns	$V_{DD} = 10V, R_L = 47\Omega,$		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	13.8		ns	$V_{GEN} = 4.5V$ , $R_{GEN} = 10\Omega$		
Turn-Off Fall Time	t <sub>F</sub>		10.5	_	ns			

Notes:

- 5. Device mounted on FR-4 PCB.
- 6. Pulse width ≤10µs, Duty Cycle ≤1%.
- 7. Short duration pulse test used to minimize self-heating effect.





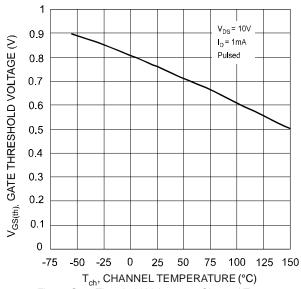


Fig. 3 Gate Threshold Voltage vs. Channel Temperature

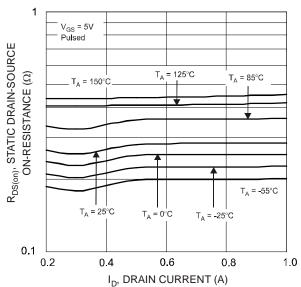
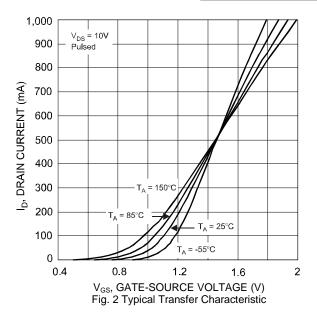


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current



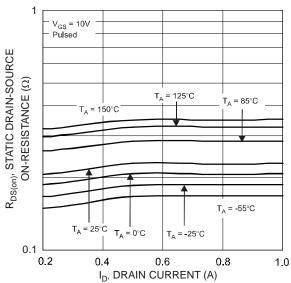


Fig. 4 Static Drain-Source On-Resistance Vs. Drain Current

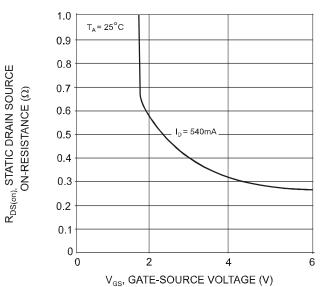


Fig. 6 Static Drain-Source, On-Resistance vs. Gate-Source Voltage



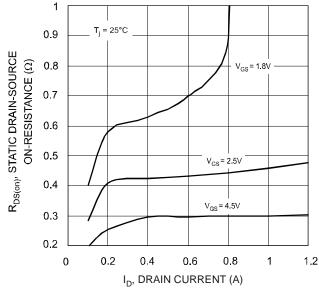


Fig. 7 On-Resistance vs. Drain Current and Gate Voltage

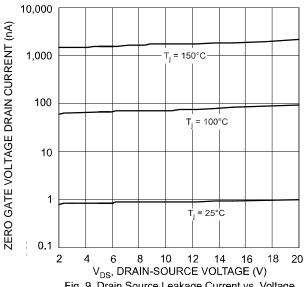


Fig. 9 Drain Source Leakage Current vs. Voltage

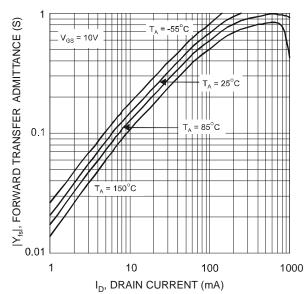


Fig. 11 Forward Transfer Admittance vs. Drain Current

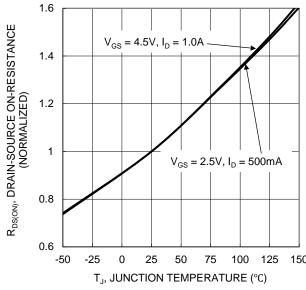
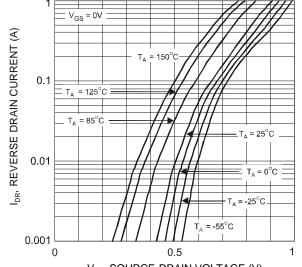
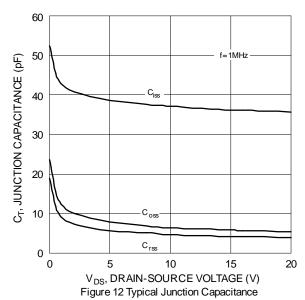


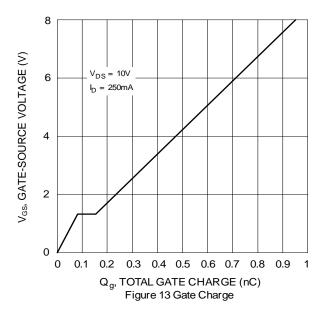
Fig. 8 On-Resistance Variation with Temperature

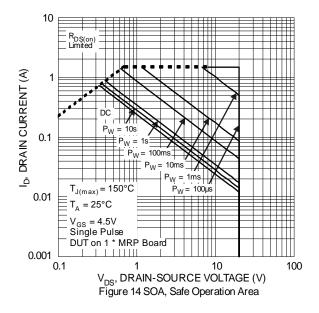


V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V) Fig. 10 Reverse Drain Current vs. Source-Drain Voltage





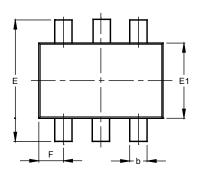


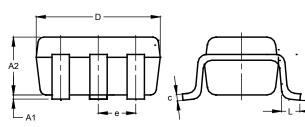




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

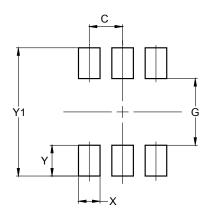




SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	1.00				
b	0.10	0.30	0.25				
С	0.10	0.22	0.11				
D	1.80 2.20	2.20	2.15				
E	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	0	).650 B	SC				
F	0.40	0.45	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

# **Suggested Pad Layout**

 $\label{prop:lease} Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$ 



Dimensions	Value (in mm)		
С	0.650		
G	1.300		
Х	0.420		
Y	0.600		
Y1	2.500		



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