

$V_{DSS}(@T_j \text{ max.})^{*6}$	650V
$R_{DS(on)}(\text{Max.})$	82mΩ
I_{DP}^{*2}	±147A
P_D	448W

●Features

- 1) Low on-resistance
- 2) Fast switching
- 3) Drive circuits can be simple
- 4) Pb-free plating ; RoHS compliant
- 5) Halogen free mold compound

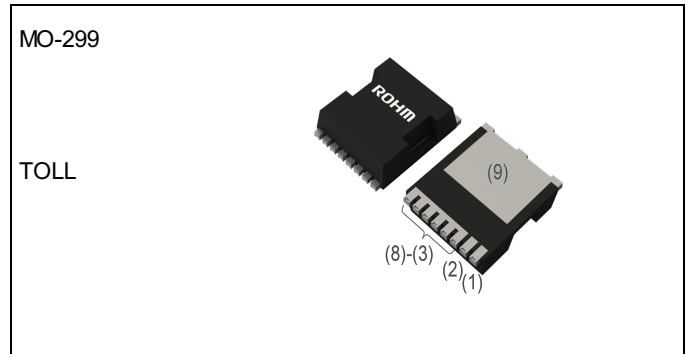
●Application

Switching applications

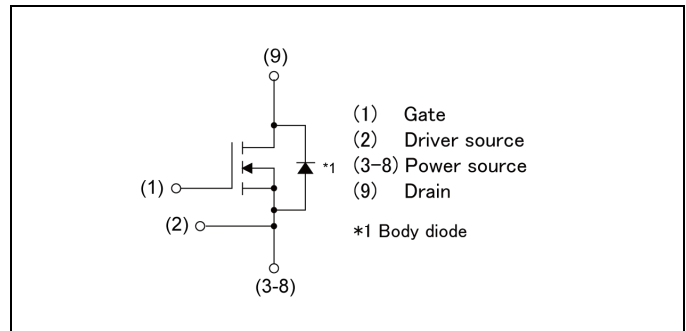
●Absolute maximum ratings ($T_a = 25^\circ\text{C}$,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	600	V
Continuous drain current ($T_c = 25^\circ\text{C}$)	I_D^{*1}	±49	A
Pulsed drain current	I_{DP}^{*2}	±147	A
Gate - Source voltage	V_{GSS}	±30	V
Avalanche current, single pulse	I_{AS}^{*3}	2.2	A
Avalanche energy, single pulse	E_{AS}^{*3}	132	mJ
MOSFET dv/dt	dv/dt^{*4}	120	V/ns
Power dissipation ($T_c = 25^\circ\text{C}$)	P_D	448	W
Junction temperature	T_j	150	°C
Operating junction and storage temperature range	T_{stg}	-55 to +150	°C

●Outline



●Inner circuit



●Packaging specifications

Marking	R6049XNJ2
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● Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - case	R_{thJC}	-	-	0.28	°C/W
Thermal resistance, junction - ambient	R_{thJA}^{*5}	-	-	110	°C/W
Soldering temperature, wavesoldering for 10s	T_{sold}	-	-	265	°C

● Electrical characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 1mA$	600	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 600V, V_{GS} = 0V$	-	-	100	μA
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	± 100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 2.9mA$	3	4	5	V
Static drain - source on - state resistance	$R_{DS(on)}^{*6}$	$V_{GS} = 10V, I_D = 11A$	-	68	82	m Ω
Gate resistance	R_G	$f = 1MHz, \text{open drain}$	-	1.4	-	Ω

●Electrical characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input capacitance	C_{iss}	$V_{GS} = 0V$	-	2418	-	pF
Output capacitance	C_{oss}	$V_{DS} = 100V$	-	107	-	
Effective output capacitance energy related	$C_{o(er)}^{*7}$	$V_{GS} = 0V$	-	98	-	
Effective output capacitance time related	$C_{o(tr)}^{*8}$	$V_{DS} = 0V \text{ to } 480V$	-	628	-	
Turn - on delay time	$t_{d(on)}^{*6}$	$V_{DD} \approx 300V, V_{GS} = 10V$	-	38	-	ns
Rise time	t_r^{*6}	$I_D = 11A$	-	30	-	
Turn - off delay time	$t_{d(off)}^{*6}$	$R_L \approx 27\Omega$	-	92	-	
Fall time	t_f^{*6}	$R_G = 10\Omega$	-	20	-	

●Gate charge characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Total gate charge	Q_g^{*6}	$V_{DD} \approx 300V$	-	58	-	nC
Gate - Source charge	Q_{gs}^{*6}	$I_D = 11A$	-	15	-	
Gate - Drain charge	Q_{gd}^{*6}	$V_{GS} = 10V$	-	27	-	
Gate plateau voltage	$V_{(plateau)}$	$V_{DD} \approx 300V, I_D = 11A$	-	6.4	-	V

●Body diode electrical characteristics (Source-Drain) ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Source current	I_S^{*1}	$T_C = 25^\circ\text{C}$	-	-	49	A
Pulsed source current	I_{SP}^{*2}		-	-	147	A
Source-Drain voltage	V_{SD}^{*6}	$V_{GS} = 0\text{V}, I_S = 11\text{A}$	-	-	1.5	V
Reverse recovery time	t_{rr}^{*6}	$V_{DD} \approx 400\text{V}$ $I_S = 11\text{A}$ $di/dt = 100\text{A}/\mu\text{s}$	-	306	-	ns
Reverse recovery charge	Q_{rr}^{*6}		-	4.1	-	μC
Peak reverse recovery current	I_{rr}^{*6}		-	27	-	A

*1 Limited only by maximum channel temperature allowed.

*2 $P_w \leq 10\mu\text{s}$, Duty cycle $\leq 1\%$

*3 $L \approx 50\text{mH}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, starting $T_j = 25^\circ\text{C}$

*4 $V_{DS} = 0$ to 400V

*5 Mounted on an epoxy PCB FR4 (20mm×25mm×0.8mm)

*6 Pulsed

*7 $Co(er)$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS} .

*8 $Co(tr)$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS} .

●Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

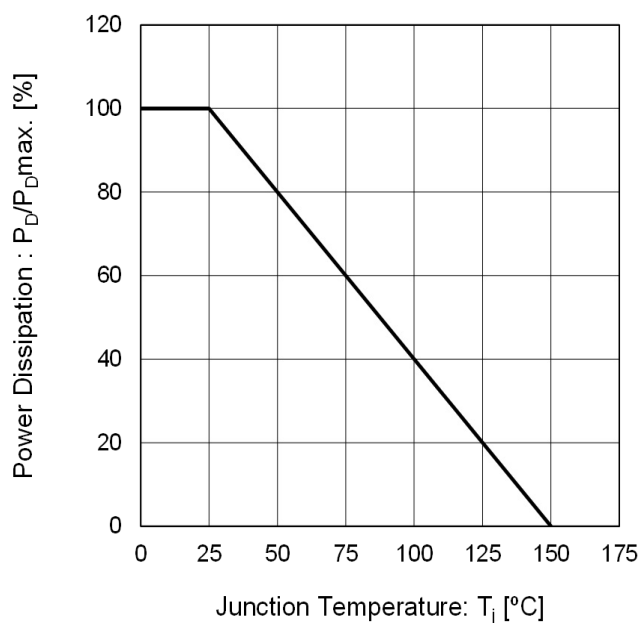


Fig.2 Drain Current Derating Curve

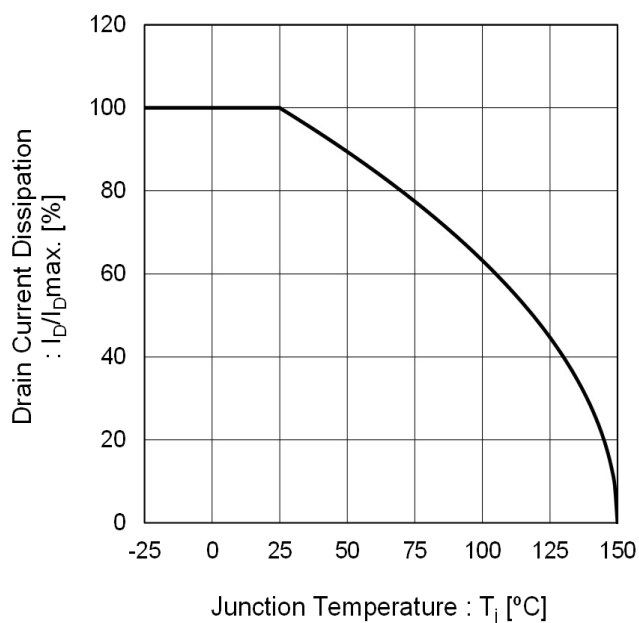


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

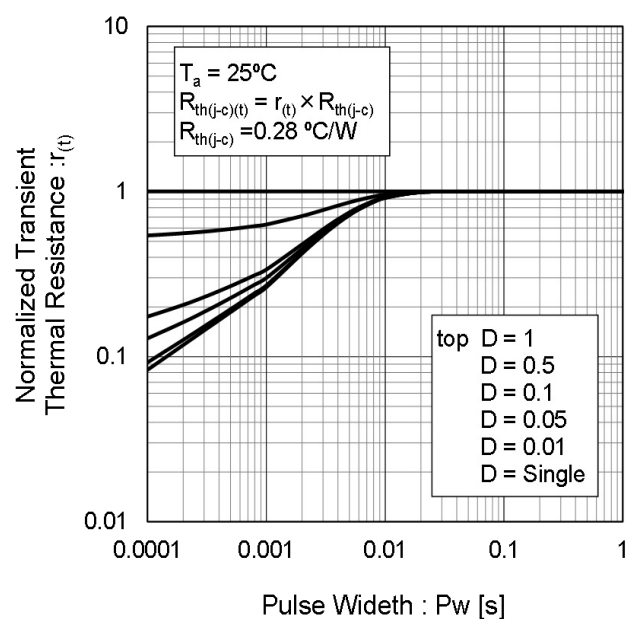
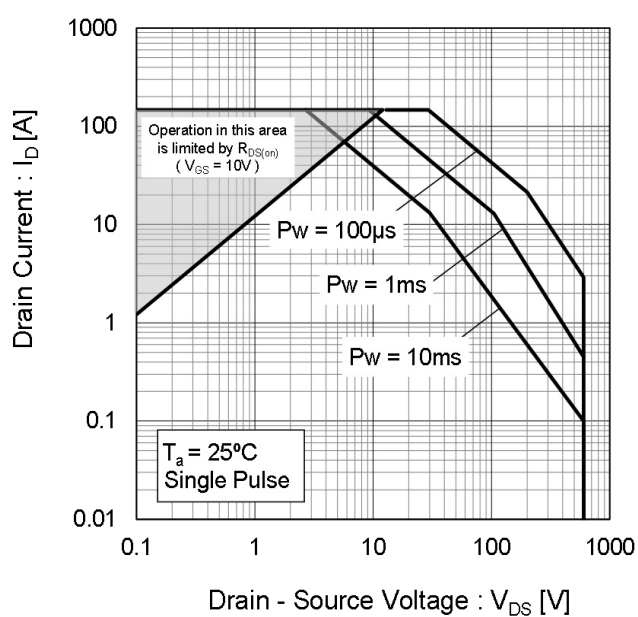


Fig.4 Maximum Safe Operating Area



●Electrical characteristic curves

Fig.5 Avalanche Energy Derating Curve vs. Junction Temperature

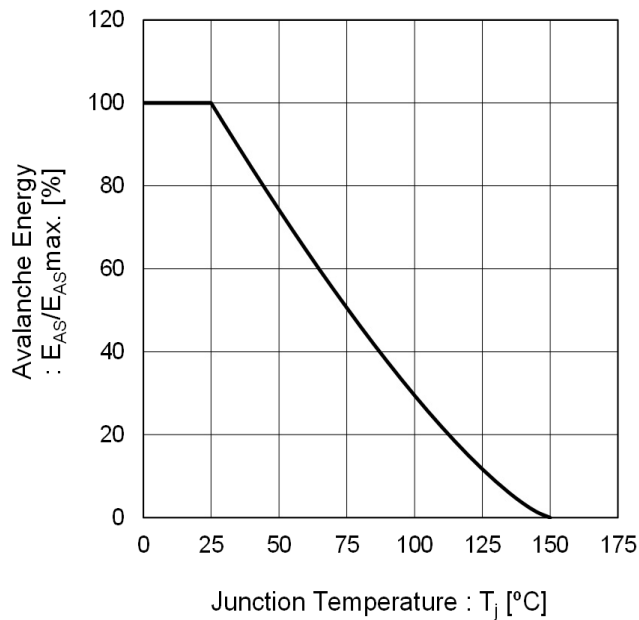


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

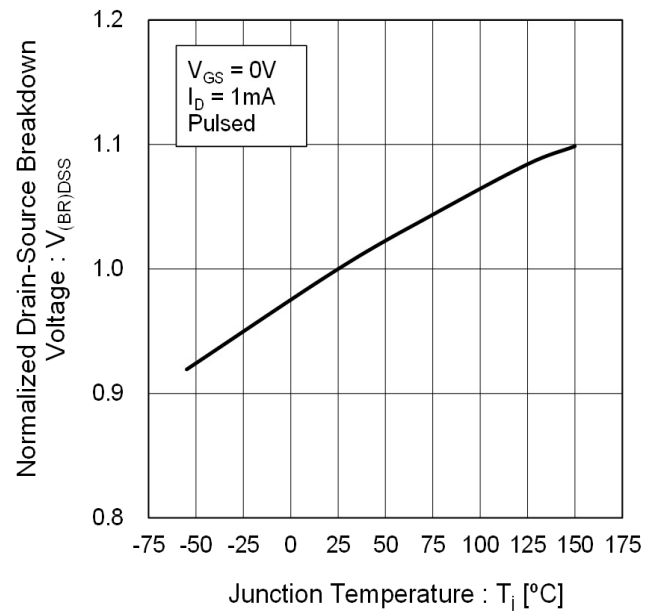


Fig.7 Typical Output Characteristics(I)

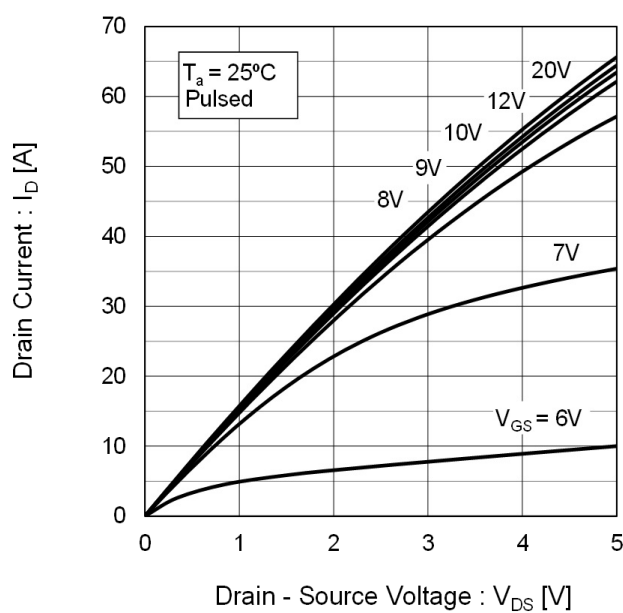
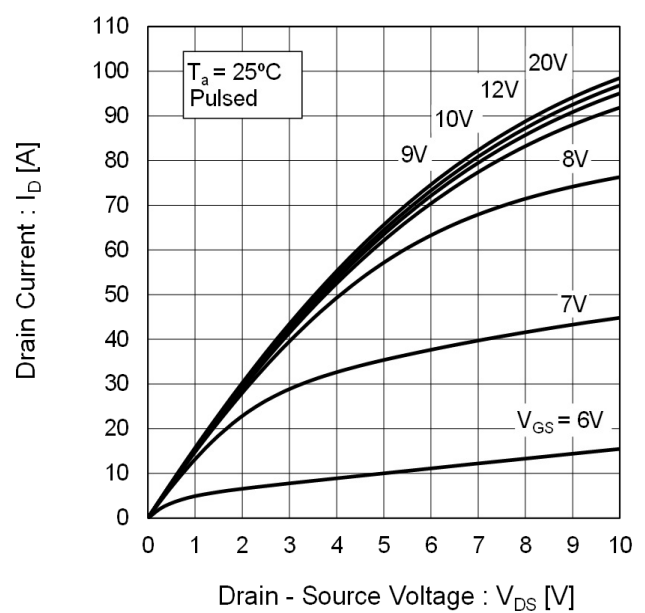


Fig.8 Typical Output Characteristics(II)



●Electrical characteristic curves

Fig.9 Gate Threshold Voltage
vs. Drain current

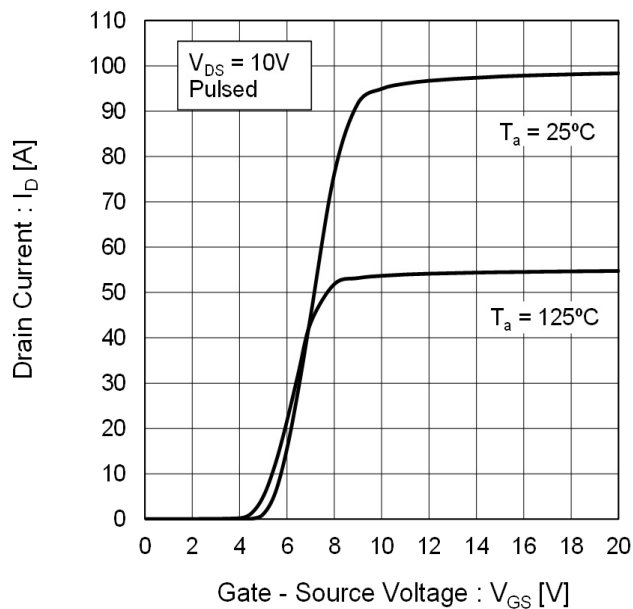


Fig.10 Normalized Gate Threshold
Voltage vs. Junction Temperature

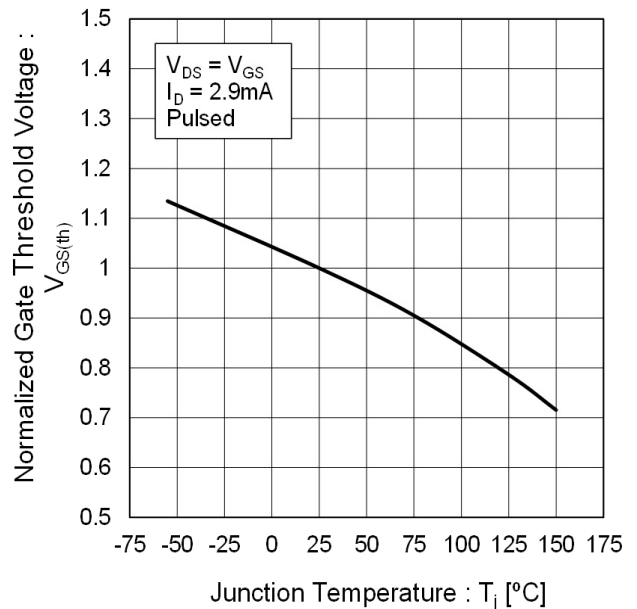


Fig.11 Static Drain - Source On - State
Resistance vs. Gate Source Voltage

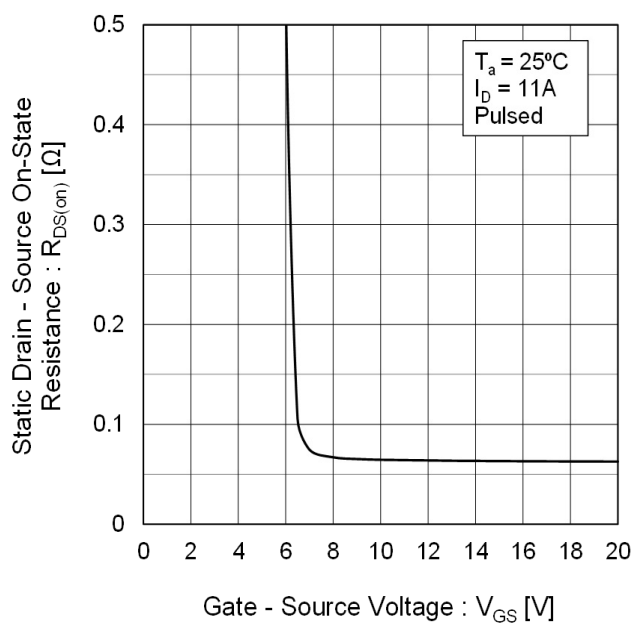
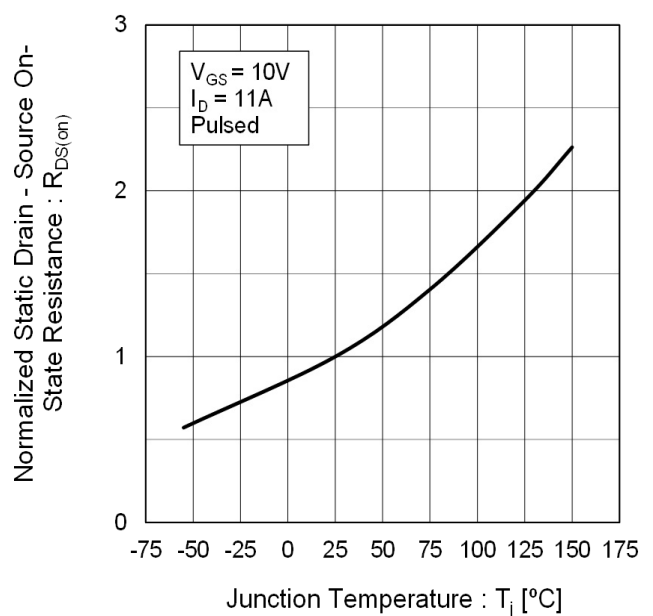


Fig.12 Normalized Static Drain - Source
On - State Resistance
vs. Junction Temperature



●Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current

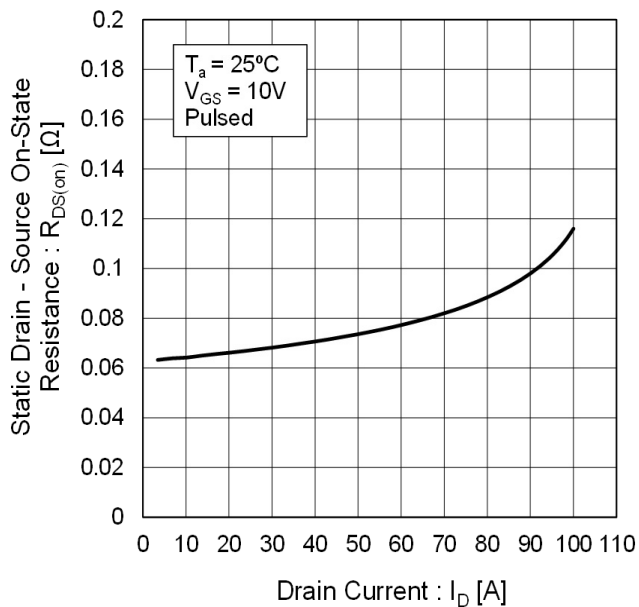


Fig.14 Capacitances

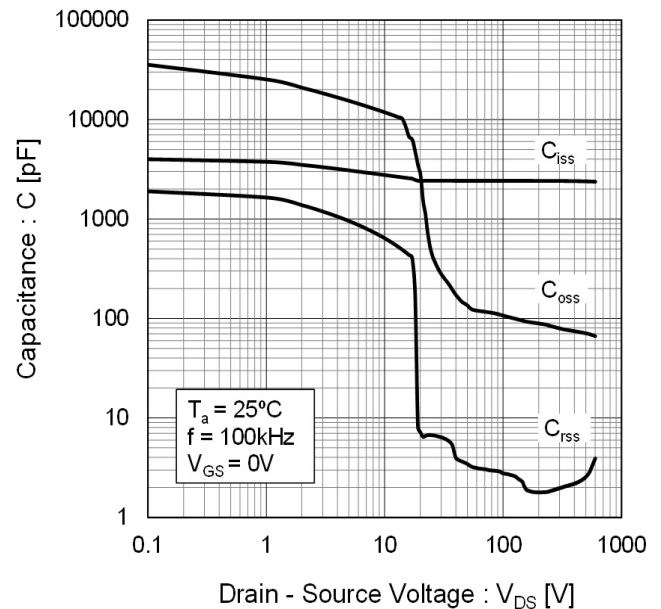


Fig.15 Coss Stored Energy

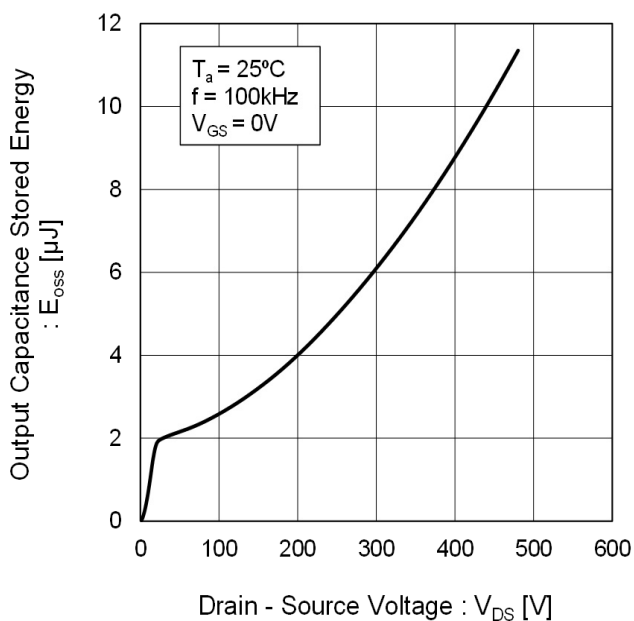
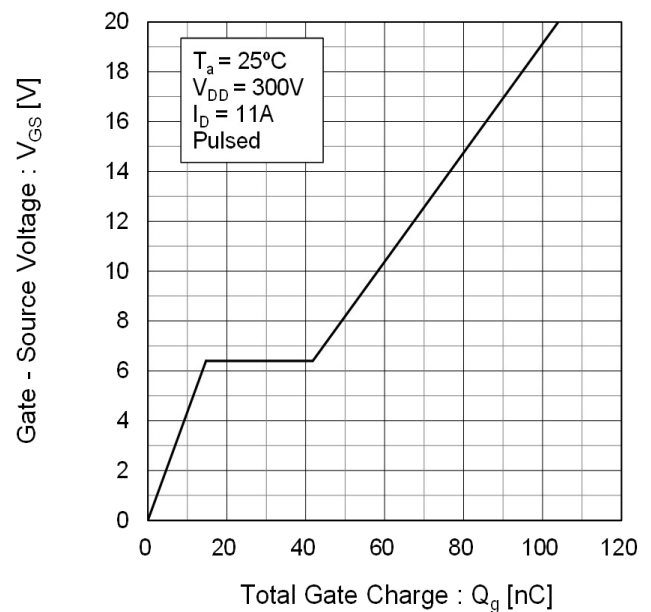
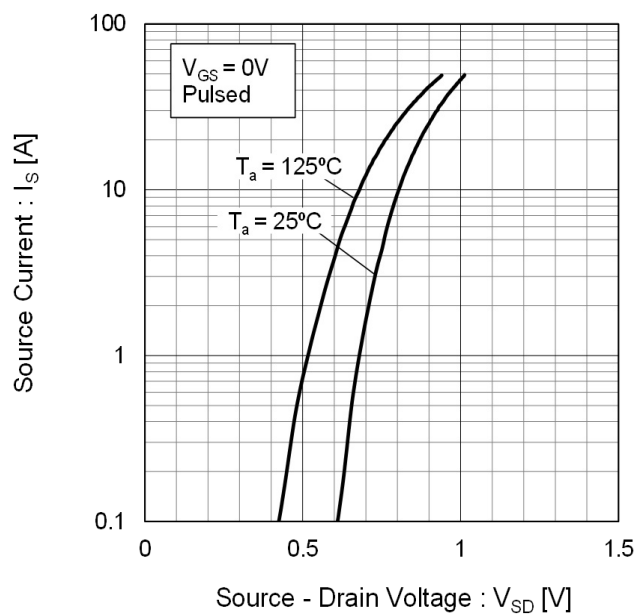
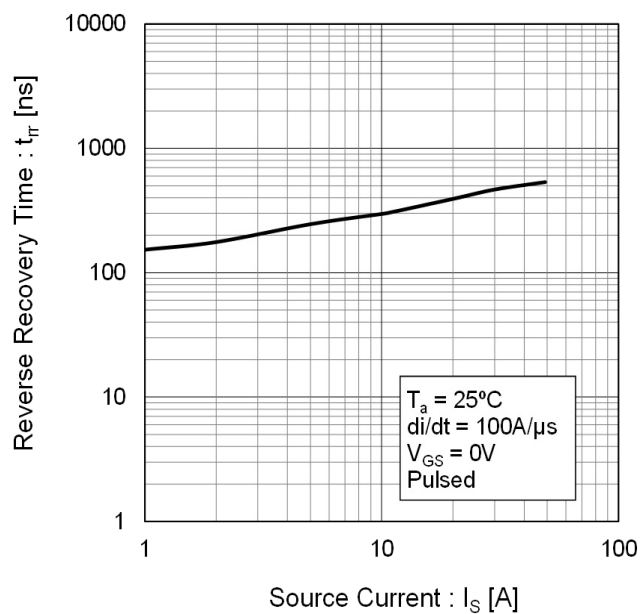


Fig.16 Gate Charge



●Electrical characteristic curves

Fig.17 Source Current
vs. Source - Drain VoltageFig.18 Reverse Recovery Time
vs. Source Current

● Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

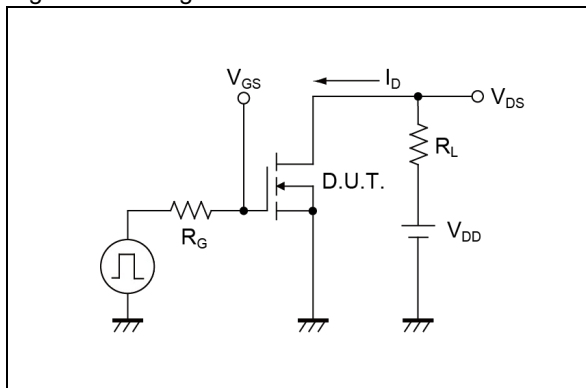


Fig.1-2 Switching Waveforms

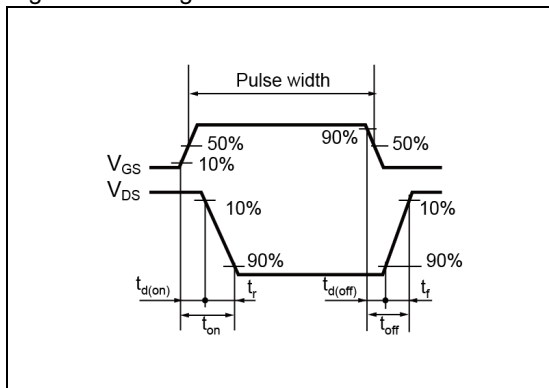


Fig.2-1 Gate Charge Measurement Circuit

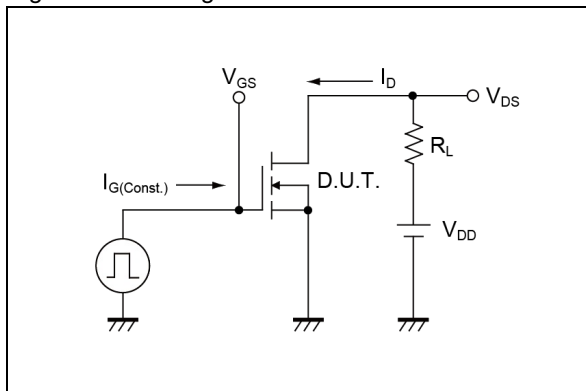


Fig.2-2 Gate Charge Waveform

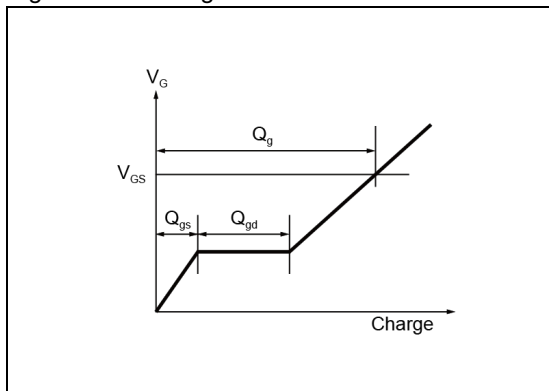


Fig.3-1 Avalanche Measurement Circuit

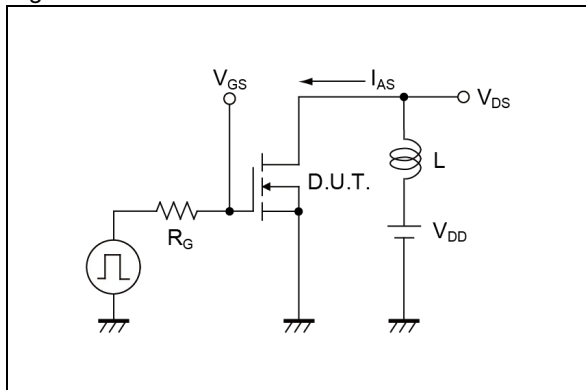


Fig.3-2 Avalanche Waveform

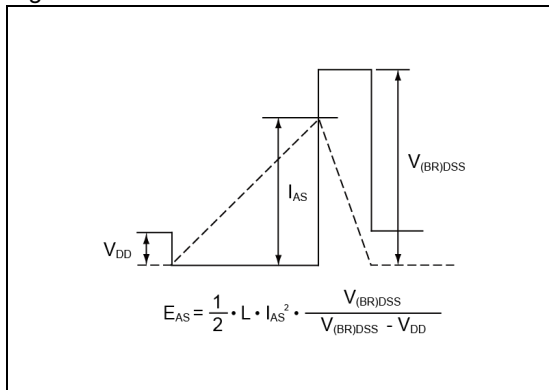


Fig.4-1 Diode Recovery Measurement Circuit

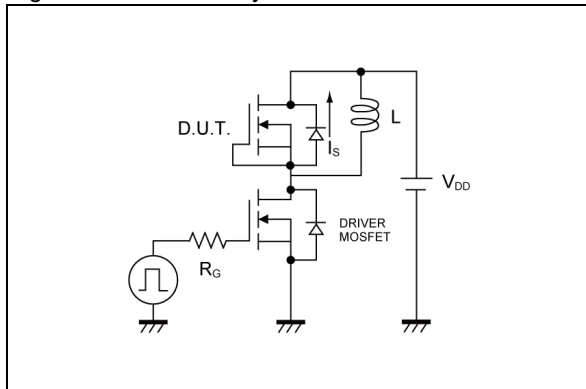
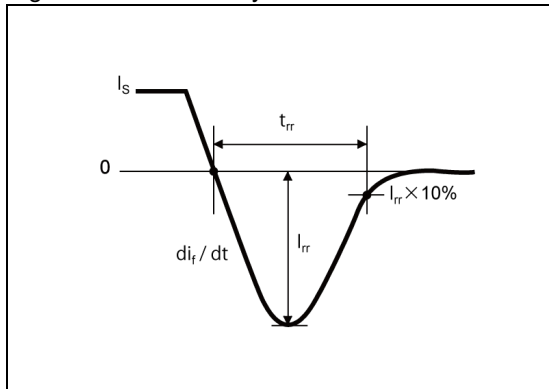
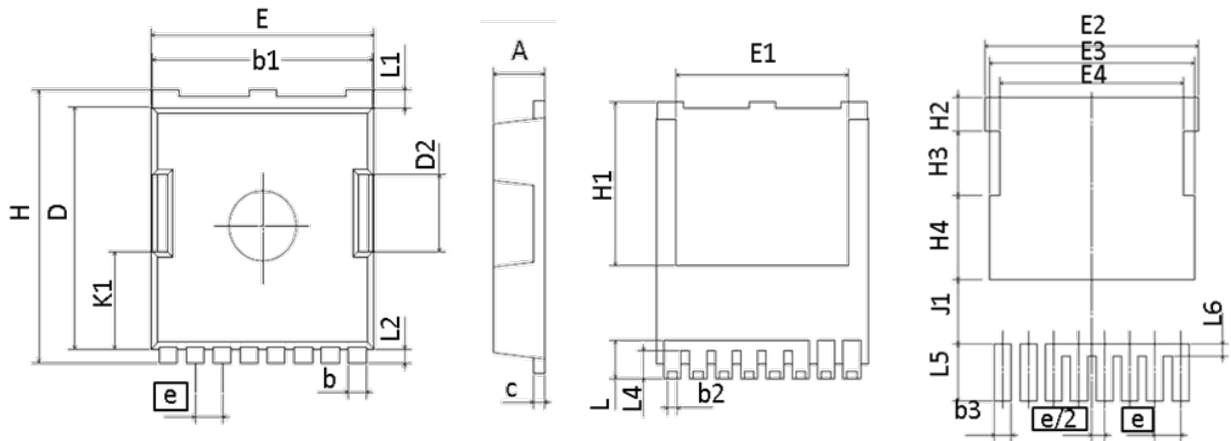


Fig.4-2 Diode Recovery Waveform



●Dimensions
MO-299
(TOLL)



Reference footprint dimensions

DIM	Milimeters		Inches	
	Min.	Max.	Min.	Max.
A	2.20	2.40	0.087	0.094
b	0.70	0.90	0.028	0.035
b1	9.70	9.90	0.382	0.390
b2	0.42	0.50	0.017	0.020
c	0.40	0.60	0.016	0.024
D	10.28	10.58	0.405	0.417
D2	3.10	3.50	0.122	0.138
E	9.70	10.10	0.382	0.398
E1	7.90	8.30	0.311	0.327
e	1.20		0.047	
H	11.48	11.88	0.452	0.468
H1	6.75	7.15	0.266	0.281
K1	3.98	4.38	0.157	0.172
L	1.40	1.80	0.055	0.071
L1	0.50	0.90	0.020	0.035
L2	0.50	0.70	0.020	0.028
L4	1.00	1.30	0.039	0.051

DIM	Milimeters		Inches	
	Nom.		Nom.	
b3	0.80		0.031	
E2	10.10		0.398	
E3	9.70		0.382	
E4	8.70		0.343	
H2	1.50		0.059	
H3	2.80		0.110	
H4	3.70		0.146	
J1	2.80		0.110	
L5	2.50		0.098	
L6	0.50		0.020	

Dimension in mm / inches

Notice

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

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 - Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.) ; or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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