DPG10IM300UC

HiPerFRED

300 V V_{RRM}

10 A I_{FAV}

35 ns

High Performance Fast Recovery Diode Low Loss and Soft Recovery Single Diode

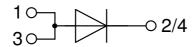
Part number

DPG10IM300UC

Marking on Product: PAOGUI



Backside: cathode



Features / Advantages:

- Planar passivated chips
- Very low leakage current
- Very short recovery time
- Improved thermal behaviour
- Very low Irm-values
- Very soft recovery behaviour
- Avalanche voltage rated for reliable operation
- Soft reverse recovery for low EMI/RFI
- Low Irm reduces:
 - Power dissipation within the diode
 - Turn-on loss in the commutating switch

Applications:

- Antiparallel diode for high frequency switching devices
- Antisaturation diode
- Snubber diode
- Free wheeling diode
- Rectifiers in switch mode power supplies (SMPS)
- Uninterruptible power supplies (UPS)

Package: TO-252 (DPak)

- Industry standard outline
- RoHS compliant
- Epoxy meets UL 94V-0

Terms _Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact the sales office, which is responsible for you.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact the sales office, which is responsible for you.

Should you intend to use the product in aviation, in health or live endangering or life support applications, please notify. For any such application we urgently recommend

- to perform joint risk and quality assessments; - the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

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Fast Diode				Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse blocki	ng voltage	$T_{VJ} = 25^{\circ}C$			300	V
V _{RRM}	max. repetitive reverse blocking ve	oltage	$T_{VJ} = 25^{\circ}C$			300	V
I _R	reverse current, drain current	$V_R = 300 \text{ V}$	$T_{VJ} = 25^{\circ}C$			1	μΑ
		$V_R = 300 V$	$T_{VJ} = 150$ °C			0.06	mΑ
V _F	forward voltage drop	I _F = 10 A	$T_{VJ} = 25^{\circ}C$			1.27	V
		$I_F = 20 A$				1.45	٧
		I _F = 10 A	T _{vJ} = 150°C			0.98	٧
		$I_F = 20 A$				1.17	V
I _{FAV}	average forward current	T _C = 150°C	$T_{VJ} = 175$ °C			10	Α
		rectangular $d = 0.5$					
V _{F0}	threshold voltage		$T_{VJ} = 175$ °C			0.74	٧
r _F	slope resistance	ess calculation only				17.7	mΩ
R _{thJC}	thermal resistance junction to case	9				2.3	K/W
R _{thCH}	thermal resistance case to heatsing	nk			0.50		K/W
P _{tot}	total power dissipation		$T_C = 25^{\circ}C$			65	W
I _{FSM}	max. forward surge current	$t = 10 \text{ ms}$; (50 Hz), sine; $V_R = 0 \text{ V}$	$T_{VJ} = 45^{\circ}C$			140	Α
C¹	junction capacitance	$V_R = 150 \text{V}$ f = 1 MHz	$T_{VJ} = 25^{\circ}C$		15		pF
I _{RM}	max. reverse recovery current	\	$T_{VJ} = 25 ^{\circ}\text{C}$		3		Α
		$I_F = 10 \text{ A}; V = 200 \text{ V}$	$T_{VJ} = 125$ °C		5.5		Α
t _{rr}	reverse recovery time	-d _F /dt = 200 A/μs	$T_{VJ} = 25 ^{\circ}\text{C}$		35		ns
		1	$T_{VJ} = 125^{\circ}C$		45		ns

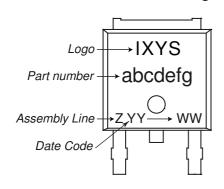




Package	Package TO-252 (DPak)			Ratings			
Symbol	Definition	Conditions	min.	typ.	max.	Unit	
IRMS	RMS current	per terminal 1)			20	Α	
T _{VJ}	virtual junction temperature		-55		175	°C	
T _{op}	operation temperature		-55		150	°C	
T _{stg}	storage temperature		-55		150	°C	
Weight				0.3		g	
F _c	mounting force with clip		20		60	N	

¹⁾ I_{nust} is typically limited by the pin-to-chip resistance (1); or by the current capability of the chip (2). In case of (1) and a product with multiple pins for one chip-potential, the current capability can be increased by connecting the pins as one contact.

Product Marking



Part description

D = Diode

P = HiPerFRED

G = extreme fast

10 = Current Rating [A]

IM = Single Diode

300 = Reverse Voltage [V]

UC = TO-252AA (DPak)

Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	DPG10IM300UC	PAOGUI	Tape & Reel	2500	505682

Similar Part	Package	Voltage class
DPG10I300PA	TO-220AC (2)	300

Equivalent Circuits for Simulation			* on die level	T _{vJ} = 175 °C
$I \rightarrow V_0$	R_0	Fast Diode		
V _{0 max}	threshold voltage	0.74		V
$R_{0 \text{ max}}$	slope resistance *	14.5		$m\Omega$

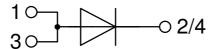
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Outlines TO-252 (DPak) Ε b3 1(36) Millimeters Inches Dim min max min max 2.20 2.40 0.087 0.094 Α A1 2.10 2.50 0.083 0.098 ØΡ 0.66 0.034 b 0.86 0.026 b2 0.96 0.038 I 5.04 b3 5.64 0.198 0.222 b4 4.34 BSC 0.171 BSC 3 BSC b5 0.50 0.020 BSC 0.016 C 0.40 0.86 0.034 5.90 0.248 D 6.30 0.232 E 6.40 6.80 0.252 0.268 2.10 2.50 0.083 0.098 е 2x b2 3x b H 9.20 10.10 0.362 0.398 0.55 1.28 0.022 0.050 2x [e] L1 2.50 2.90 0.098 0.114 0.40 0.60 0.016 0.024 (5.55 mm) 0.50 0.90 L3 0.020 0.035 0.220 inches 0.60 1.00 0.024 0.039 L4 L5 0.82 1.22 0.032 0.048 L6 0.79 0.99 0.031 0.039 0.81 1.01 0.032 0.040 L7 L8 0.40 0.80 0.016 0.031 (5.04) 0.200 1.50 BSC 0.059 BSC 0.00 0.04 ØP 1.00 BSC 0.039 BSC (8.40) \Seppler (6.21) 0.210 Option Recommended min. foot print (2.28) 0 0 90 (1.25) 0.050





Fast Diode

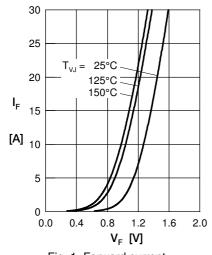


Fig. 1 Forward current I_F versus V_F

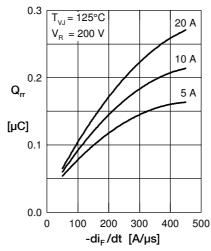


Fig. 2 Typ. reverse recov. charge Q_{rr} versus $-di_F/dt$

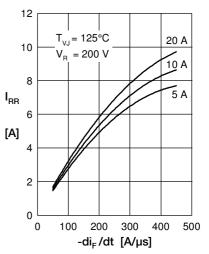


Fig. 3 Typ. reverse recov. current I_{RR} versus $-di_F/dt$

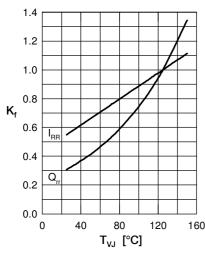


Fig. 4 Typ. dynamic parameters Q_{rr} , I_{RR} versus T_{VJ}

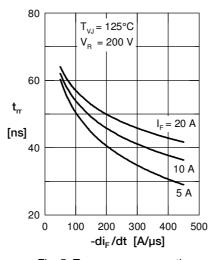


Fig. 5 Typ. reverse recov. time t_{rr} versus $-di_F/dt$

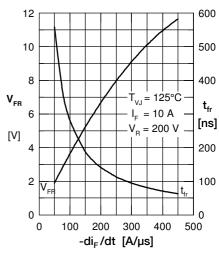


Fig. 6 Typ. forward recov. voltage V_{FB} and t_{fr} versus di_{F}/dt

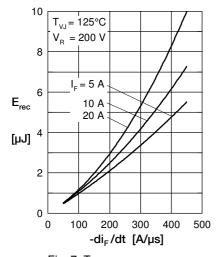


Fig. 7 Typ. recovery energy E_{rec} versus $-di_F/dt$

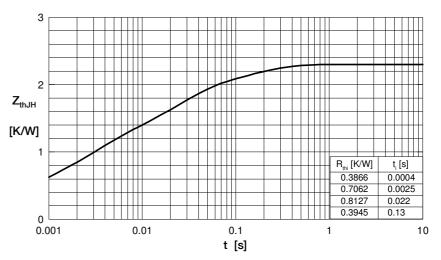


Fig. 8 Transient thermal resistance junction to case

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