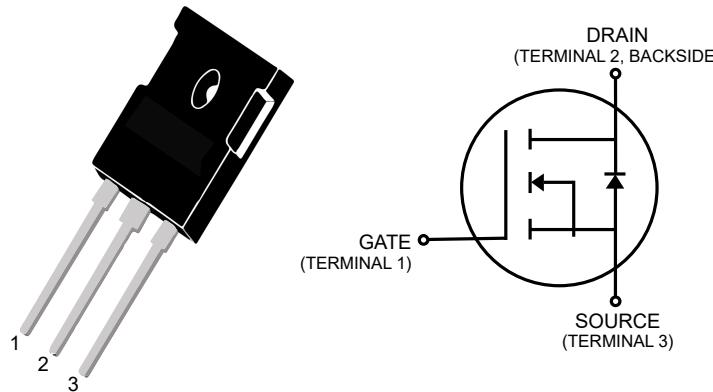


## Product Overview

1200V, 25 mΩ typical at  $V_{GS} = 20V$ , 28 mΩ typical at  $V_{GS} = 18V$ , Silicon Carbide (SiC) N-Channel MOSFET, TO-247.



### Features

- AEC-Q101 qualified option available
- Low capacitances and low gate charge
- Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature,  $T_{J(max)} = 175\text{ °C}$
- Fast and reliable body diode
- Superior avalanche ruggedness
- RoHS compliant

### Benefits

- High efficiency to enable lighter and more compact system
- Simple to drive and easy to parallel
- Improved thermal capabilities and lower switching losses
- Eliminates the need for external freewheeling diode
- Lower system cost of ownership

### Applications

- Photovoltaic (PV) inverter, converter, and industrial motor drives
- Smart grid transmission and distribution
- Induction heating and welding
- Hybrid Electric Vehicle (HEV) powertrain and Electric Vehicle (EV) charger
- Power supply and distribution

## 1. Device Specifications

This section shows the specifications of this device.

### 1.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings of this device.

**Table 1-1.** Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit
$V_{DSS}$	Drain source voltage	1200	V
$I_D$	Continuous drain current at $T_C = 25^\circ\text{C}$	113	A
	Continuous drain current at $T_C = 100^\circ\text{C}$	80	
$I_{DM}$	Pulsed drain current <sup>1</sup>	408	
$V_{GS}$	Gate-source voltage	23 to -10	V
	Transient gate-source voltage	25 to -12	
$P_D$	Total power dissipation at $T_C = 25^\circ\text{C}$	577	W
	Linear derating factor	3.8	W/ $^\circ\text{C}$

**Note:**

1. Repetitive rating: pulse width and case temperature are limited by the maximum junction temperature.

The following table shows the thermal and mechanical characteristics of this device.

**Table 1-2.** Thermal and Mechanical Characteristics

Symbol	Characteristic/Test Conditions	Min.	Typ.	Max.	Unit
$R_{\theta JC}$	Junction-to-case thermal resistance	—	0.20	0.26	$^\circ\text{C}/\text{W}$
$T_J$	Operating junction temperature	-55	—	175	$^\circ\text{C}$
$T_{STG}$	Storage temperature	-55	—	175	
$T_L$	Lead temperature for 10 seconds	—	—	300	$^\circ\text{C}$
$\tau_M$	Mounting torque, M3 screw for heat sink attachment (requires 1, not included)	—	0.8	—	N-m
Wt	Package weight	—	6.2	—	g

ESD practices should comply with JESD-625.

### 1.2 Electrical Performance

The following table shows the static characteristics of this device.  $T_J = 25^\circ\text{C}$  unless otherwise specified.

**Table 1-3.** Static Characteristics

Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source breakdown voltage	$V_{GS} = 0\text{V}$ , $I_D = 100\text{ }\mu\text{A}$	1200	—	—	V
$R_{DS(on)}$	Drain-source on resistance <sup>1</sup>	$V_{GS} = 20\text{V}$ , $I_D = 40\text{A}$	—	25	31	$\text{m}\Omega$
		$V_{GS} = 18\text{V}$ , $I_D = 40\text{A}$	—	28	—	
$V_{GS(th)}$	Gate-source threshold voltage	$V_{GS} = V_{DS}$ , $I_D = 3\text{ mA}$	1.9	3.0	5.0	V
$I_{DSS}$	Zero gate voltage drain current	$V_{DS} = 1200\text{V}$ , $V_{GS} = 0\text{V}$	—	0.3	35	$\mu\text{A}$
		$V_{DS} = 1200\text{V}$ , $V_{GS} = 0\text{V}$ , $T_J = 175^\circ\text{C}$	—	3.5	—	
$I_{GSS}$	Gate-source leakage current	$V_{GS} = 20\text{V}/-10\text{V}$	—	—	$\pm 100$	nA

**Note:**

1. Pulse test: pulse width < 380  $\mu$ s, duty cycle < 2%.

The following table shows the dynamic characteristics of this device.  $T_J = 25^\circ\text{C}$  unless otherwise specified. The dynamic characteristics are characterized, not 100% tested, at the recommended operating  $V_{GS} = 20\text{V}/-5\text{V}$ .

**Table 1-4.** Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
$C_{iss}$	Input capacitance	$V_{GS} = 0\text{V}$	—	3633	—	pF
$C_{rss}$	Reverse transfer capacitance	$V_{DD} = 1000\text{V}$	—	18	—	
$C_{oss}$	Output capacitance	$V_{AC} = 25\text{ mV}$ $f = 200\text{ KHz}$	—	271	—	
$Q_G$	Total gate charge	$V_{GS} = -5\text{V}/20\text{V}$	—	232	—	nC
$Q_{GS}$	Gate-source charge	$V_{DD} = 800\text{V}$	—	41	—	
$Q_{GD}$	Gate-drain charge	$I_D = 40\text{A}$	—	50	—	
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 800\text{V}$	—	63	—	ns
$t_r$	Voltage rise time	$V_{GS} = -5\text{V}/20\text{V}$	—	15	—	
$t_{d(off)}$	Turn-off delay time	$I_D = 50\text{A}$	—	38	—	
$t_f$	Voltage fall time	$R_{G(ext)} = 4\Omega$	—	25	—	
$E_{on}$	Turn-on switching energy	Freewheeling diode = MSC025SMA120B ( $V_{GS} = -5\text{V}$ ); reference Figure 1-19	—	2126	—	$\mu\text{J}$
$E_{off}$	Turn-off switching energy	—	—	304	—	
ESR	Gate equivalent series resistance	$f = 1\text{ MHz}, 25\text{ mV}$ , drain short	—	0.88	—	$\Omega$
SCWT	Short circuit withstand time	$V_{DS} = 960\text{V}, V_{GS} = 20\text{V}$	—	3	—	$\mu\text{s}$
$E_{AS}$	Avalanche energy, single pulse	$I_D = 40\text{A}$	—	3500	—	mJ

The following table shows the body diode characteristics of this device.  $T_J = 25^\circ\text{C}$  unless otherwise specified. The body diode reverse recovery is characterized, not 100% tested.

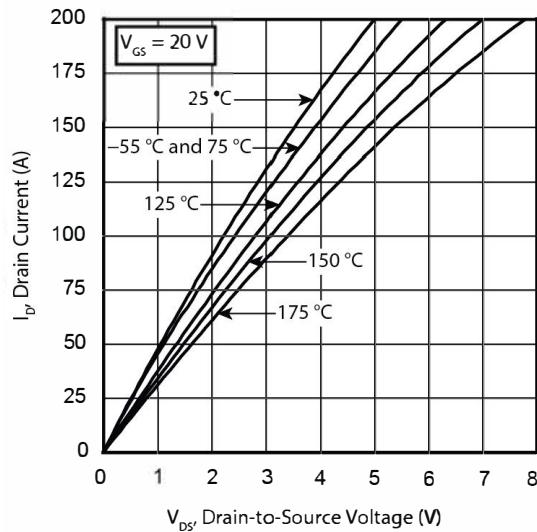
**Table 1-5.** Body Diode Characteristics

Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
$V_{SD}$	Diode forward voltage	$I_{SD} = 40\text{A}, V_{GS} = 0\text{V}$	—	3.6	—	V
		$I_{SD} = 40\text{A}, V_{GS} = -5\text{V}$	—	3.8	5.0	
$t_{rr}$	Reverse recovery time	$I_{SD} = 50\text{A}, V_{GS} = -5\text{V}$ , Drive $R_G = 4\Omega$ , $V_{DD} = 800\text{V}$ , $dI/dt = -3300\text{ A}/\mu\text{s}$	—	50	—	ns
$Q_{rr}$	Reverse recovery charge	—	—	600	—	nC
$I_{RRM}$	Reverse recovery current	—	—	19	—	A

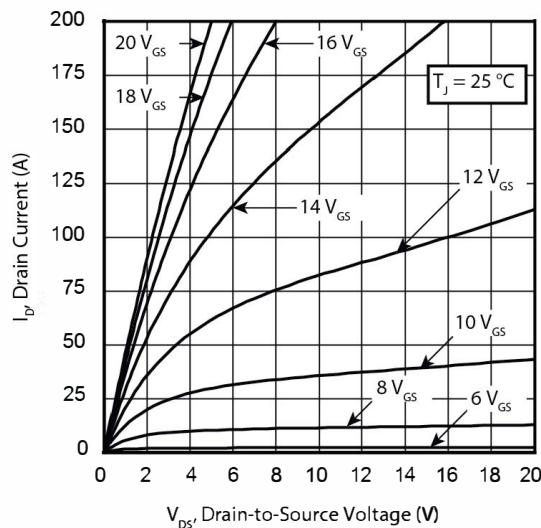
## 1.3 Typical Performance Curves

Data for performance curves are characterized, not 100% tested.

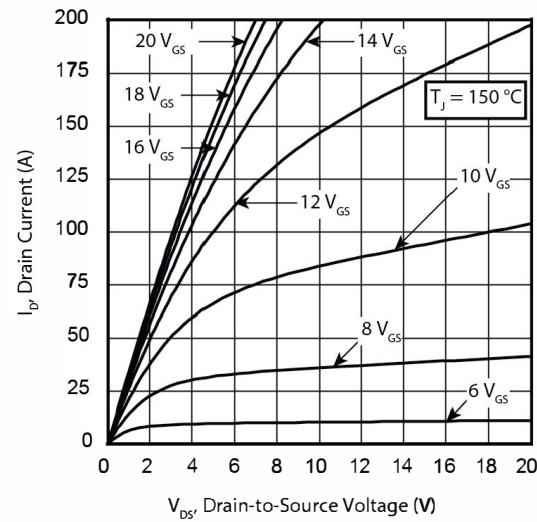
**Figure 1-1.** Drain Current vs.  $V_{DS}$  at  $T_J$



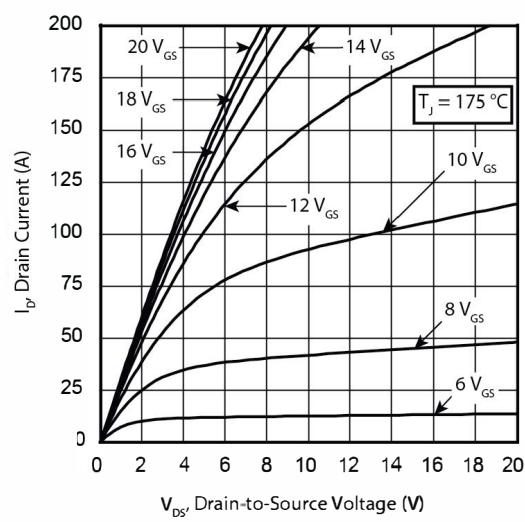
**Figure 1-2.** Drain Current vs.  $V_{DS}$  at  $V_{GS}$



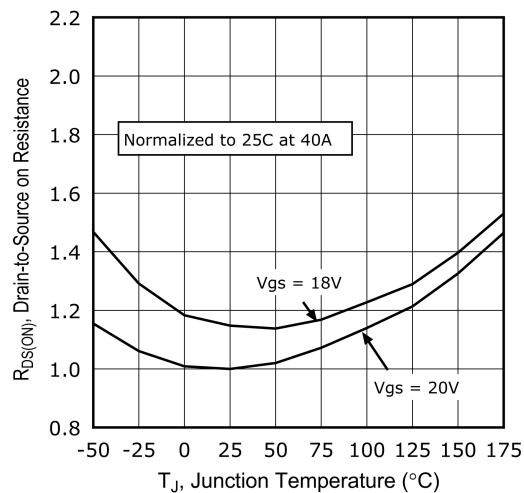
**Figure 1-3.** Drain Current vs.  $V_{DS}$  at  $V_{GS}$



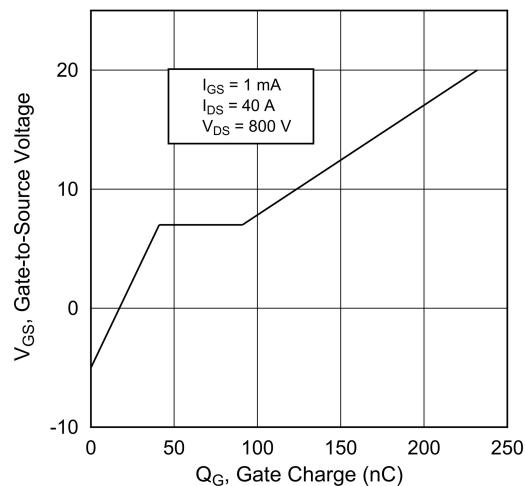
**Figure 1-4.** Drain Current vs.  $V_{DS}$  at  $V_{GS}$



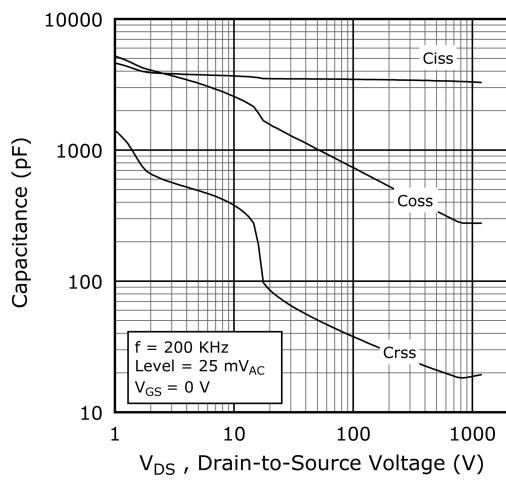
**Figure 1-5.  $R_{DS(on)}$  vs. Junction Temperature**



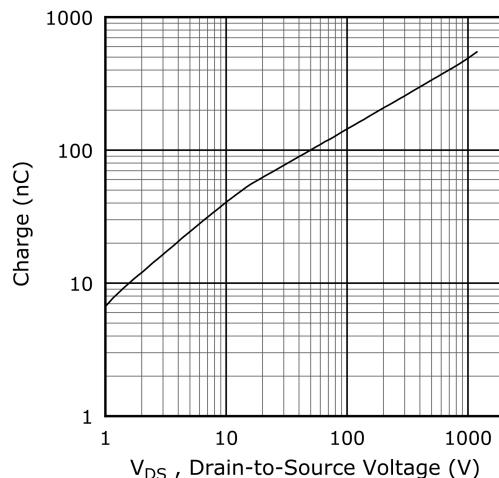
**Figure 1-6. Gate Charge Characteristics**



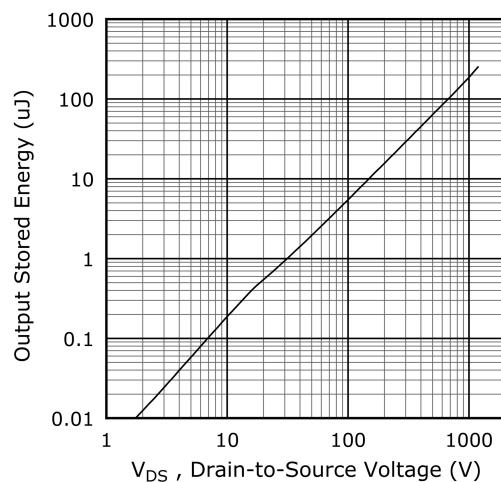
**Figure 1-7. Capacitance vs. Drain-to-Source Voltage**



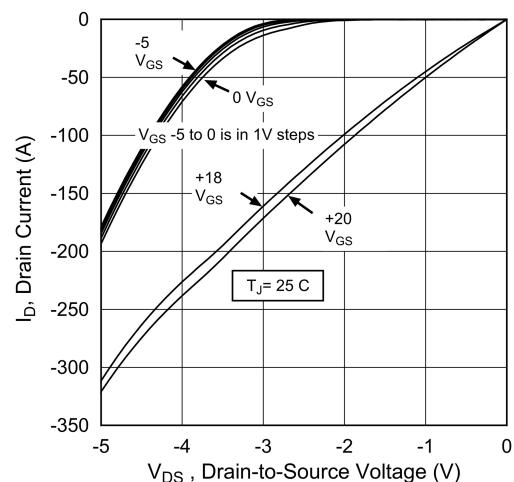
**Figure 1-8. Output Charge vs. Drain-to-Source Voltage**



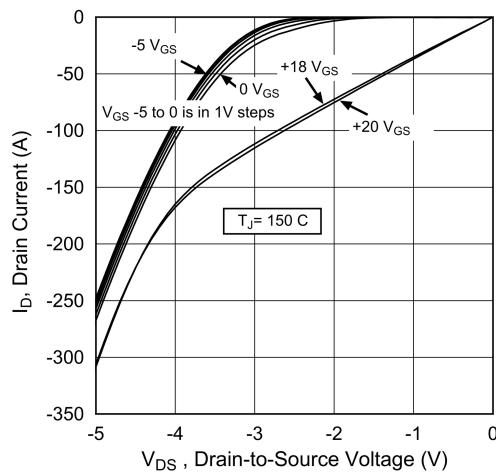
**Figure 1-9.** Output Stored Energy vs.  $V_{DS}$



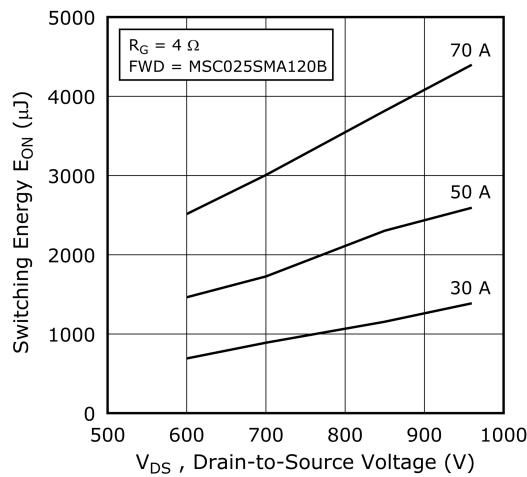
**Figure 1-10.**  $I_D$  vs.  $V_{DS}$  3<sup>rd</sup> Quadrant Conduction



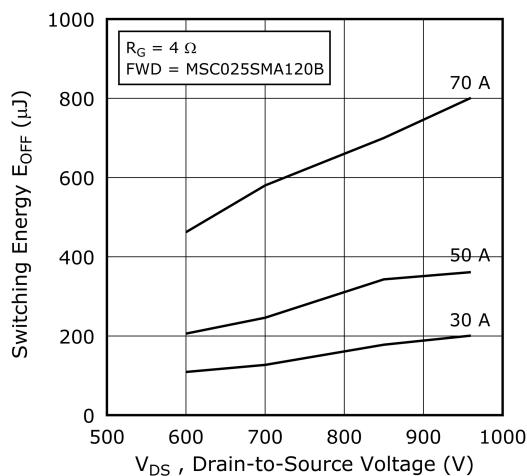
**Figure 1-11.**  $I_D$  vs.  $V_{DS}$  3<sup>rd</sup> Quadrant Conduction



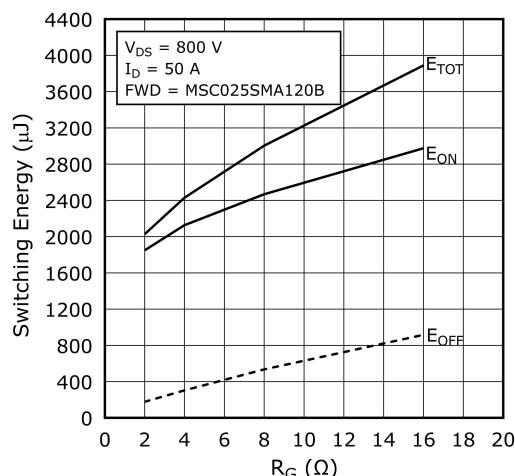
**Figure 1-12.** Switching Energy  $E_{on}$  vs.  $V_{DS}$  &  $I_D$



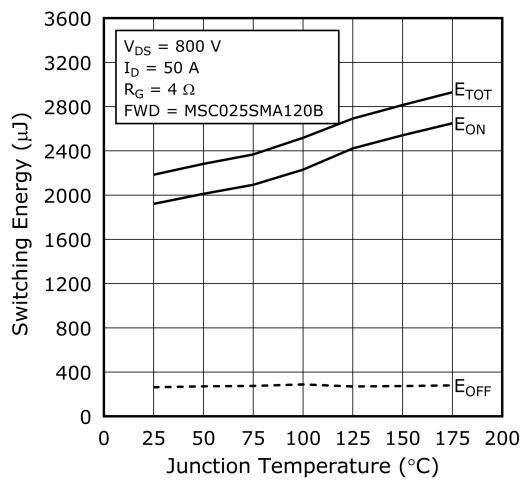
**Figure 1-13.** Switching Energy  $E_{\text{off}}$  vs.  $V_{\text{DS}}$  &  $I_{\text{D}}$



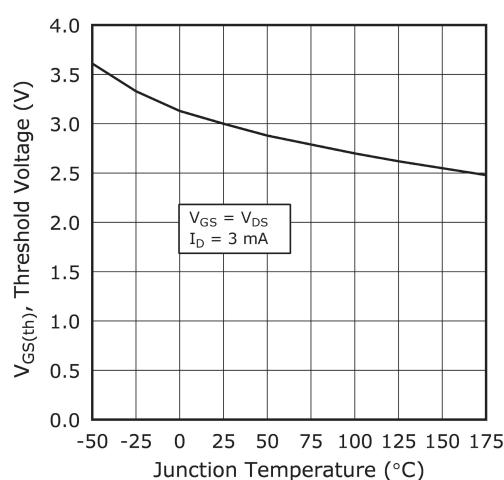
**Figure 1-14.** Switching Energy vs.  $R_{\text{G}}$



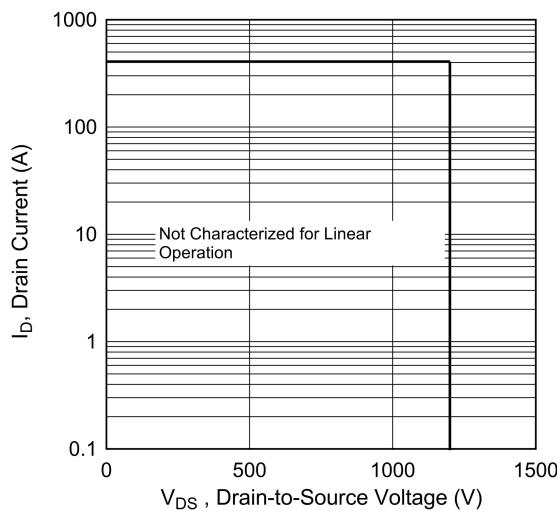
**Figure 1-15.** Switching Energy vs. Junction Temperature



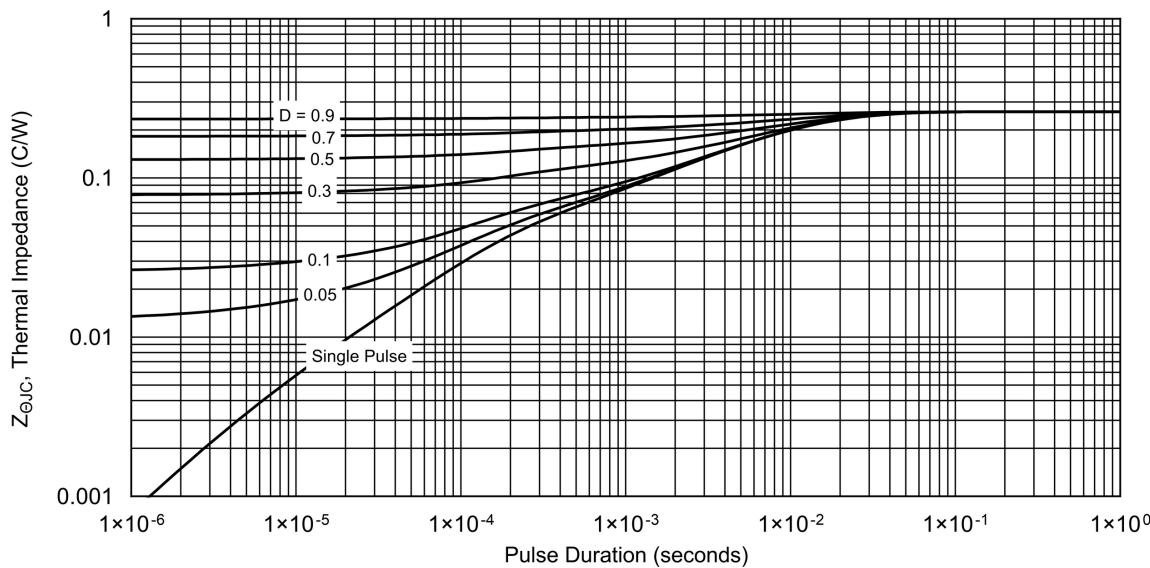
**Figure 1-16.** Threshold Voltage vs. Junction Temperature



**Figure 1-17.** Forward Safe Operating Area

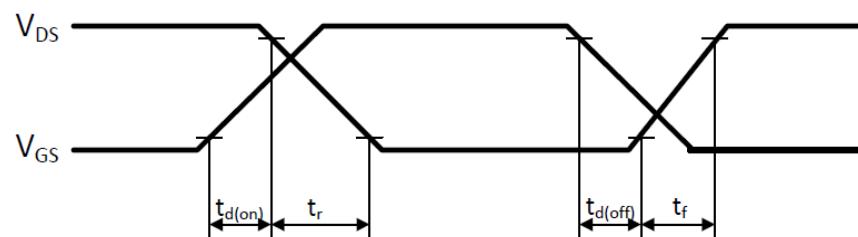


**Figure 1-18.** Maximum Transient Thermal Impedance



The following figure shows the switching waveform diagram of this device.

**Figure 1-19.** Switching Waveform



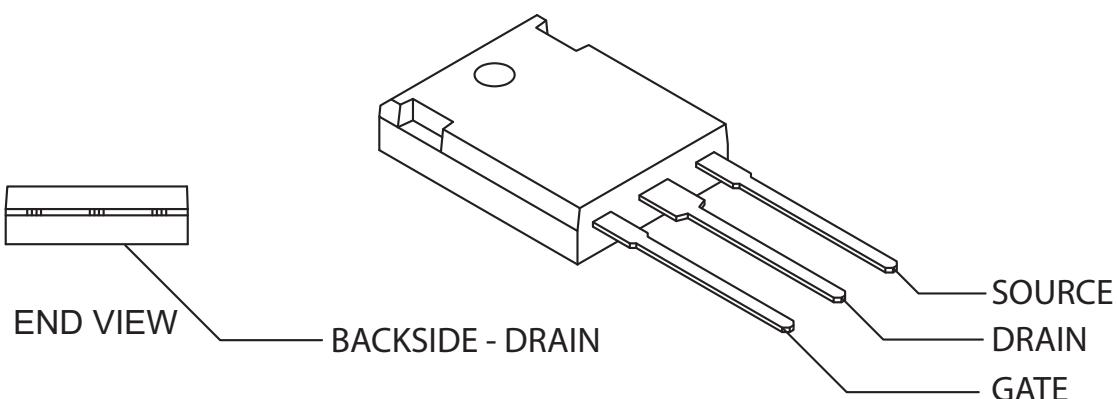
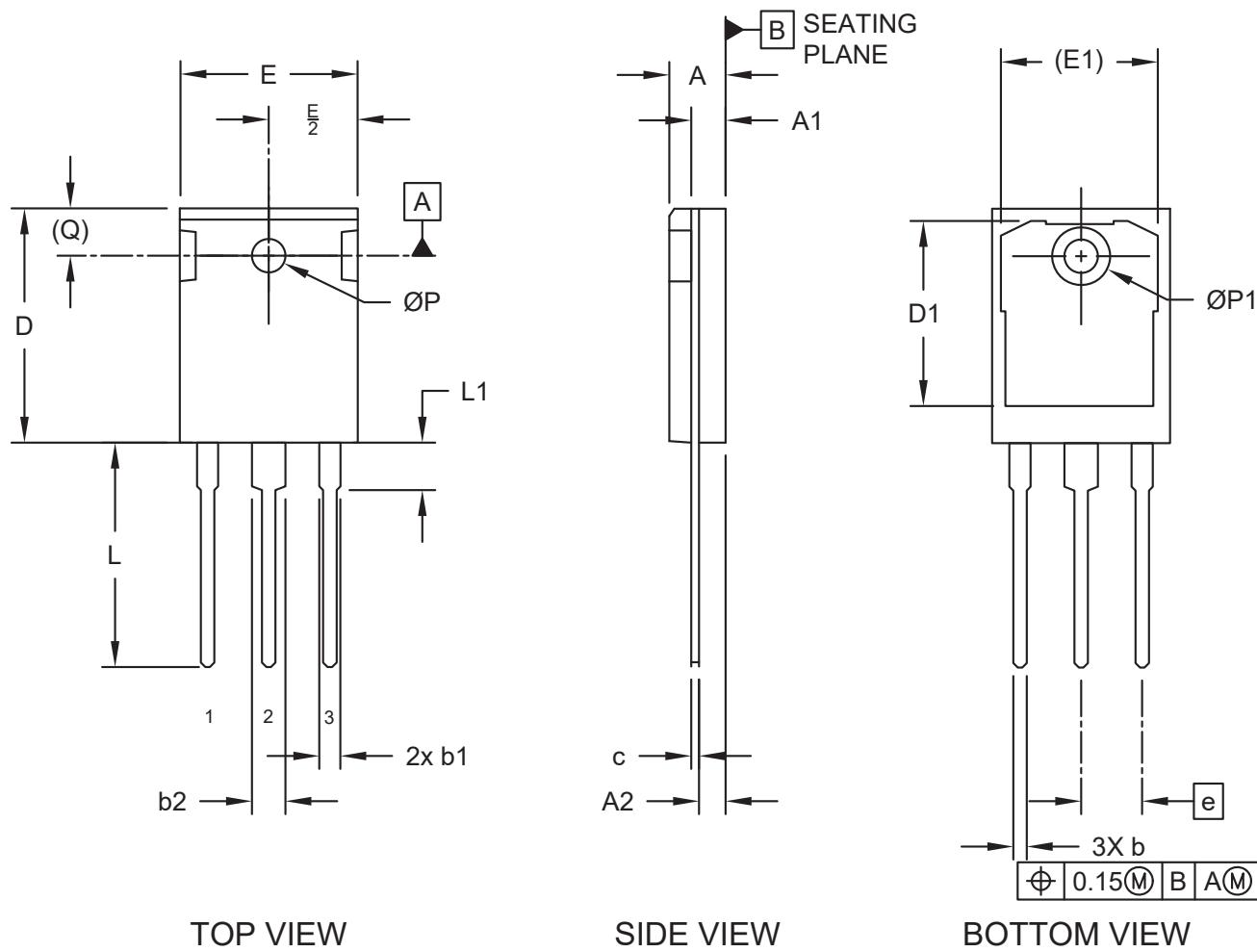
## 2. Package Specification

This section shows the package specification of this device.

### 2.1 Package Outline Drawing

The following figure illustrates the TO-247 package outline of this device.

Figure 2-1. Package Outline Drawing



The following table shows the TO-247 dimensions and should be used in conjunction with the package outline drawing.

**Table 2-1.** TO-247 Dimensions

Symbol	Description	Min. (mm)	Max. (mm)
N	Number of leads	3	
e	Pitch	5.44 BSC	
A	Overall height	4.70	5.31
A1	Tab height	1.50	2.49
A2	Seating plane to lead	2.21	2.59
b	Lead width	1.02	1.40
b1	Lead shoulder width (X2)	1.65	2.41
b2	Lead shoulder width	2.87	3.38
c	Lead thickness	0.41	0.79
L	Lead length	19.81	20.32
L1	Lead shoulder length	3.99	4.50
D	Molded body length	20.80	21.46
D1	Thermal pad length	16.25	17.65
E	Total width	15.49	16.26
E1	Thermal pad width	13.10	14.50
Q	Hole center to tab edge	6.15 REF	
ØP	Hole diameter	3.51	3.81
ØP1	Thermal pad hole diameter	7.18 REF	

**Notes:**

Dimensioning and tolerancing per ASME Y14.5M.

- BSC: Basic dimension. Theoretically exact value shown without tolerances.
- REF: Reference dimension, usually without tolerance, for information purposes only.

### 3. Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

**Table 3-1.** Revision History

Revision	Date	Description
B	09/2024	Updated mounting torque row in <a href="#">Table 1-2</a> .
A	02/2024	<p>The following changes are made in this revision of the document:</p> <ul style="list-style-type: none"><li>• Document migrated from Microsemi template to Microchip template; Assigned Microchip literature number DS-00005233A, which replaces the previous Microsemi literature number 050-7735.</li><li>• Added <a href="#">Figure 1-9</a>.</li></ul>
Initial releases (Microsemi Revisions A, B, and C)	11/2018-10/2019	Initial releases.

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