

Description

The ULN2002A, ULN2003A, and ULN2004A are high-voltage, high-current Darlington arrays each containing seven open collector common emitter pairs. Each pair is rated at 500mA. Suppression diodes are included for inductive load driving. The inputs and outputs are pinned in opposition to simplify board layout.

Device options are designed to be compatible with common logic families:

- ULN2002A (14V to 25V pMOS)
- ULN2003A (5V TTL, CMOS)
- ULN2004A (6V to 15V CMOS, pMOS)

These devices are capable of driving a wide range of loads including solenoids, relays, DC motors, LED displays, filament lamps, thermal print-heads, and high-power buffers.

The ULN2002A and ULN2004A are available in a small outline 16-pin package (SO-16). The ULN2003A offers both SO-16 (Type SM) and TSSOP-16 (Type CJ) packages for different requirements.

Features

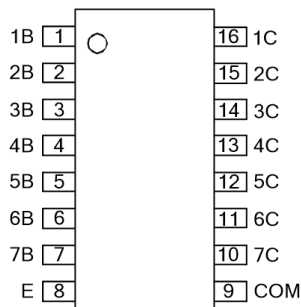
- 500mA Rated Collector Current (Single Output)
- High-Voltage Outputs: 50V
- Output Clamp Diodes
- Inputs Compatible with Popular Logic Types
- Relay Driver Applications
- "Green" Molding Compound (No Br, Sb)
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative.**
<https://www.diodes.com/quality/product-definitions/>

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Pin Assignments

ULN2003A

(Top View)

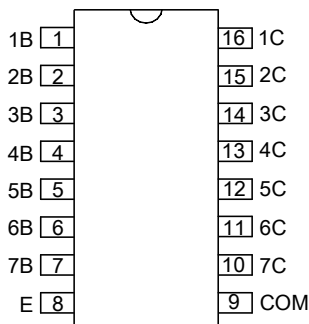


SO-16 (Type SM)

TSSOP-16 (Type CJ)

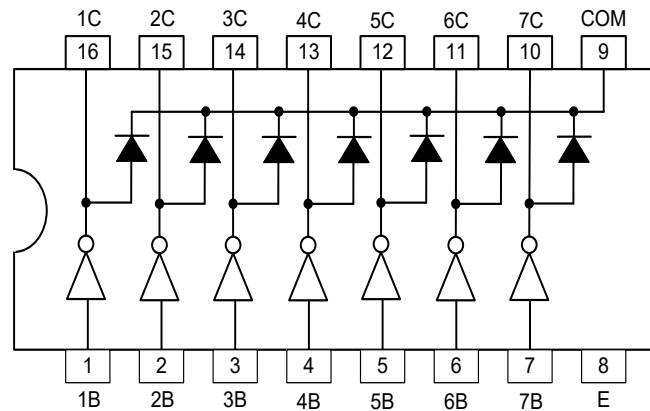
ULN2002A/ULN2004A

(Top View)



SO-16

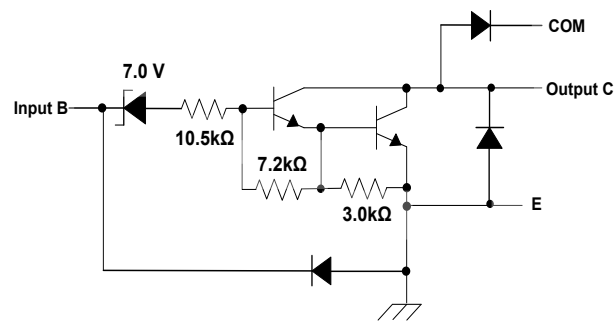
Connection Diagram



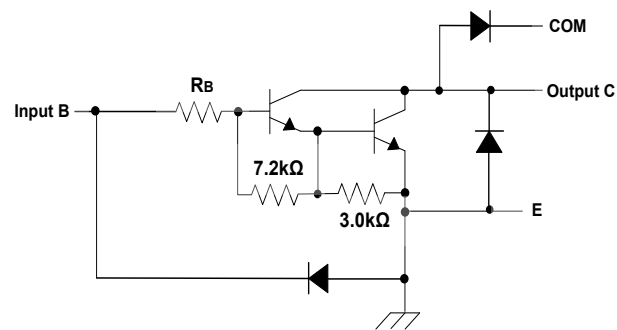
Pin Descriptions

Pin Number	Pin Name	Function
SO-16/SO-16 (Type SM)/TSSOP-16 (Type CJ)		
1	1B	Input Pair 1
2	2B	Input Pair 2
3	3B	Input Pair 3
4	4B	Input Pair 4
5	5B	Input Pair 5
6	6B	Input Pair 6
7	7B	Input Pair 7
8	E	Common Emitter (Ground)
9	COM	Common Clamp Diodes
10	7C	Output Pair 7
11	6C	Output Pair 6
12	5C	Output Pair 5
13	4C	Output Pair 4
14	3C	Output Pair 3
15	2C	Output Pair 2
16	1C	Output Pair 1

Functional Block Diagram



ULN2002A



ULN2003A: $R_B = 2.7k$
ULN2004A: $R_B = 10.5k$

ULN2003A/ULN2004A

Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter		Rating	Unit
V _{CC}	Collector to Emitter Voltage		50	V
V _R	Clamp Diode Reverse Voltage (Note 5)		50	V
V _I	Input Voltage (Note 5)		30	V
I _{CP}	Peak Collector Current		500	mA
I _{OK}	Output Clamp Current		500	mA
I _{TE}	Total Emitter Current		-2.5	A
θ _{JA}	Thermal Resistance Junction-to-Ambient (Note 6)	SO-16 & SO-16 (Type SM)	63.0	°C/W
		TSSOP-16 (Type CJ)	98	
θ _{JC}	Thermal Resistance Junction-to-Case (Note 7)	SO-16 & SO-16 (Type SM)	12.0	°C/W
		TSSOP-16 (Type CJ)	31	
ESD	HBM		2	kV
	CDM		2	kV
T _J	Junction Temperature		+150	°C
T _{STG}	Storage Temperature		-65 to +150	°C

- Notes:
- Stresses greater than those listed under *Absolute Maximum Ratings* can cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to *Absolute Maximum Ratings* for extended periods can affect device reliability.
 - All voltage values are with respect to the emitter/substrate terminal E, unless otherwise noted.
 - Maximum power dissipation is a function of T_J(max), θ_{JA} and T_A. The maximum allowable power dissipation at any allowable ambient temperature is P_D = (T_J(max) - T_A)/θ_{JA}. Operating at the absolute maximum T_J of +150°C can affect reliability.
 - Maximum power dissipation is a function of T_J(max), θ_{JC} and T_A. The maximum allowable power dissipation at any allowable ambient temperature is P_D = (T_J(max) - T_C)/θ_{JC}. Operating at the absolute maximum T_J of +150°C can affect reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{CC}	Collector to Emitter Voltage	0	50	V
T _A	Operating Ambient Temperature	-40	+105	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

ULN2002A							
Symbol	Parameter	Test Figure	Test Conditions	Min	Typ	Max	Unit
V _{I(ON)}	On-State Input Voltage	6	V _{CE} = 2V, I _C = 300mA	—	—	13	V
V _{CE(SAT)}	Collector Emitter Saturation Voltage	5	I _I = 250μA, I _C = 100mA	—	0.9	1.1	V
			I _I = 350μA, I _C = 200mA	—	1	1.3	
			I _I = 500μA, I _C = 350mA	—	1.2	1.6	
V _F	Clamp Forward Voltage	8	I _F = 350mA	—	1.7	2	V
I _{CEX}	Collector Cutoff Current	1	V _{CE} = 50V, I _I = 0	—	—	50	μA
		2	V _{CE} = 50V T _A = +105°C I _I = 0 V _I = 6V	—	—	100 500	
I _{I(OFF)}	Off-State Input Current	3	V _{CE} = 50V, I _C = 500μA	50	65	—	μA
I _I	Input Current	4	V _I = 17V	—	0.82	1.25	mA
I _R	Clamp Reverse Current	7	V _R = 50V T _A = +105°C	—	—	100	μA
			—	—	—	50	
C _I	Input Capacitance	—	V _I = 0, f = 1MHz	—	—	25	pF

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.) (continued)

ULN2003A								
Parameter		Test Figure	Test Conditions		Min	Typ	Max	Unit
V _{I(ON)}	On-State Input Voltage	6	V _{CE} = 2V	I _C = 200mA	—	—	2.4	V
				I _C = 250mA	—	—	2.7	
				I _C = 300mA	—	—	3	
V _{CE(SAT)}	Collector Emitter Saturation Voltage	5	I _I = 250μA, I _C = 100mA		—	0.9	1.1	V
			I _I = 350μA, I _C = 200mA		—	1	1.3	
			I _I = 500μA, I _C = 350mA		—	1.2	1.6	
V _F	Clamp Forward Voltage	8	I _F = 350mA		—	1.7	2	V
I _{CEX}	Collector Cutoff Current	1	V _{CE} = 50V, I _I = 0		—	—	50	μA
		2	V _{CE} = 50V T _A = +105°C	I _I = 0	—	—	100	
I _{I(OFF)}	Off-State Input Current	3	V _{CE} = 50V, I _C = 500μA		50	65	—	μA
I _I	Input Current	4	V _I = 3.85V		—	0.93	1.35	mA
I _R	Clamp Reverse Current	7	V _R = 50V	T _A = +105°C	—	—	100	μA
				—	—	—	50	
C _I	Input Capacitance	—	V _I = 0, f = 1MHz		—	15	25	pF
ULN2004A								
Parameter		Test Figure	Test Conditions		Min	Typ	Max	Unit
V _{I(ON)}	On-State Input Voltage	6	V _{CE} = 2V	I _C = 125mA	—	—	5	V
				I _C = 200mA	—	—	6	
				I _C = 275mA	—	—	7	
				I _C = 350mA	—	—	8	
V _{CE(SAT)}	Collector Emitter Saturation Voltage	5	I _I = 250μA, I _C = 100mA		—	0.9	1.1	V
			I _I = 350μA, I _C = 200mA		—	1	1.3	
			I _I = 500μA, I _C = 350mA		—	1.2	1.6	
V _F	Clamp Forward Voltage	8	I _F = 350mA		—	1.7	2	V
I _{CEX}	Collector Cutoff Current	1	V _{CE} = 50V, I _I = 0		—	—	50	μA
		2	V _{CE} = 50V T _A = +105°C	I _I = 0	—	—	100	
				V _I = 6V	—	—	500	
I _{I(OFF)}	Off-State Input Current	3	V _{CE} = 50V, I _C = 500μA		50	65	—	μA
I _I	Input Current	4	V _I = 5V		—	0.35	0.5	mA
I _R	Clamp Reverse Current	7	V _R = 50V	T _A = +105°C	—	—	100	μA
				—	—	—	50	
C _I	Input Capacitance	—	V _I = 0, f = 1MHz		—	15	25	pF

Electrical Characteristics (@T_A = -40°C to +105°C, unless otherwise specified.)

ULN2003A							
Parameter		Test Figure	Test Conditions	Min	Typ	Max	Unit
V _{I(ON)}	On-State Input Voltage	6	V _{CE} = 2V	I _C = 200mA	—	—	2.7
				I _C = 250mA	—	—	2.9
				I _C = 300mA	—	—	3
V _{CE(SAT)}	Collector Emitter Saturation Voltage	5	I _I = 250μA, I _C = 100mA	—	0.9	1.2	V
			I _I = 350μA, I _C = 200mA	—	1	1.4	
			I _I = 500μA, I _C = 350mA	—	1.2	1.7	
V _F	Clamp Forward Voltage	8	I _F = 350mA	—	1.7	2.2	V
I _{CEX}	Collector Cutoff Current	1	V _{CE} = 50V, I _I = 0	—	—	100	μA
I _{I(OFF)}	Off-State Input Current	3	V _{CE} = 50V, I _C = 500μA	30	65	—	μA
I _I	Input Current	4	V _I = 3.85V	—	0.93	1.35	mA
I _R	Clamp Reverse Current	7	V _R = 50V	—	—	100	μA
C _I	Input Capacitance	—	V _I = 0, f = 1MHz	—	15	25	pF

Switching Characteristics (@T_A = +25°C, unless otherwise specified.)

ULN2002A/ULN2003A/ULN2004A						
Parameter		Test Figure	Min	Typ	Max	Unit
t _{PLH}	Propagation Delay Time, Low to High-Level Output	9	—	0.25	1	μs
t _{PHL}	Propagation Delay Time, High to Low-Level Output	9	—	0.25	1	μs
V _{OH}	High-Level Output Voltage after Switching	9 (V _S = 50V, I _O = 300mA)	V _S -20	—	—	mV

Switching Characteristics (@T_A = -40 to +105°C, unless otherwise specified.)

ULN2003A						
Parameter		Test Figure	Min	Typ	Max	Unit
t _{PLH}	Propagation Delay Time, Low to High-Level Output	9	—	1	10	μs
t _{PHL}	Propagation Delay Time, High to Low-Level Output	9	—	1	10	μs
V _{OH}	High-Level Output Voltage after Switching	9 (V _S = 50V, I _O = 300mA)	V _S -50	—	—	mV

Parameter Measurement Circuits

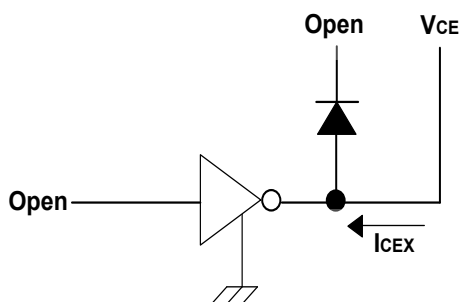


Fig.1 I_{CEX} Test Circuit

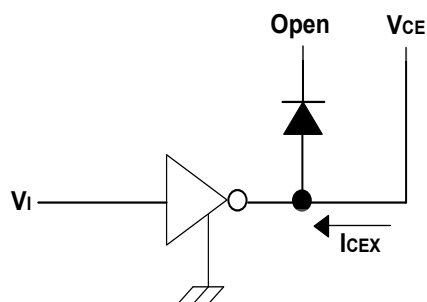


Fig.2 I_{CEX} Test Circuit

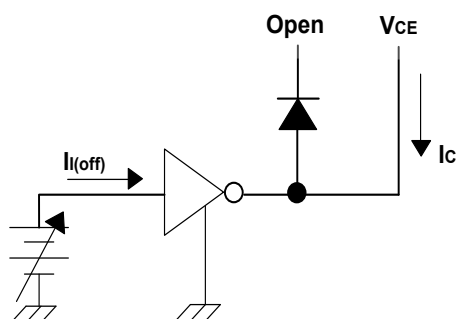


Fig.3 $I_{I(off)}$ Test Circuit

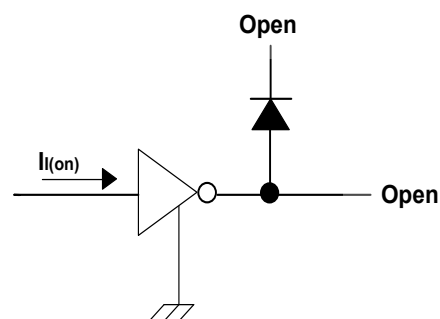


Fig.4 I_I Test Circuit

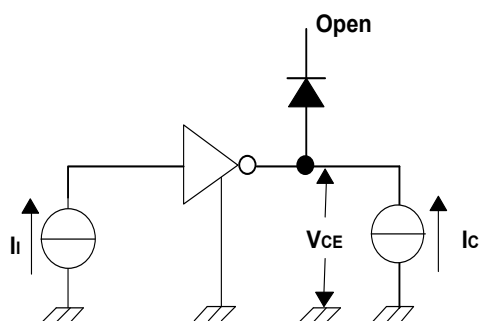


Fig.5 h_{FE} , $V_{CE(sat)}$ Test Circuit

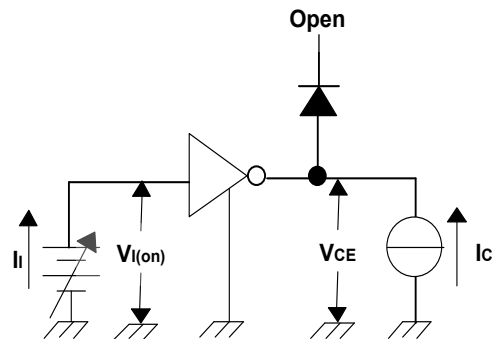


Fig.6 $V_{I(on)}$ Test Circuit

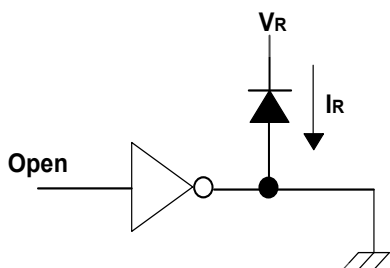


Fig.7 I_R Test Circuit

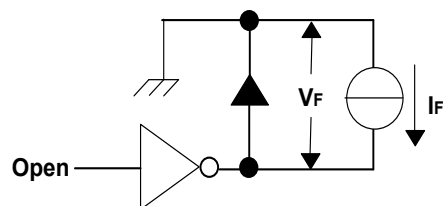


Fig.8 V_F Test Circuit

Parameter Measurement Circuits (continued)

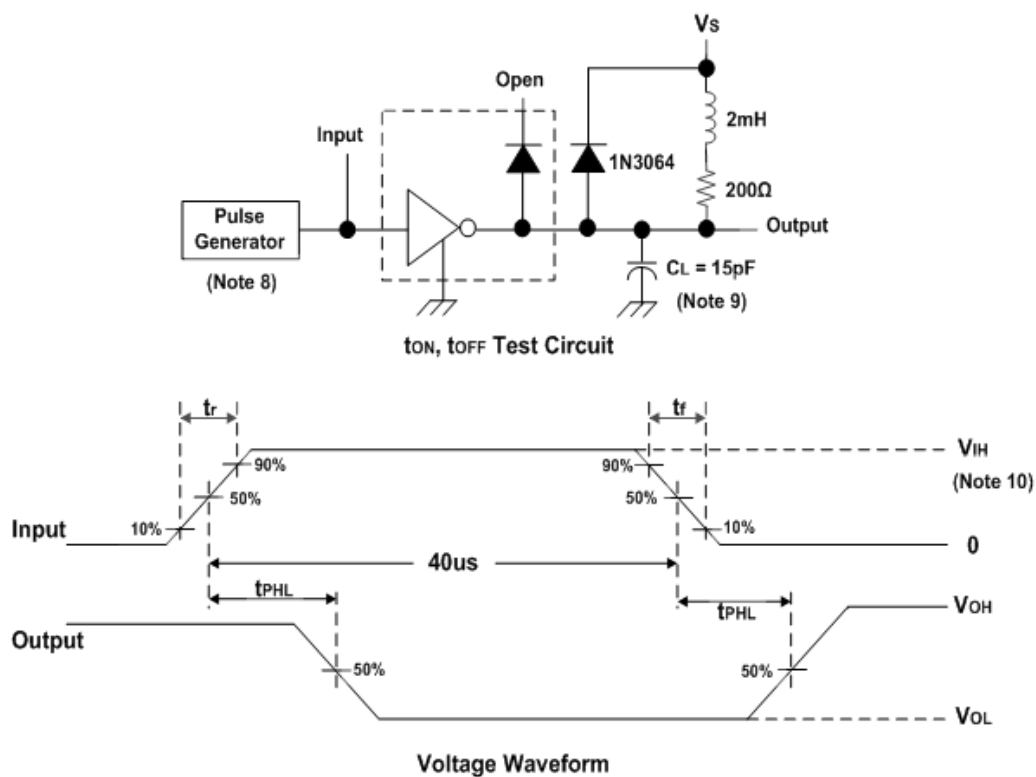
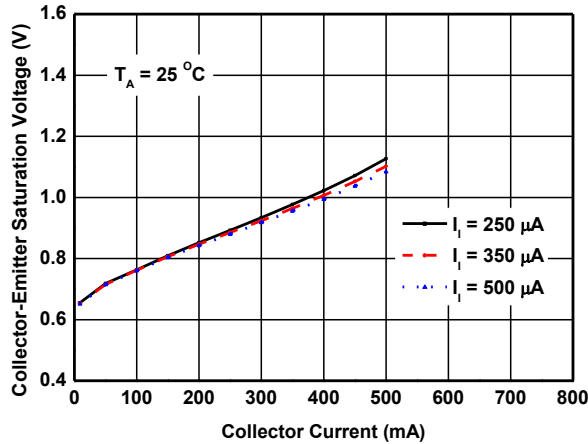


Fig. 9 Latchup Test Circuit and Voltage Waveform

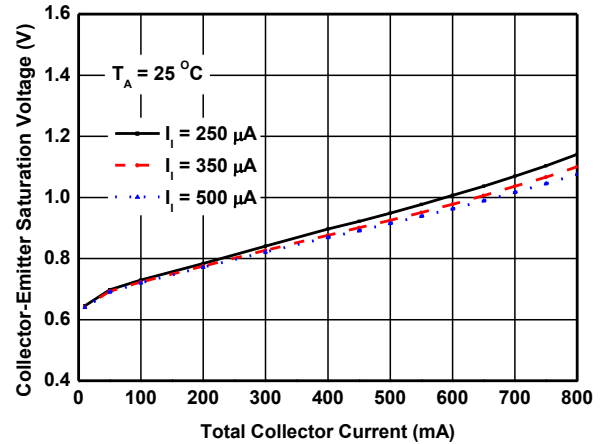
- Notes:
- 8. The pulse generator has the following characteristics: Pulse width = 12.5Hz, output impedance 50Ω, $t_r \leq 5\text{ns}$, $t_f \leq 10\text{ns}$.
 - 9. C_L includes probe and jig capacitance.
 - 10. For testing the ULN2002A, $V_{IH} = 13\text{V}$; for the ULN2003A, $V_{IH} = 3\text{V}$; for the ULN2004A, $V_{IH} = 8\text{V}$.

Typical Performance Characteristics

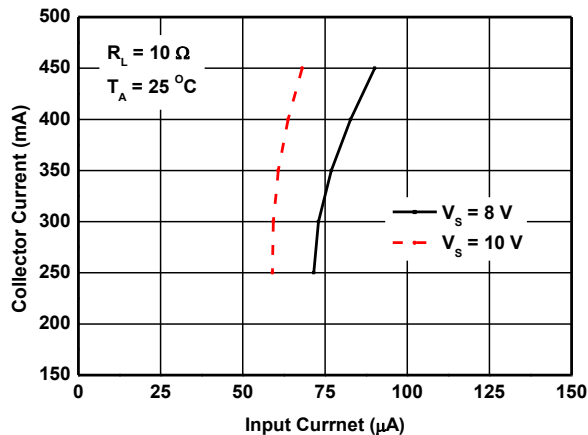
Collector-Emitter Saturation Voltage vs. Collector Current (One Darlington)



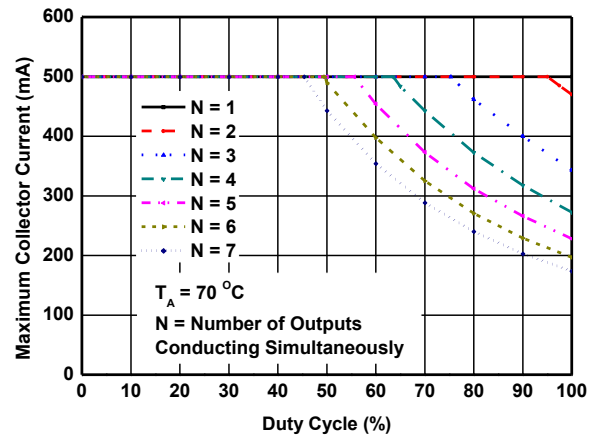
Collector-Emitter Saturation Voltage vs. Collector Current (Two Darlington in Parallel)



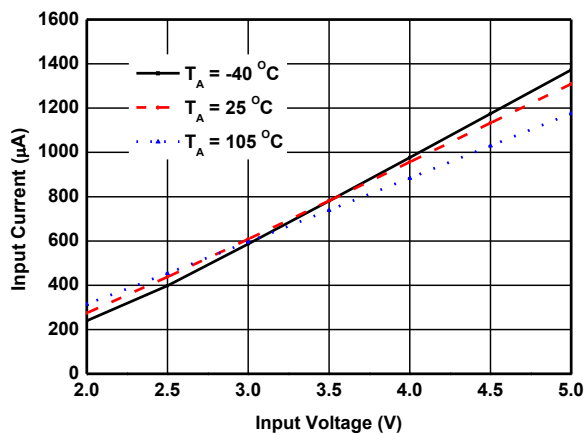
Collector Current vs. Input Current



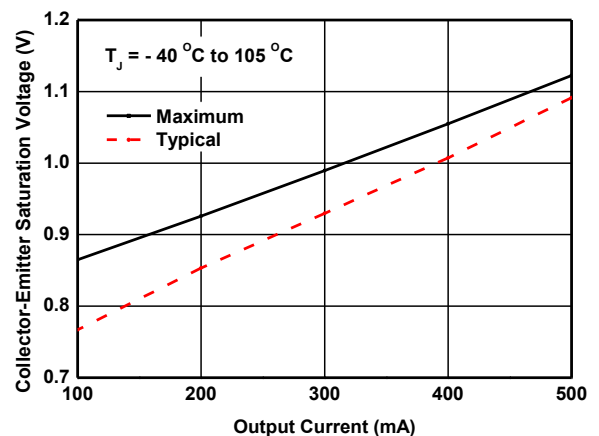
Maximum Collector Current vs. Duty Cycle



Input Current vs. Input Voltage

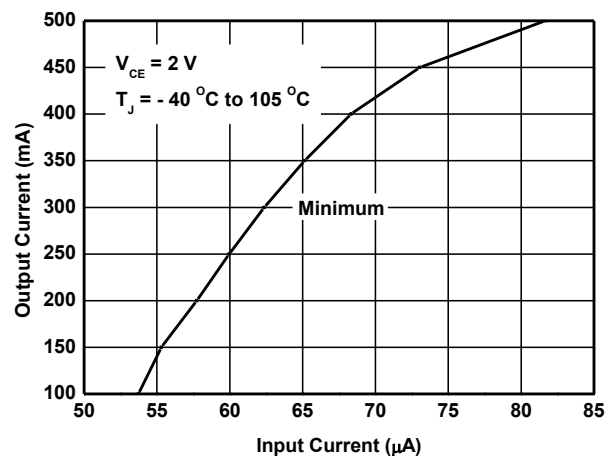


Collector-Emitter Saturation Voltage vs. Output Current

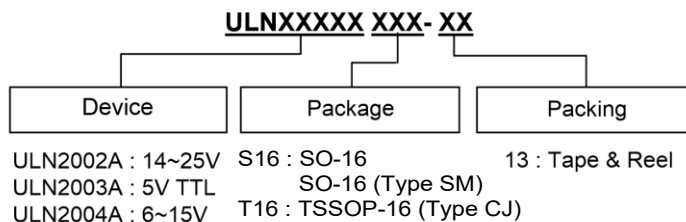


Typical Performance Characteristics (continued)

Output Current vs. Input Current



Ordering Information (Note 11)

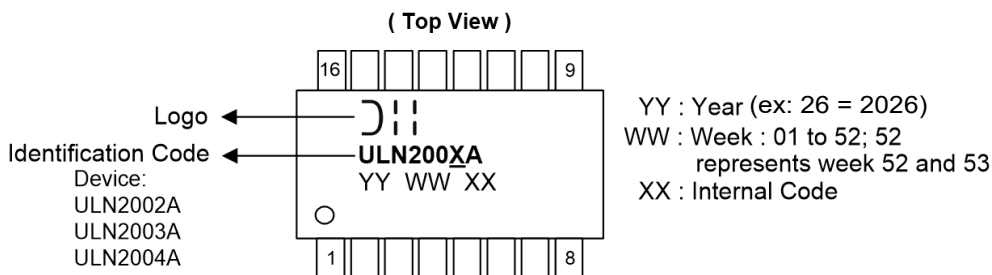


Orderable Part Number	Package Code	Package	Max. Thickness (mm)	Status	Packing	
					Qty.	Carrier
ULN2002AS16-13	S16	SO-16	1.26	Production	2,500	13" Tape and Reel
ULN2004AS16-13	S16	SO-16	1.26	Production	2,500	13" Tape and Reel
ULN2003AS16-13	S16	SO-16	1.26	NRND (Note 12)	2,500	13" Tape and Reel
ULN2003AFS16-13	S16	SO-16 (Type SM)	1.75	Production	2,500	13" Tape and Reel
ULN2003AT16-13	T16	TSSOP-16 (Type CJ)	1.2	Production	2,500	13" Tape and Reel

Notes: 11. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.
 12. NRND = Not Recommended for New Design. Please [contact us](#).

Marking Information

SO-16, SO-16 (Type SM), TSSOP-16 (Type CJ)

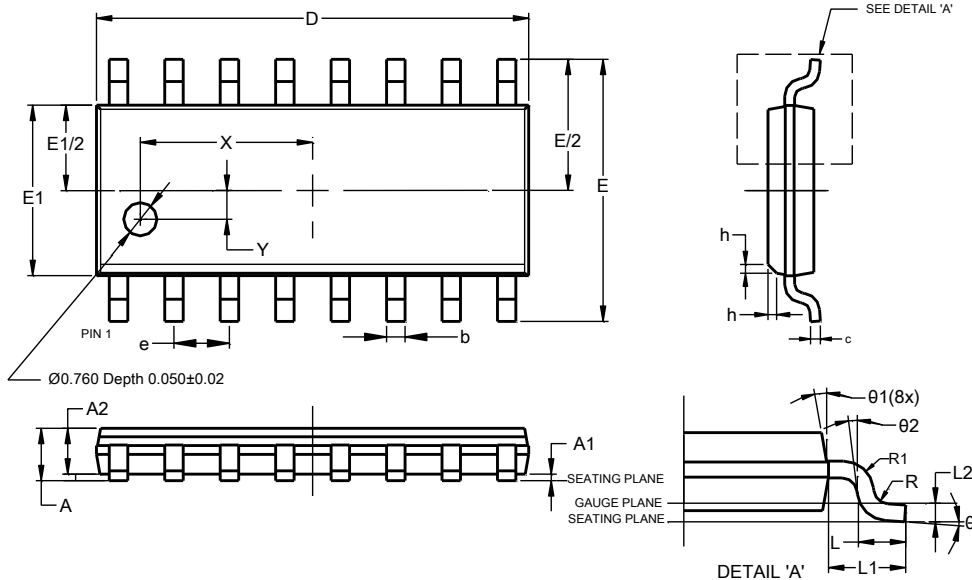


Orderable Part Number	Package	Identification Code
ULN2002AS16-13	SO-16	ULN2002A
ULN2004AS16-13	SO-16	ULN2004A
ULN2003AFS16-13	SO-16 (Type SM)	ULN2003A
ULN2003AT16-13	TSSOP-16 (Type CJ)	ULN2003A

Package Outline Dimensions

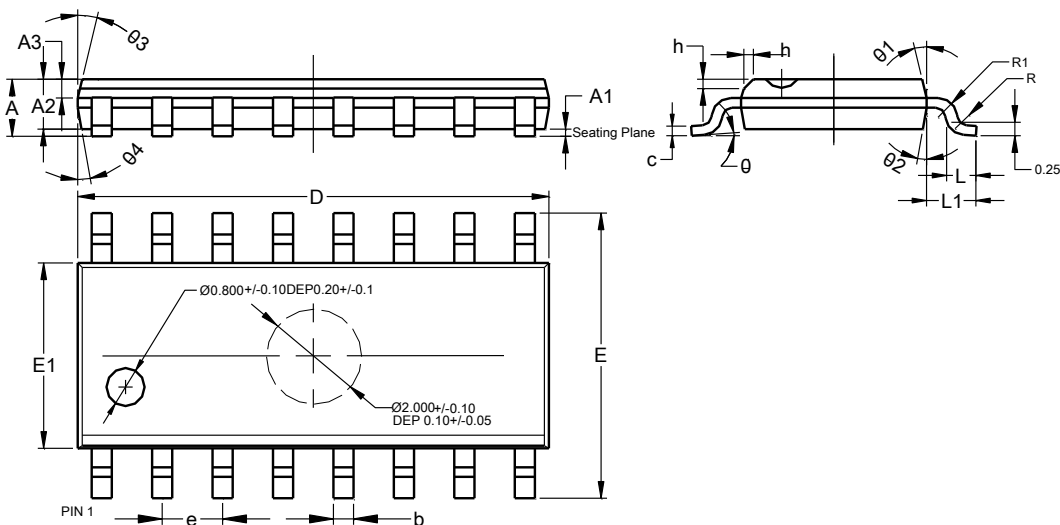
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-16



SO-16			
Dim	Min	Max	Typ
A	--	1.260	--
A1	0.10	0.23	--
A2	1.02	--	--
b	0.31	0.51	--
c	0.10	0.25	--
D	9.80	10.00	--
E	5.90	6.10	--
E1	3.80	4.00	--
e	1.27 BSC		
h	0.15	0.25	0.20
L	0.40	1.27	--
L1	1.04 REF		
L2	0.25 BSC		
R	0.07	--	--
R1	0.07	--	--
X	3.945 REF		
Y	0.661 REF		
θ	0°	8°	--
θ1	5°	15°	--
θ2	0°	--	--
All Dimensions in mm			

SO-16 (Type SM)

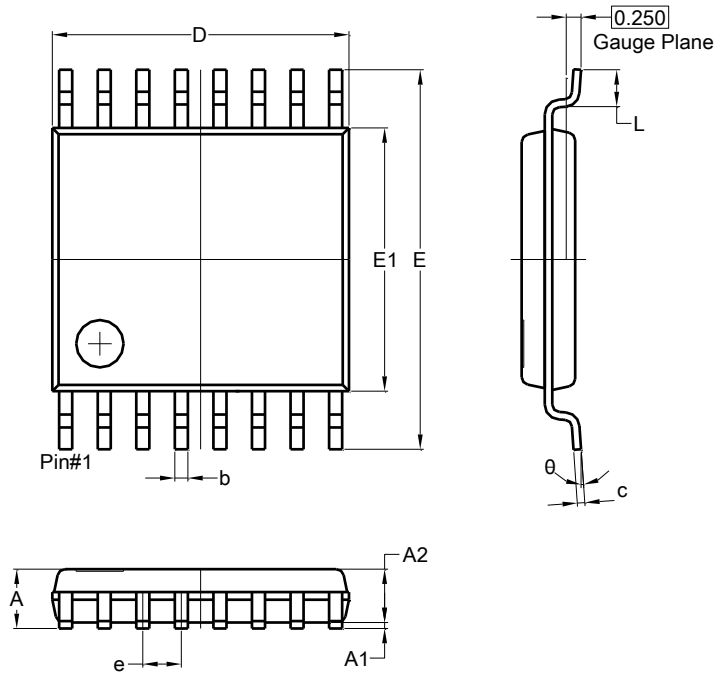


SO-16 (Type SM)			
Dim	Min	Max	Typ
A	1.35	1.75	1.60
A1	0.10	0.25	0.15
A2	1.25	1.65	1.45
A3	0.55	0.75	0.65
b	0.36	0.51	--
c	0.17	0.25	--
D	9.80	10.00	9.90
E	5.80	6.20	6.00
E1	3.80	4.00	3.90
e	1.27BSC		
h	0.30	0.50	0.40
L	0.45	0.80	0.60
L1	1.04REF		
R	0.07	--	--
R1	0.07	--	--
θ	0°	8°	--
θ1	10°	14°	12°
θ2	8°	12°	10°
θ3	10°	14°	12°
θ4	8°	12°	10°
All Dimensions in mm			

Package Outline Dimensions (continued)

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TSSOP-16 (Type CJ)

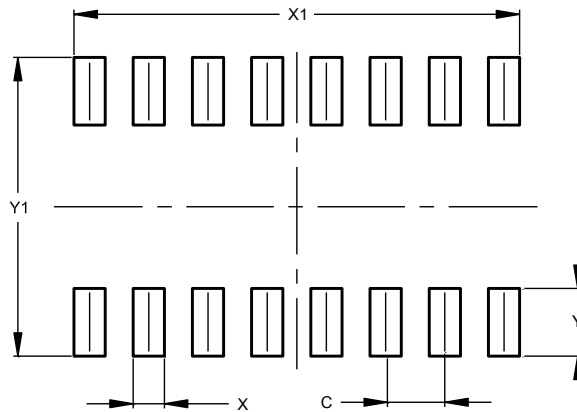


TSSOP-16 (Type CJ)			
Dim	Min	Max	Typ
A	--	1.200	--
A1	0.020	0.100	--
A2	0.800	1.000	--
b	0.190	0.300	--
c	0.090	0.200	--
D	4.900	5.100	--
E	6.250	6.550	--
E1	4.300	4.500	--
e	0.650 BSC		
L	0.500	0.700	--
θ	1°	7°	--
All Dimensions in mm			

Suggested Pad Layout

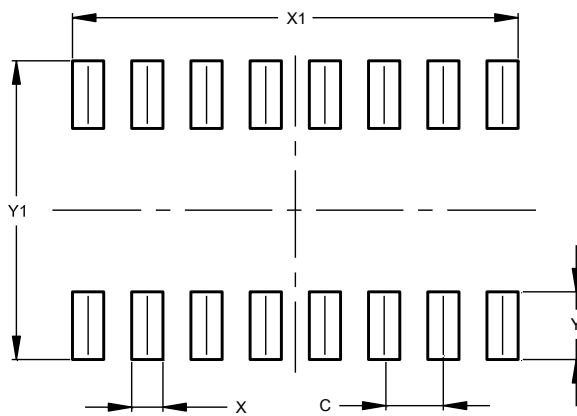
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-16



Dimensions	Value (in mm)
C	1.270
X	0.670
X1	9.560
Y	1.450
Y1	6.400

SO-16 (Type SM)

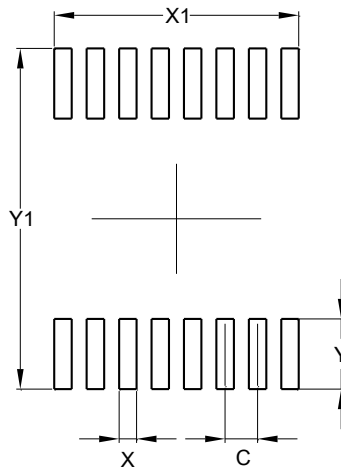


Dimensions	Value (in mm)
C	1.270
X	0.670
X1	9.560
Y	1.450
Y1	6.400

Suggested Pad Layout (continued)

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TSSOP-16 (Type CJ)



Dimensions	Value (in mm)
C	0.650
X	0.350
X1	4.900
Y	1.400
Y1	6.800

Mechanical Data

- Moisture Sensitivity:
 - SO-16: Level 1 per J-STD-020
 - SO-16 (Type SM): Level 3 per J-STD-020
 - TSSOP-16 (Type CJ): Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 **e3**
- Weight:
 - SO-16: 0.13 grams (Approximate)
 - SO-16 (Type SM): 0.15 grams (Approximate)
 - TSSOP-16 (Type CJ): 0.055 grams (Approximate)

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