

## Power MOSFET -30V, 150mΩ, -2A, Single P-Channel

This Low-profile High-power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and ultra low on resistance. This device is suitable for applications with low gate charge driving or ultra low on resistance requirements.

### Features

- Low On-Resistance
- 4V drive
- Pb-Free, Halogen Free and RoHS compliance
- Ultra small package SCH6 (1.6mm×1.6mm×0.56mm)

### Typical Applications

- LCD Driver
- Load Switch
- Voltage Protection

### SPECIFICATIONS

#### ABSOLUTE MAXIMUM RATING at $T_a = 25^\circ\text{C}$ (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	$V_{DSS}$	-30	V
Gate to Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current (DC)	$I_D$	-2	A
Drain Current (Pulse) $P_W \leq 10\mu\text{s}$ , duty cycle $\leq 1\%$	$I_{DP}$	-8	A
Power Dissipation When mounted on ceramic substrate (900mm <sup>2</sup> × 0.8mm)	$P_D$	0.8	W
Junction Temperature	$T_j$	150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

2 : This product is designed to "ESD immunity<200V\*", so please take care when handling.

\*Machine Model

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (900mm <sup>2</sup> × 0.8mm)	$R_{\theta JA}$	156.2	$^\circ\text{C/W}$

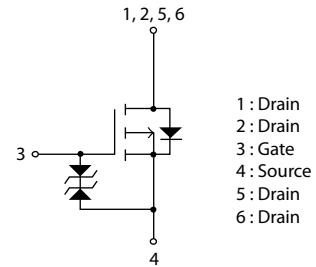


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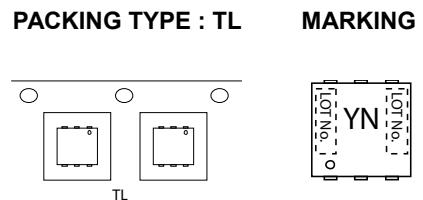
[www.onsemi.com](http://www.onsemi.com)

$V_{DSS}$	$R_{DS(on)}$ Max	$I_D$ Max
-30V	150mΩ@ -10V	-2A
	255mΩ@ -4.5V	
	292mΩ@ -4V	

### ELECTRICAL CONNECTION P-Channel



### PACKING TYPE : TL



### MARKING

### ORDERING INFORMATION

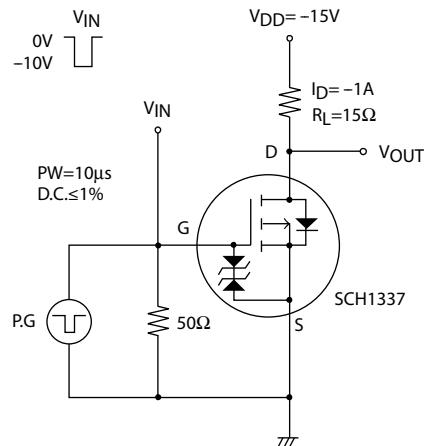
See detailed ordering and shipping information on page 5 of this data sheet.

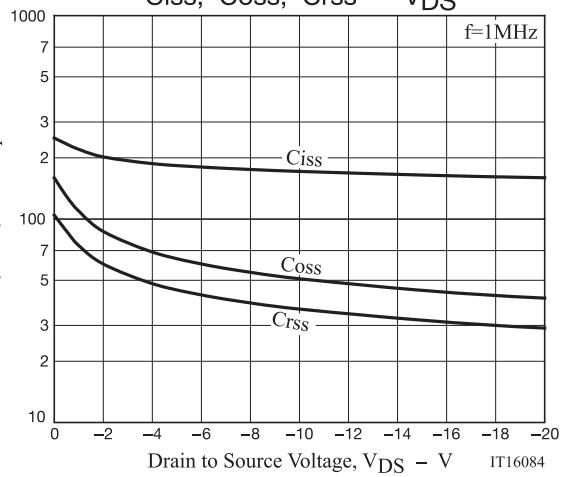
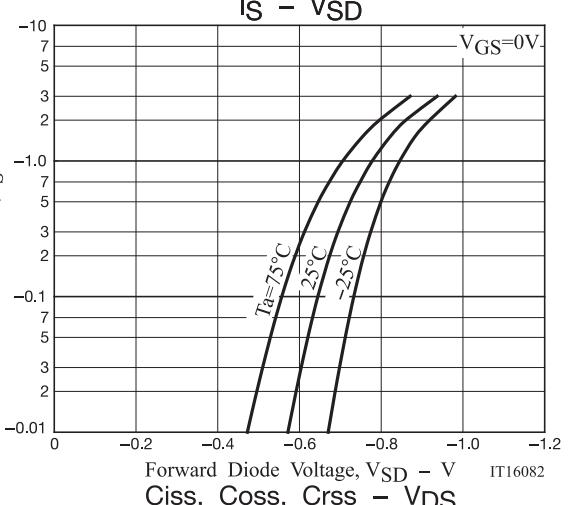
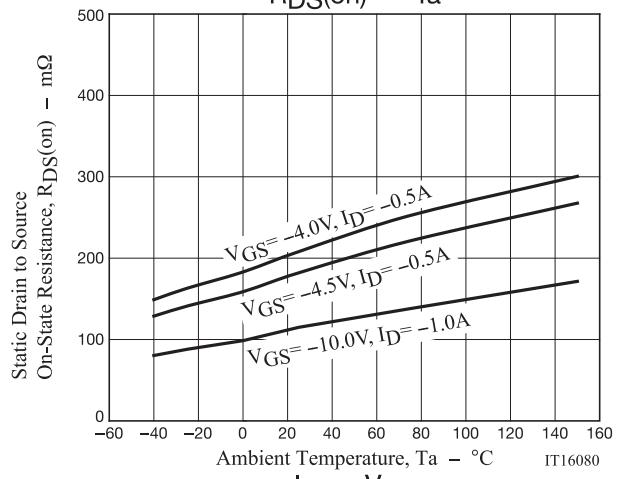
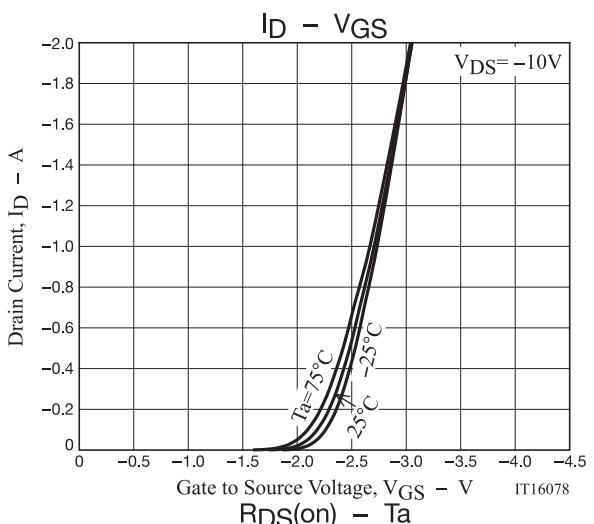
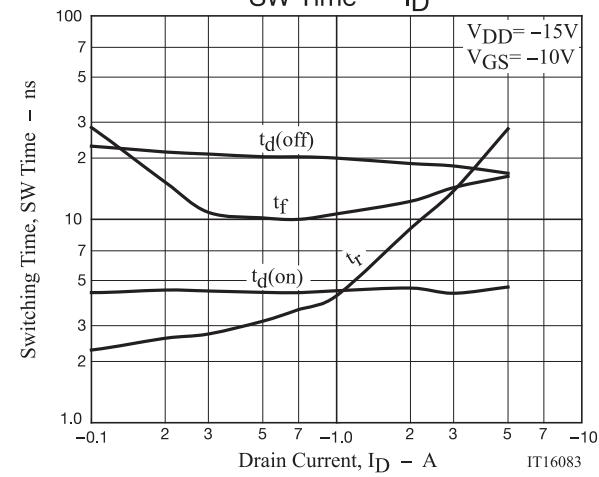
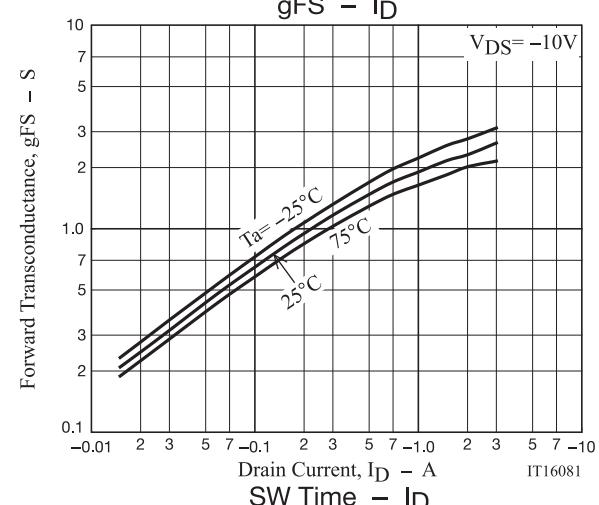
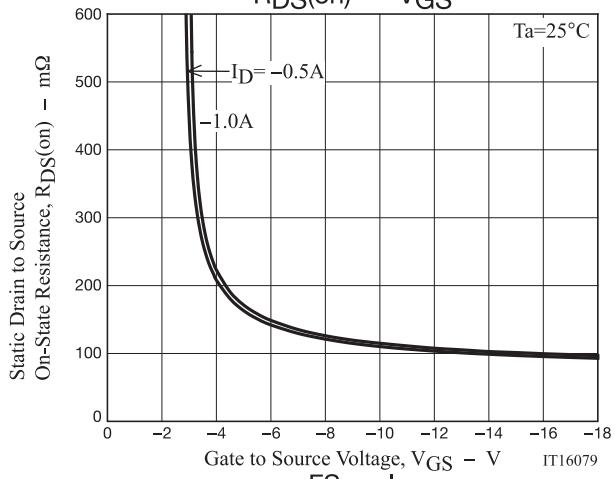
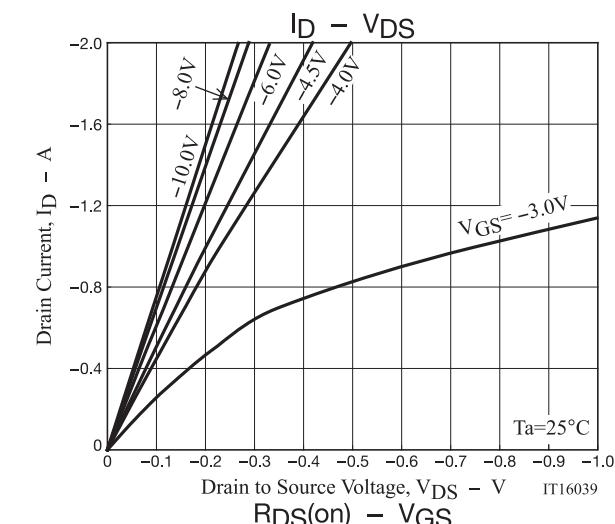
ELECTRICAL CHARACTERISTICS at  $T_a = 25^\circ\text{C}$  (Note 3)

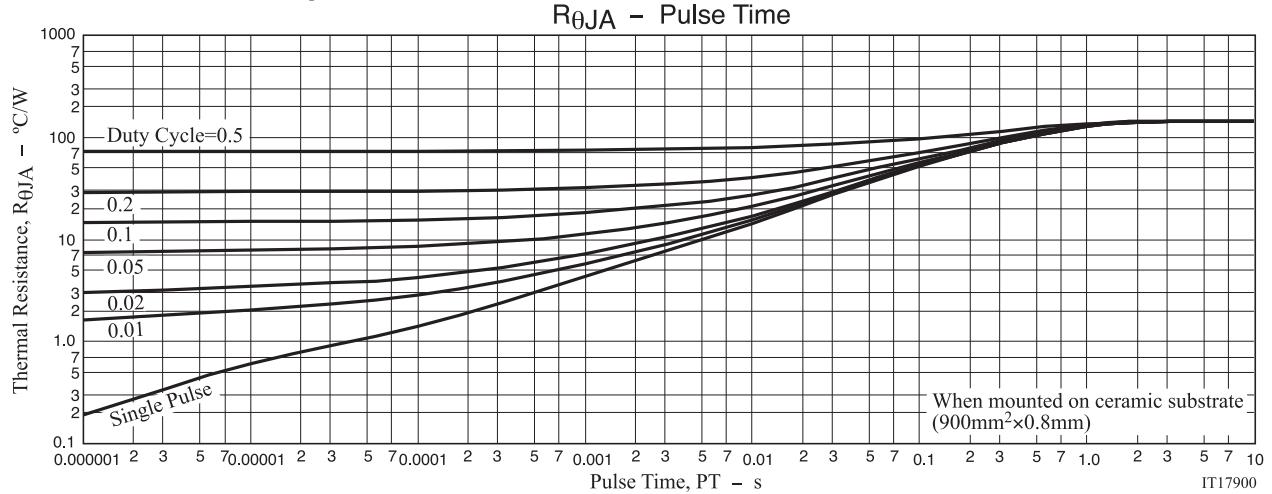
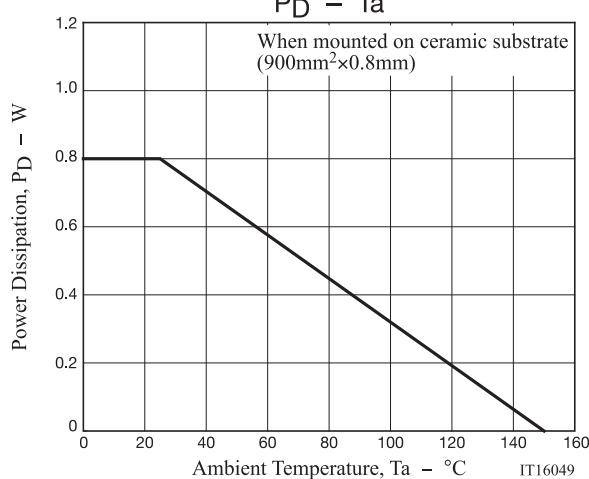
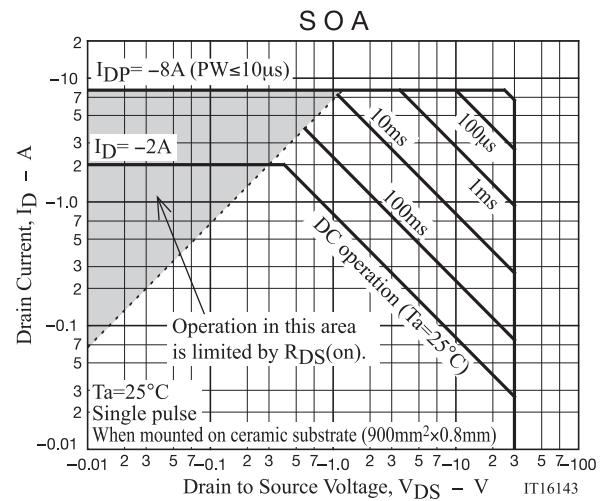
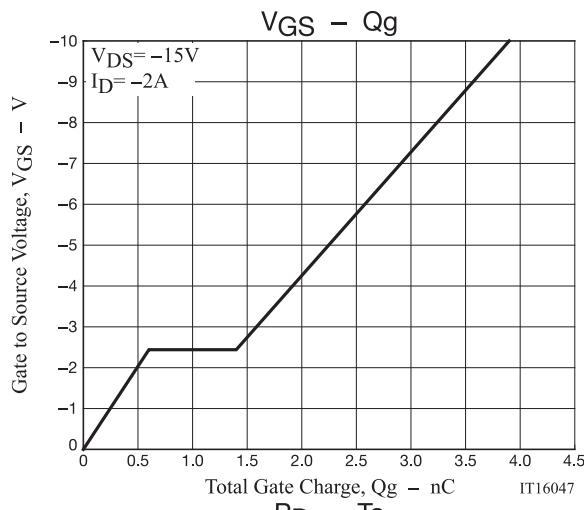
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V(\text{BR})_{\text{DSS}}$	$I_D = -1\text{mA}, V_{GS} = 0\text{V}$	-30			V
Zero-Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{DS} = -30\text{V}, V_{GS} = 0\text{V}$			-1	$\mu\text{A}$
Gate to Source Leakage Current	$I_{\text{GSS}}$	$V_{GS} = \pm 16\text{V}, V_{DS} = 0\text{V}$			$\pm 10$	$\mu\text{A}$
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = -10\text{V}, I_D = -1\text{mA}$	-1.2		-2.6	V
Forward Transconductance	$g_{\text{FS}}$	$V_{DS} = -10\text{V}, I_D = -1\text{A}$		1.9		S
Static Drain to Source On-State Resistance	$R_{DS(\text{on})1}$	$I_D = -1\text{A}, V_{GS} = -10\text{V}$		115	150	$\text{m}\Omega$
	$R_{DS(\text{on})2}$	$I_D = -0.5\text{A}, V_{GS} = -4.5\text{V}$		182	255	$\text{m}\Omega$
	$R_{DS(\text{on})3}$	$I_D = -0.5\text{A}, V_{GS} = -4\text{V}$		208	292	$\text{m}\Omega$
Input Capacitance	$C_{\text{iss}}$	$V_{DS} = -10\text{V}, f = 1\text{MHz}$		172		$\text{pF}$
Output Capacitance	$C_{\text{oss}}$			51		$\text{pF}$
Reverse Transfer Capacitance	$C_{\text{rss}}$			36		$\text{pF}$
Turn-ON Delay Time	$t_{\text{q(on)}}$			4.5		ns
Rise Time	$t_r$	See specified Test Circuit		4.2		ns
Turn-OFF Delay Time	$t_{\text{q(off)}}$			20		ns
Fall Time	$t_f$			10.6		ns
Total Gate Charge	$Q_g$			3.9		$\text{nC}$
Gate to Source Charge	$Q_{gs}$	$V_{DS} = -15\text{V}, V_{GS} = -10\text{V}, I_D = -2\text{A}$		0.6		$\text{nC}$
Gate to Drain "Miller" Charge	$Q_{gd}$			0.8		$\text{nC}$
Forward Diode Voltage	$V_{SD}$	$I_S = -2\text{A}, V_{GS} = 0\text{V}$		-0.86	-1.5	V

Note 3 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## Switching Time Test Circuit







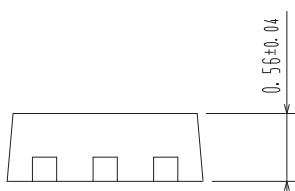
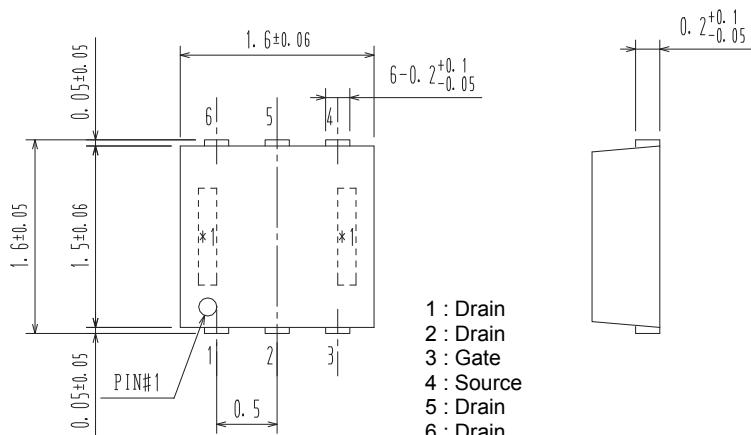
## PACKAGE DIMENSIONS

unit : mm

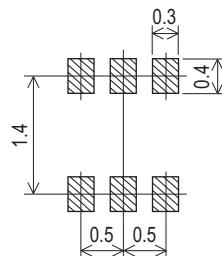
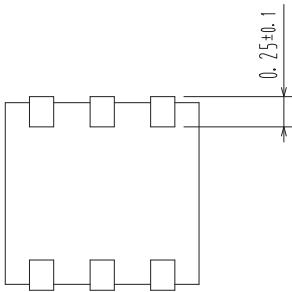
SOT-563 / SCH6

CASE 463AB

ISSUE O



**Recommended  
Soldering Footprint**



## ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
SCH1337-TL-H	YN	SOT-563 / SCH6 (Pb-Free / Halogen Free)	5,000 / Tape & Reel
SCH1337-TL-W			

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. [http://www.onsemi.com/pub\\_link/Collateral/BRD8011-D.PDF](http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF)

Note on usage : Since the SCH1337 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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