

XC9103/XC9104/XC9105 Series

ETR04004-010d

Ceramic Capacitor Compatible, Step-up DC/DC Controllers

☆Green Operation Compatible

■GENERAL DESCRIPTION

The XC9103/XC9104/XC9105 series are PWM, PWM/PFM auto switching /manual switching controlled universal step-up DC/DC converter controllers.

Output will be stable no matter which load capacitors are used but should a low ESR capacitor be used, R_{SENSE} of about 0.1Ω will be required and phase compensation will be achieved. This allows the use of ceramic capacitors and enables to obtain lower output ripple and small PCB design. Tantalum and electrolytic capacitors can also be used, in which case, R_{SENSE} becomes unnecessary.

With $0.9V$ internal voltage reference and by using externally connected two resistors, output voltage can be set freely within a range of $1.5V$ to $30V$. The series is available in $300kHz$ and $180kHz$ frequencies, the size of the external components can be reduced. $100kHz$ and $500kHz$ are also available in custom options.

The XC9103 offers PWM operation. The XC9104 offers PWM/PFM automatic switching operation. The PWM operation is shifted to the PFM operation automatically at light load so that it maintains high efficiency over a wide range of load currents. The XC9105 offers both PWM and PWM/PFM auto switching operations and it can be selected by external signal.

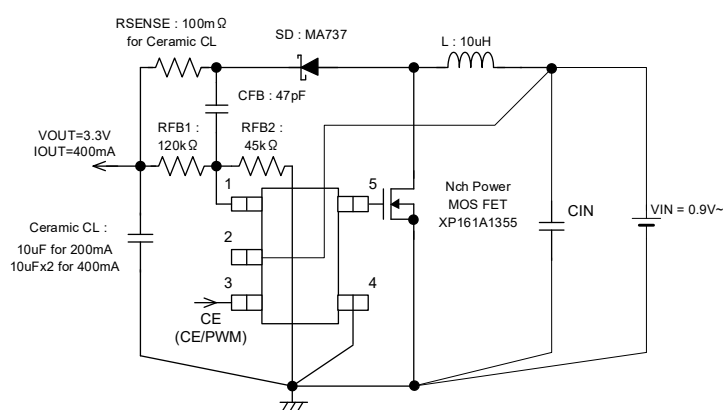
■APPLICATIONS

- E-book Readers / Electronic dictionaries
- Smart phones / Mobile phones
- Note PCs / Tablet PCs
- Digital audio equipments
- Multi-function power supplies

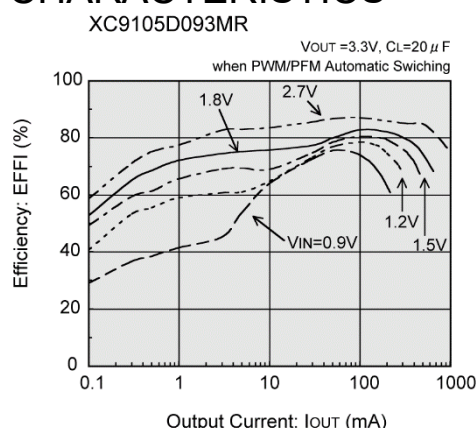
■FEATURES

Input Voltage Range	: $0.9V \sim 10V$
Supply Voltage Range	: $1.8V \sim 10V$
Output Voltage Range	: $1.5V \sim 30V$ Set freely with the reference voltage $0.9V(\pm 2.0\%)$ and two resistors
Oscillation Frequency	: $100kHz, 180kHz, 300kHz, 500kHz (\pm 15\%)$
Output Current	: more than $400mA$ ($V_{IN}=1.8V, V_{OUT}=3.3V$)
Controls	: PWM (XC9103) PWM/PFM auto-switching (XC9104) PWM/PFM manual switching (XC9105)
High Efficiency	: 85% (TYP.)
Stand-by Current	: $I_{STB}=1.0\mu A$ (MAX.)
Load Capacitors	: Low ESR capacitors compatible
Packages	: SOT-25, USP-6B
Environmentally Friendly	: EU RoHS Compliant, Pb Free

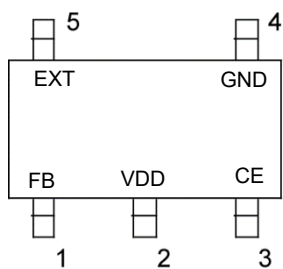
■TYPICAL APPLICATION CIRCUIT



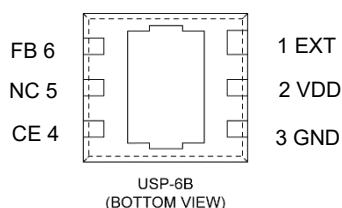
■TYPICAL PERFORMANCE CHARACTERISTICS



PIN CONFIGURATION



SOT-25
(TOP VIEW)



The dissipation pad for the USP-6B package should be solder-plated in recommended mount pattern and metal masking so as to enhance mounting strength and heat release. If the pad needs to be connected to other pins, it should be connected to the VDD (No.2) pin.

PIN ASSIGNMENT

PIN NUMBER		PIN NAME	FUNCTION
SOT-25	USP-6B		
1	6	FB	Output Resistor Connection
2	2	VDD	Supply Voltage
3	4	CE	Chip Enable
		CE (/PWM)	Serves as both PWM/PFM switching pin and CE pin for XC9105
4	3	GND	Ground
5	1	EXT	External Transistor Connection
-	5	NC	No Connection

FUNCTION CHART

XC9103/XC9104 Series

CE PIN	STATUS
H	Operation
L	Shut-Down

XC9105 Series

CE/PWM PIN		STATUS
H	More than $V_{DD}-0.2V$	Operation (PWM control)
M	$0.65 \sim V_{DD}-1.0V$	Operation (PWM/PFM automatic switching control)
L	$0 \sim 0.2V$	Shut-Down

PRODUCT CLASSIFICATION

Ordering Information

XC9103①②③④⑤⑥-⑦^(*): PWM Control

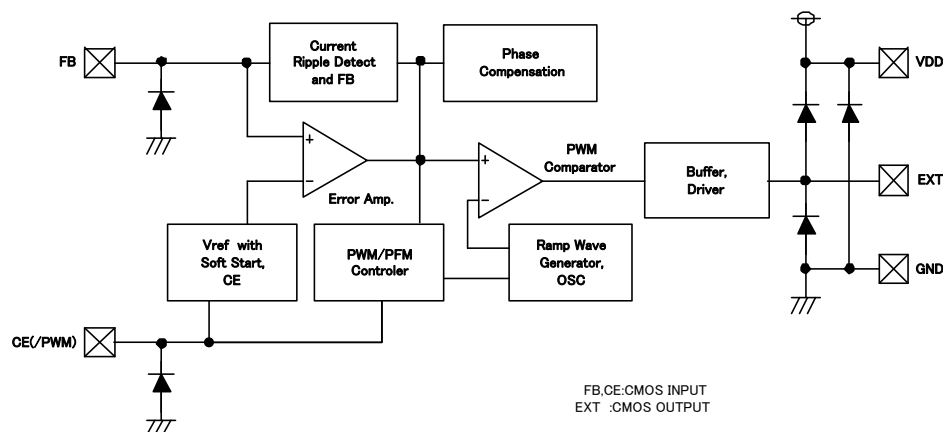
XC9104①②③④⑤⑥-⑦^(*): PWM/PFM Automatic Switching Control

XC9105①②③④⑤⑥-⑦^(*): PWM/PFM Manual Switching Control

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
①	Type of DC/DC Controller	D	Without current limiter
②③	Output Voltage	09	FB voltage (e.g. FB Voltage=0.9V→②=0, ③=9)
④	Oscillation Frequency	3	300kHz
		1	100kHz
		2	180kHz
		5	500kHz
⑤⑥-⑦	Packages (Order Unit)	MR-G	SOT-25 (3,000pcs/Reel)
		DR-G	USP-6B (3,000pcs/Reel)

(*) The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNITS
VDD pin Voltage		VDD	-0.3 ~ 12.0	V
FB pin Voltage		FB	-0.3 ~ 12.0	V
CE pin Voltage		VCE	-0.3 ~ 12.0	V
EXT pin Voltage		VEXT	-0.3 ~ VDD + 0.3	V
EXT pin Current		IEXT/	±100	mA
Power Dissipation (Ta=25°C)	SOT-25	Pd	250 (IC only)	mW
			600 (40mm x 40mm Standard board) ^(*)	
			760 (JESD51-7 board) ^(*)	
	USP-6B		120 (IC only)	
			1000 (40mm x 40mm Standard board) ^(*)	
Operating Ambient Temperature		Topr	-40 ~ 85	°C
Storage Temperature		Tstg	-55 ~ 125	°C

(*) The power dissipation figure shown is PCB mounted and is for reference only.

Please refer to PACKAGING INFORMATION for the mounting condition.

ELECTRICAL CHARACTERISTICS

XC9103D091, XC9104D091, XC9105D091

($f_{osc}=100kHz$)

Ta=25°C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage	V _{OUT}		3.234	3.300	3.366	V	①
Output Voltage Range	V _{OUTSET}	V _{IN} =V _{OUTSET} ×0.6, V _{DD} =3.3V I _{OUT} =10mA, Using 2SD1628	1.5	-	30.0	V	②
FB Control Voltage	V _{FB}		0.882	0.900	0.918	V	④
Supply Voltage Range (*1)	V _{DD}		1.8	-	10.0	V	
Operation Start Voltage	V _{ST1}	Recommended circuit using 2SD1628, I _{OUT} =1.0mA	-	-	0.9	V	③
Oscillation Start Voltage (*1)	V _{ST2}	No external components, CE connected to VDD, Voltage applied, FB=0V	-	-	0.8	V	④
Operation Hold Voltage	V _{HLD}	Recommended circuit using 2SD1628, I _{OUT} =1.0mA	-	-	0.7	V	③
Supply Current 1	I _{DD1}	Same as V _{ST2} , V _{DD} =3.3V	-	29	41	μA	④
Supply Current 2	I _{DD2}	Same as I _{DD1} , FB=1.2V	-	14	19	μA	④
Stand-by Current	I _{STB}	Same as I _{DD1} , CE=0V	-	-	1.0	μA	⑤
Oscillation Frequency	f _{OSC}	Same as I _{DD1}	85	100	115	kHz	④
Maximum Duty Cycle	MAXDTY	Same as I _{DD1}	75	81	87	%	④
PFM Duty Rate	PFMDTY	No load (XC9104D, XC9105D)	20	28	36	%	①
Efficiency	EFFI	Recommended circuit using XP161A1355	-	85	-	%	①
Soft-Start Time	t _{SS}		5.0	10.0	20.0	ms	①
CE "H" Voltage (*2)	V _{CEH}	Same as I _{DD1}	0.65	-	-	V	⑤
CE "L" Voltage (*2)	V _{CEL}	Same as I _{DD1}	-	-	0.20	V	⑤
PWM "H" Voltage (*2)	V _{PWMH}	I _{OUT} =1.0mA (XC9105D)	V _{DD} -0.2	-	-	V	①
PWM "L" Voltage (*2)	V _{PWML}	I _{OUT} =1.0mA (XC9105D)	-	-	V _{DD} -1.0	V	①
EXT "H" On Resistance	R _{EXTH}	Same as I _{DD1} , V _{EXT} =V _{OUT} -0.4V	-	24	36	Ω	④
EXT "L" On Resistance	R _{EXTL}	Same as I _{DD2} , V _{EXT} =0.4V	-	16	24	Ω	④
CE "H" Current	I _{CEH}	Same as I _{DD2} , CE=V _{DD}	-	-	0.1	μA	⑤
CE "L" Current	I _{CEL}	Same as I _{DD2} , CE=0V	-	-	-0.1	μA	⑤
FB "H" Current	I _{FBH}	Same as I _{DD2} , FB=V _{DD}	-	-	0.1	μA	⑤
FB "L" Current	I _{FBL}	Same as I _{DD2} , FB=1V	-	-	-0.1	μA	⑤

Test Conditions: Unless otherwise stated, C_L: ceramic, recommended MOSFET should be connected.

V_{OUT}=3.3V, V_{IN}=2.0V, I_{OUT}=170mA

NOTE:

*1 Although the IC starts step-up operations from a V_{DD} of 0.8V, the output voltage and oscillation frequency are stabilized at V_{DD} ≥ 1.8V. Therefore, a V_{DD} of more than 1.8V is recommended when V_{DD} is supplied from V_{IN} or other power sources.

*2 With the XC9105 series, the CE pin also serves as a PWM/PFM switching pin. In operation, PWM control is selected when the voltage at the CE pin is more than V_{DD} -0.2V. On the other hand, PWM/PFM automatic switching control at a duty = 25% is selected when the voltage at the CE pin is less than V_{DD} -1.0V and more than V_{CEH}.

ELECTRICAL CHARACTERISTICS

XC9103D092MR, XC9104D092MR, XC9105D092MR

($f_{osc}=180kHz$)

Ta=25°C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage	V _{OUT}		3.234	3.300	3.366	V	①
Output Voltage Range	V _{OUTSET}	V _{IN} =V _{OUTSET} ×0.6, V _{DD} =3.3V I _{OUT} =10mA, Using 2SD1628	1.5	-	30.0	V	②
FB Control Voltage	V _{FB}		0.882	0.900	0.918	V	④
Supply Voltage Range (*1)	V _{DD}		1.8	-	10.0	V	
Operation Start Voltage	V _{ST1}	Recommended circuit using 2SD1628, I _{OUT} =1.0mA	-	-	0.9	V	③
Oscillation Start Voltage (*1)	V _{ST2}	No external components, CE connected to V _{DD} , Voltage applied, FB=0V	-	-	0.8	V	④
Operation Hold Voltage	V _{HLD}	Recommended circuit using 2SD1628, I _{OUT} =1.0mA	-	-	0.7	V	③
Supply Current 1	I _{DD1}	Same as V _{ST2} , V _{DD} =3.3V	-	45	64	μA	④
Supply Current 2	I _{DD2}	Same as I _{DD1} , FB=1.2V	-	17	24	μA	④
Stand-by Current	I _{STB}	Same as I _{DD1} , CE=0V	-	-	1.0	μA	⑤
Oscillation Frequency	f _{osc}	Same as I _{DD1}	153	180	207	kHz	④
Maximum Duty Cycle	MAXDTY	Same as I _{DD1}	75	81	87	%	④
PFM Duty Rate	PFMDTY	No load (XC9104D,XC9105D)	20	28	36	%	①
Efficiency	EFFI	Recommended circuit using XP161A1355	-	85	-	%	①
Soft-Start Time	t _{ss}		5.0	10.0	20.0	ms	①
CE "H" Voltage (*2)	V _{CEH}	Same as I _{DD1}	0.65	-	-	V	⑤
CE "L" Voltage (*2)	V _{CEL}	Same as I _{DD1}	-	-	0.20	V	⑤
PWM "H" Voltage (*2)	V _{PWMH}	I _{OUT} =1.0mA (XC9105D)	V _{DD} -0.2	-	-	V	①
PWM "L" Voltage (*2)	V _{PWML}	I _{OUT} =1.0mA (XC9105D)	-	-	V _{DD} -1.0	V	①
EXT "H" On Resistance	R _{EXTH}	Same as I _{DD1} , V _{EXT} =V _{OUT} -0.4V	-	24	36	Ω	④
EXT "L" On Resistance	R _{EXTL}	Same as I _{DD2} , V _{EXT} =0.4V	-	16	24	Ω	④
CE "H" Current	I _{CEH}	Same as I _{DD2} , CE=V _{DD}	-	-	0.1	μA	⑤
CE "L" Current	I _{CEL}	Same as I _{DD2} , CE=0V	-	-	-0.1	μA	⑤
FB "H" Current	I _{FBH}	Same as I _{DD2} , FB=V _{DD}	-	-	0.1	μA	⑤
FB "L" Current	I _{FBL}	Same as I _{DD2} , FB=1V	-	-	-0.1	μA	⑤

Test Conditions: Unless otherwise stated, C_L: ceramic, recommended MOSFET should be connected.

V_{OUT}=3.3V, V_{IN}=2.0V, I_{OUT}=170mA

NOTE:

*1 Although the IC starts step-up operations from a V_{DD} of 0.8V, the output voltage and oscillation frequency are stabilized at V_{DD} ≥ 1.8V. Therefore, a V_{DD} of more than 1.8V is recommended when V_{DD} is supplied from V_{IN} or other power sources.

*2 With the XC9105 series, the CE pin also serves as a PWM/PFM switching pin. In operation, PWM control is selected when the voltage at the CE pin is more than V_{DD} - 0.2V. On the other hand, PWM/PFM automatic switching control at a duty = 25% is selected when the voltage at the CE pin is less than V_{DD} - 1.0V and more than V_{CEH}.

ELECTRICAL CHARACTERISTICS (Continued)

XC9103D093MR, XC9104D093MR, XC9105D093MR

(f_{osc}=300kHz)

T_a=25°C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage	V _{OUT}		3.234	3.300	3.366	V	①
Output Voltage Range	V _{OUTSET}	V _{IN} =V _{OUTSET} ×0.6, V _{DD} =3.3V I _{OUT} =10mA, Using 2SD1628	1.5	-	30.0	V	②
FB Control Voltage	V _{FB}		0.882	0.900	0.918	V	④
Supply Voltage Range (*1)	V _{DD}		1.8	-	10.0	V	
Operation Start Voltage	V _{ST1}	Recommended circuit using 2SD1628, I _{OUT} =1.0mA	-	-	0.9	V	③
Oscillation Start Voltage (*1)	V _{ST2}	No external components, CE connected to V _{DD} , Voltage applied, FB=0V	-	-	0.8	V	④
Operation Hold Voltage	V _{HLD}	Recommended circuit using 2SD1628, I _{OUT} =1.0mA	-	-	0.7	V	③
Supply Current 1	I _{DD1}	Same as V _{ST2} , V _{DD} =3.3V	-	62	88	μA	④
Supply Current 2	I _{DD2}	Same as I _{DD1} , FB=1.2V	-	16	22	μA	④
Stand-by Current	I _{STB}	Same as I _{DD1} , CE=0V	-	-	1.0	μA	⑤
Oscillation Frequency	f _{osc}	Same as I _{DD1}	255	300	345	kHz	④
Maximum Duty Cycle	MAXDTY	Same as I _{DD1}	75	81	87	%	④
PFM Duty Rate	PFMDTY	No load (XC9104D, XC9105D)	24	32	40	%	①
Efficiency	EFFI	Recommended circuit using XP161A1355	-	85	-	%	①
Soft-Start Time	t _{ss}		5.0	10.0	20.0	ms	①
CE "H" Voltage (*2)	V _{CEH}	Same as I _{DD1}	0.65	-	-	V	⑤
CE "L" Voltage (*2)	V _{CEL}	Same as I _{DD1}	-	-	0.20	V	⑤
PWM "H" Voltage (*2)	V _{PWMH}	I _{OUT} =1.0mA (XC9105D)	V _{DD} -0.2	-	-	V	①
PWM "L" Voltage (*2)	V _{PWML}	I _{OUT} =1.0mA (XC9105D)	-	-	V _{DD} -1.0	V	①
EXT "H" On Resistance	R _{EXTH}	Same as I _{DD1} , V _{EXT} =V _{OUT} -0.4V	-	24	36	Ω	④
EXT "L" On Resistance	R _{EXTL}	Same as I _{DD2} , V _{EXT} =0.4V	-	16	24	Ω	④
CE "H" Current	I _{CEH}	Same as I _{DD2} , CE=V _{DD}	-	-	0.1	μA	⑤
CE "L" Current	I _{CEL}	Same as I _{DD2} , CE=0V	-	-	-0.1	μA	⑤
FB "H" Current	I _{FBH}	Same as I _{DD2} , FB=V _{DD}	-	-	0.1	μA	⑤
FB "L" Current	I _{FBL}	Same as I _{DD2} , FB=1V	-	-	-0.1	μA	⑤

Test Conditions: Unless otherwise stated, C_L: ceramic, recommended MOSFET should be connected.

V_{OUT}=3.3V, V_{IN}=2.0V, I_{OUT}=170mA

NOTE:

*1 Although the IC starts step-up operations from a V_{DD} of 0.8V, the output voltage and oscillation frequency are stabilized at V_{DD}≥1.8V. Therefore, a V_{DD} of more than 1.8V is recommended when V_{DD} is supplied from V_{IN} or other power sources.

*2 With the XC9105 series, the CE pin also serves as a PWM/PFM switching pin. In operation, PWM control is selected when the voltage at the CE pin is more than V_{DD}-0.2V. On the other hand, PWM/PFM automatic switching control at a duty = 25% is selected when the voltage at the CE pin is less than V_{DD}-1.0V and more than V_{CEH}.

■ ELECTRICAL CHARACTERISTICS (Continued)

XC9103D095, XC9104D095, XC9105D095

(f_{osc}=500kHz)

T_a=25°C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage	V _{OUT}		3.234	3.300	3.366	V	①
Output Voltage Range	V _{OUTSET}	V _{IN} =V _{OUTSET} ×0.6, V _{DD} =3.3V I _{OUT} =10mA, Using 2SD1628	1.5	-	30.0	V	②
FB Control Voltage	V _{FB}		0.882	0.900	0.918	V	④
Supply Voltage Range (*1)	V _{DD}		1.8	-	10.0	V	
Operation Start Voltage	V _{ST1}	Recommended circuit using 2SD1628, I _{OUT} =1.0mA	-	-	0.9	V	③
Oscillation Start Voltage (*1)	V _{ST2}	No external components, CE connected to V _{DD} , Voltage applied, FB=0V	-	-	0.8	V	④
Operation Hold Voltage	V _{HLD}	Recommended circuit using 2SD1628, I _{OUT} =1.0mA	-	-	0.7	V	③
Supply Current 1	I _{DD1}	Same as V _{ST2} , V _{DD} =3.3V	-	97	137	μA	④
Supply Current 2	I _{DD2}	Same as I _{DD1} , FB=1.2V	-	20	28	μA	④
Stand-by Current	I _{STB}	Same as I _{DD1} , CE=0V	-	-	1.0	μA	⑤
Oscillation Frequency	f _{osc}	Same as I _{DD1}	425	500	575	kHz	④
Maximum Duty Cycle	MAXDTY	Same as I _{DD1}	74	80	86	%	④
PFM Duty Rate	PFMDTY	No load (XC9104D, XC9105D)	24	32	40	%	①
Efficiency	EFFI	Recommended circuit using XP161A1355	-	85	-	%	①
Soft-Start Time	t _{ss}		5.0	10.0	20.0	ms	①
CE "H" Voltage (*2)	V _{CEH}	Same as I _{DD1}	0.65	-	-	V	⑤
CE "L" Voltage (*2)	V _{CEL}	Same as I _{DD1}	-	-	0.20	V	⑤
PWM "H" Voltage (*2)	V _{PWMH}	I _{OUT} =1.0mA (XC9105D)	V _{DD} -0.2	-	-	V	①
PWM "L" Voltage (*2)	V _{PWML}	I _{OUT} =1.0mA (XC9105D)	-	-	V _{DD} -1.0	V	①
EXT "H" On Resistance	R _{EXTH}	Same as I _{DD1} , V _{EXT} =V _{OUT} -0.4V	-	24	36	Ω	④
EXT "L" On Resistance	R _{EXTL}	Same as I _{DD2} , V _{EXT} =0.4V	-	16	24	Ω	④
CE "High Current	I _{CEH}	Same as I _{DD2} , CE=V _{DD}	-	-	0.1	μA	⑤
CE "L" Current	I _{CEL}	Same as I _{DD2} , CE=0V	-	-	-0.1	μA	⑤
FB "H" Current	I _{FBH}	Same as I _{DD2} , FB=V _{DD}	-	-	0.1	μA	⑤
FB "L" Current	I _{FBL}	Same as I _{DD2} , FB =1V	-	-	-0.1	μA	⑤

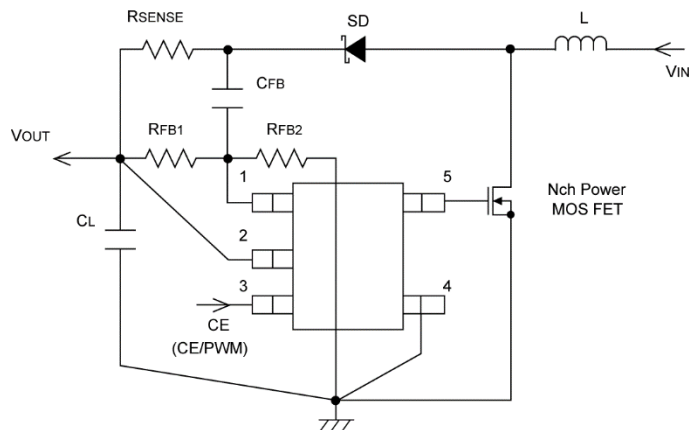
Test Conditions: Unless otherwise stated, C_L: ceramic, recommended MOSFET should be connected.
V_{OUT}=3.3V, V_{IN}=2.0V, I_{OUT}=170mA

NOTE:

*1 Although the IC starts step-up operations from a V_{DD} of 0.8V, the output voltage and oscillation frequency are stabilized at V_{DD}≥1.8V. Therefore, a V_{DD} of more than 1.8V is recommended when V_{DD} is supplied from V_{IN} or other power sources.

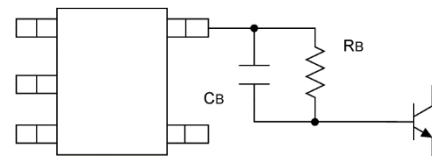
*2 With the XC9105 series, the CE pin also serves as a PWM/PFM switching pin. In operation, PWM control is selected when the voltage at the CE pin is more than V_{DD}-0.2V. On the other hand, PWM/PFM automatic switching control at a duty = 25% is selected when the voltage at the CE pin is less than V_{DD}-1.0V and more than V_{CEH}.

TYPICAL APPLICATION CIRCUIT



When obtaining V_{DD} from a source other than V_{OUT} , please insert a capacitor C_{DD} between the V_{DD} pin and the GND pin in order to provide stable operations.

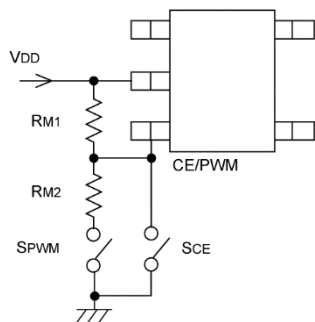
Please place C_L and C_{DD} as close as to the V_{OUT} and V_{DD} pins respectively and also close to the GND pin. Strengthen the wiring sufficiently. R_{SENSE} should be removed and shorted when the C_L capacitor except for ceramic or low ESR capacitor is used.



Insert R_B and C_B when using a bipolar NPN Transistor.

NOTES ON USE

<XC9105 CE/PWM PIN>



SCE	SPWM	CONDITIONS
ON	—	Chip Disable
OFF	ON	Duty=25%, PWM/PFM automatic switching
OFF	OFF	PWM

By using external signals, the control of the XC9105 series can be alternated between PWM control and PWM/PFM automatic switching control. By inputting a voltage of more than $V_{DD} - 0.2V$ to the CE/PWM pin, PWM control can be selected. On the other hand, PWM/PFM automatic switching control can be selected by inputting a voltage of less than $V_{DD} - 1.0V$.

With the XC9105, by connecting resistors of the same value (R_{M1} , R_{M2}) as shown in the diagram to the left, it is possible to obtain chip disable with SCE ON and, SPWM ON or OFF, PWM/PFM auto switching at Duty=25% with SCE OFF & SPWM ON, & PFM control with both switches OFF.

Note:

When operating at $V_{DD} - 1.8V$ and below (stepping-up from $V_{IN}=0.9V$), it is necessary to pull-up to V_{DD} in order to allow the CE/PWM pin reach the V_{CEH} voltage level. Please make sure that the IC is in PWM control (SPWM=OFF) when operations start. If SPWM is ON, there are times when chip enable might not operate.

* Please select your external components carefully.

■ OPERATIONAL EXPLANATION

The XC9103/04/05 series are step-up DC/DC converter controller ICs with built-in high speed, low ON resistance drivers.

<Error Amp.>

Error amplifier is designed to monitor the output voltage, comparing the feedback voltage (FB) with the reference voltage Vref. In response to feedback of a voltage lower than the reference voltage Vref, the output voltage of the error amp. decreases.

<OSC Generator>

This circuit generates the internal reference clock.

<Ramp Wave Generator>

The ramp wave generator generates a saw-tooth waveform based on outputs from the OSC Generator.

<PWM Comparator>

The PWM comparator compares outputs from the error amp. and saw-tooth waveform. When the voltage from the Error Amp's output is low, the external switch will be set to ON.

<PWM/PFM Controller>

This circuit generates PFM pulses.

The PWM/PFM automatic switching mode switches between PWM and PFM automatically depending on the load. The PWM/PFM automatic switching mode is selected when the voltage of the CE pin is less than $V_{DD} - 1.0V$, and the control switches between PWM and PFM automatically depending on the load. PWM/PFM control turns into PFM control when threshold voltage becomes lower than voltage of error amps. PWM control mode is selected when the voltage of the CE pin is more than $V_{DD} - 0.2V$. Noise is easily reduced with PWM control since the switching frequency is fixed. The series is suitable for noise sensitive portable audio equipment as PWM control can suppress noise during operation and PWM/PFM switching control can reduce consumption current during light load in stand-by.

<Vref 1 with Soft Start>

The reference voltage, Vref (FB pin voltage)=0.9V, is adjusted and fixed by laser trimming (for output voltage settings, please refer to the notes on next page). To protect against inrush current, when the power is switched on, and also to protect against voltage overshoot, soft-start time is set internally to 10ms. It should be noted, however, that this circuit does not protect the load capacitor (CL) from inrush current. With the Vref voltage limited and depending upon the input to the error amps, the operation maintains a balance between the two inputs of the error amps and controls the EXT pin's ON time so that it doesn't increase more than is necessary.

<Enable Function>

This function controls the operation and shutdown of the IC. When the voltage of the CE pin is 0.2V or less, the mode will be disable, the channel's operations will stop and the EXT1 pin will be kept at a low level (the external N-type MOSFET will be OFF). When the IC is in a state of disable, current consumption will be no more than $1.0 \mu A$.

When the CE pin's voltage is 0.65V or more, the mode will be enabled and operations will recommence.

■ OPERATIONAL EXPLANATION (Continued)

① Output Voltage Setting

Output voltage can be set by adding external split resistors. Output voltage is determined by the following equation, based on the values of RFB1 and RFB2. The sum of RFB1 and RFB2 should normally be 2MΩ or less.

$$V_{OUT} = 0.9 \times (R_{FB1} + R_{FB2}) / R_{FB2}$$

The value of CFB, speed-up capacitor for phase compensation, should result in $f_{zfb} = 1/(2\pi \times C_{FB} \times R_{FB1})$ equal to 5 to 30kHz. Adjustments are required depending on the application, value of inductance (L), and value of load capacity (CL).

fzfb = 30kHz (L=10 μH)	[Example of Equation]
fzfb = 20kHz (L=22 μH)	RFB1 : 120kΩ RFB2 : 45kΩ
fzfb = 10kHz (L=47 μH)	CFB : 47pF (fzfb = 30kHz, L = 10 μH)
	68pF (fzfb = 20kHz, L = 22 μH)
	130pF (fzfb = 10kHz, L = 47 μH)

② The use of ceramic capacitor CL

The circuit of the XC9103/04/05 series is organized by a specialized circuit, which reenacts negative feedback of both voltage and current. Also by insertion of approximately 100mΩ of a low and inexpensive sense resistor as current sense, a high degree of stability is possible even using a ceramic capacitor, a condition which used to be difficult to achieve. Compared to a tantalum condenser, because the series can be operated in a very small capacity, it is suited to use of the ceramic capacitor, which is cheap and small.

③ External Components

Tr	:*When a MOSFET is used: XP161A1355PR (N-ch Power MOSFET, TOREX) Note*: As the breakdown voltage of XP161A1355 is 8V, take care with the power supply voltage. With output voltages over 6V, use the XP161A1265 with a breakdown voltage of 12V. VST1: XP161A1355PR = 1.2V (MAX.) XP161A1265PR = 1.5V (MAX.)	*When a NPN Tr. Is used: 2SD1628 (ON Semiconductor) RB : 500Ω (Adjust with Tr's HFE or load) CB : 2200pF (Ceramic type set so that RB and pole is less than 70% of fosc) $CB \leq 1 / (2\pi \times RB \times f_{osc} \times 0.7)$
SD	:MA2Q737 (Schottky type, Panasonic)	
L, CL	:When Using Ceramic Type	

Ceramic Type

L	:22 μH (CDRH5D28, SUMIDA, fosc = 100, 180kHz) 10 μH (CDRH5D18, SUMIDA, fosc = 300, 500kHz)
CL	:10V 10 μF (Ceramic Type, LMK325BJ106ML, TAIYO YUDEN) Use the formula below when step-up ratio and output current is large. $C_L = (C_L \text{ standard value}) \times (I_{OUT}(mA) / 300mA \times V_{OUT} / V_{IN})$
RSense	:100mΩ (fosc = 180, 300, 500kHz) 50mΩ (fosc = 100kHz)

Tantalum Type

L	:22 μH (CDRH5D28, SUMIDA, fosc = 300kHz) 47 μH (CDRH5D28, SUMIDA, fosc = 100, 180kHz) Except when $I_{OUT}(mA) / 100mA \times V_{OUT} / V_{IN} > 2 \rightarrow 22 \mu H$ 10 μH (CDRH5D18, SUMIDA, fosc = 500kHz)
CL	:16V, 47 μF (Tantalum Type 16MCE476MD2, NICHICHEMI) Use the formula below when step-up ratio and output current is large. $C_L = (C_L \text{ standard value}) \times (I_{OUT}(mA) / 300mA \times V_{OUT} / V_{IN})$
RSense	:Not required, but short out the wire.

AL Electrolytic Type

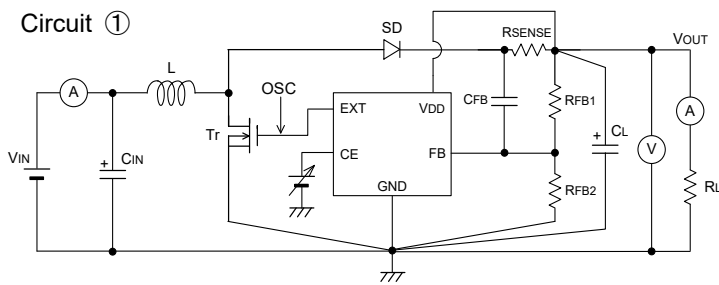
L	:22 μH (CDRH5D28 SUMIDA, fosc = 300kHz) 47 μH (CDRH5D28 SUMIDA, fosc = 100, 180kHz) Except when $I_{OUT}(mA) / 100mA \times V_{OUT} / V_{IN} > 2 \rightarrow 22 \mu H$
CL	:16V, 100 μF (AL Electrolytic Type) + 10V, 2.2 μF (Ceramic Type) Strengthen appropriately when step-up ratio and output current is large.
RSense	:Not required, but short out the wire.

④ For temporary, transitional voltage drop or voltage rising phenomenon, the IC is liable to malfunction should the ratings be exceeded.

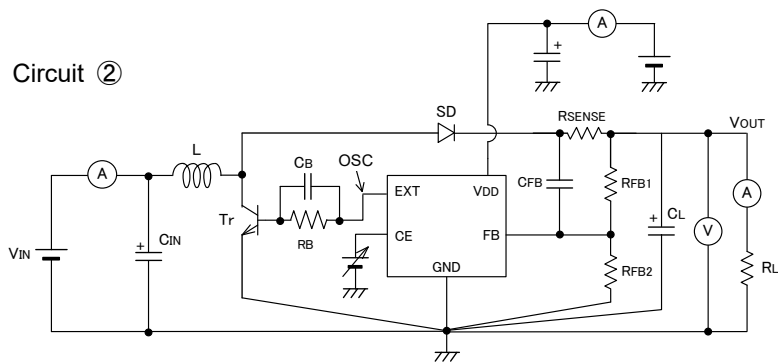
⑤ Torex places an importance on improving our products and their reliability. We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

■ TEST CIRCUITS

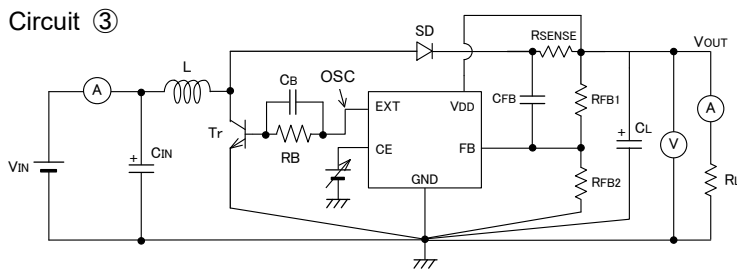
Circuit ①



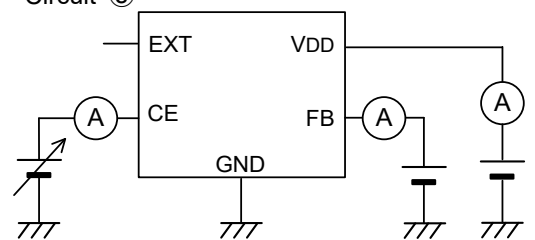
Circuit ②



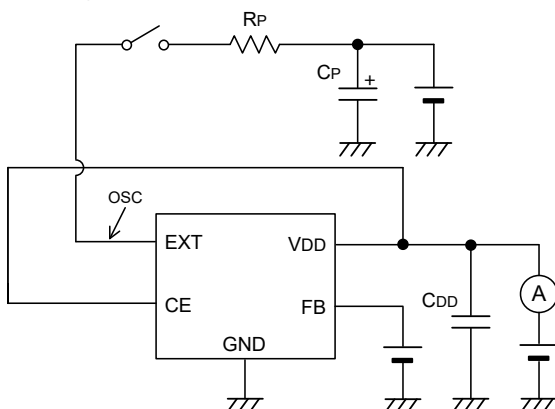
Circuit ③



Circuit ⑤

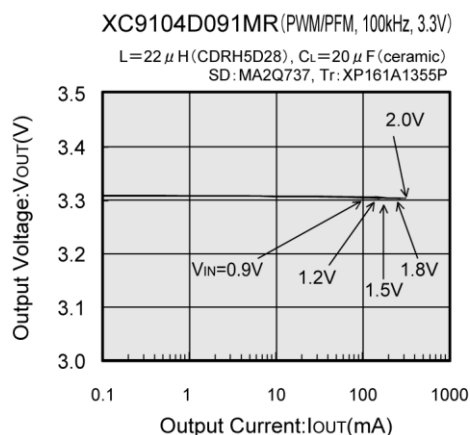
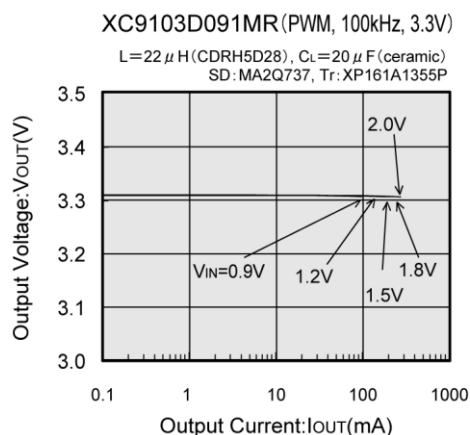
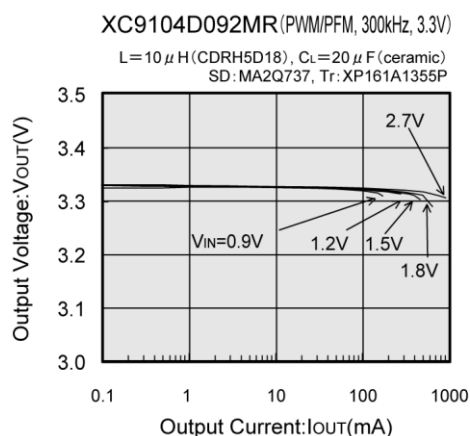
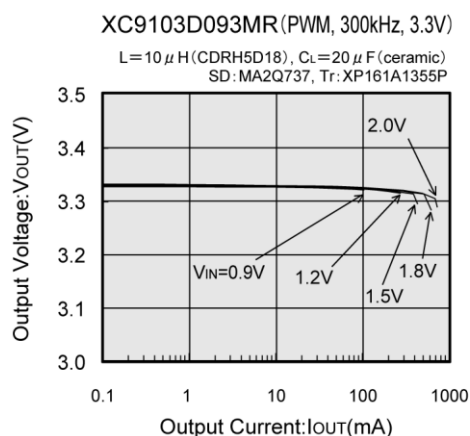
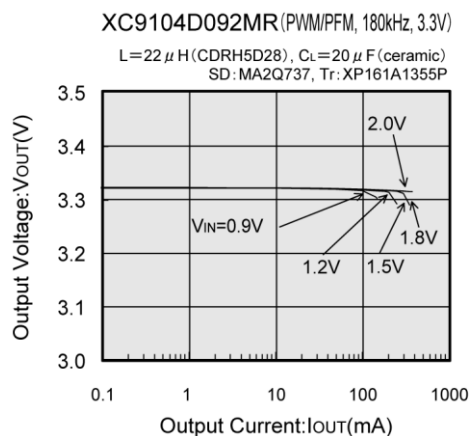
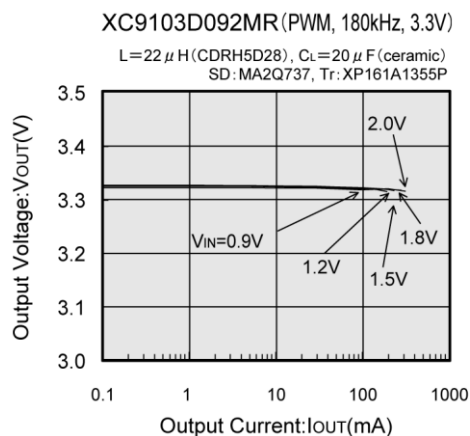


Circuit ④



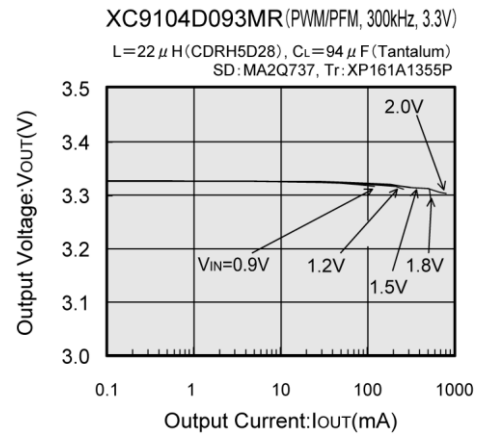
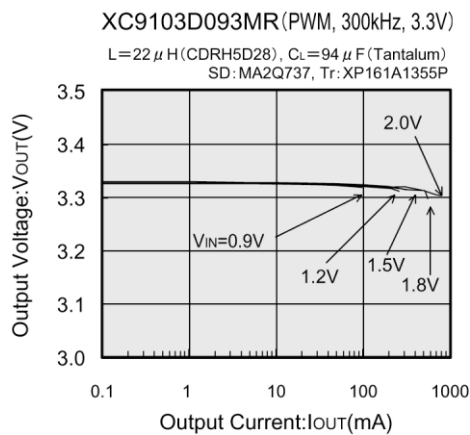
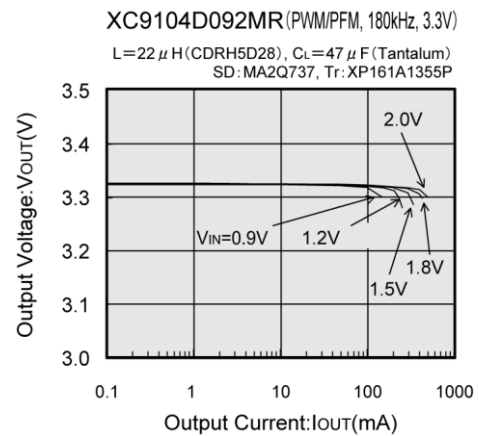
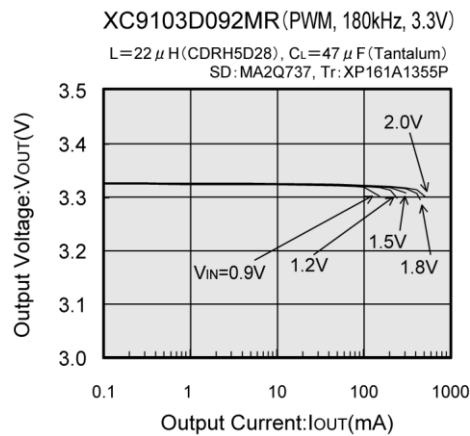
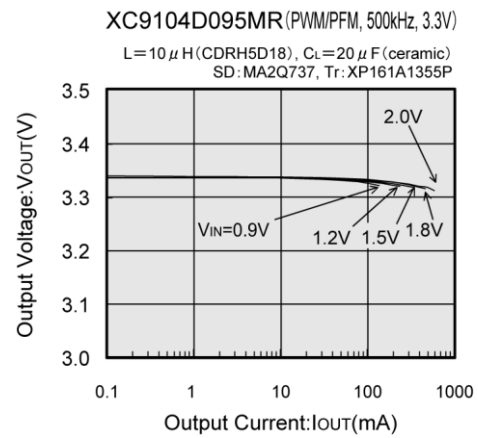
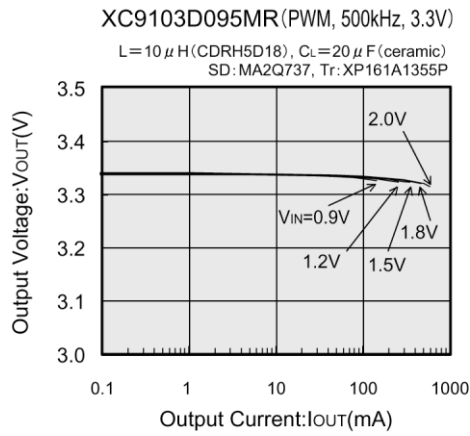
TYPICAL PERFORMANCE CHARACTERISTICS

(1) Output Voltage vs. Output Current



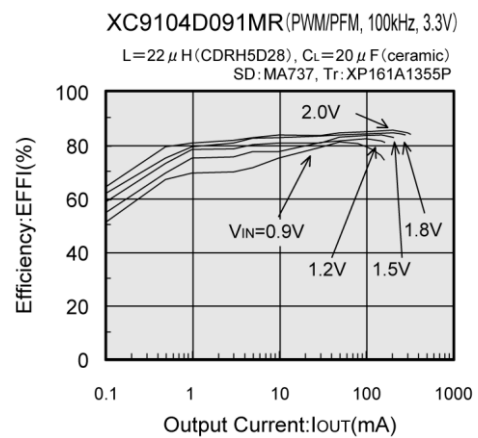
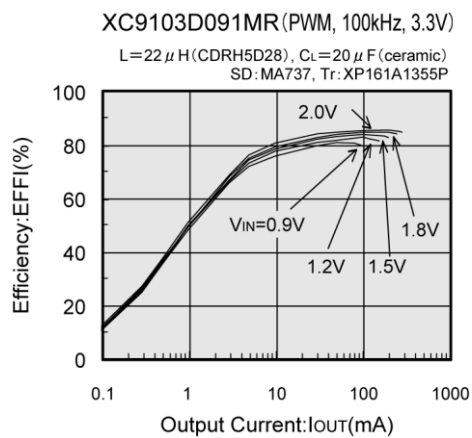
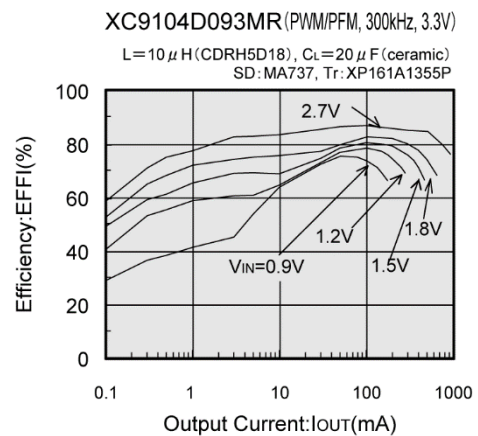
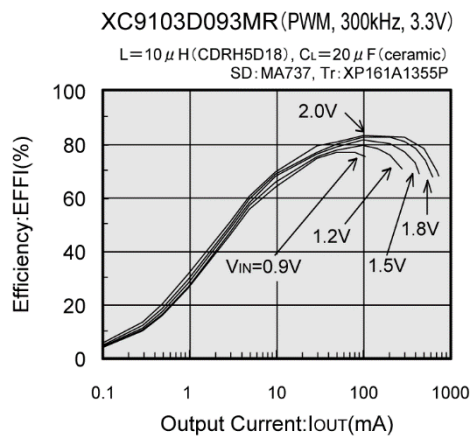
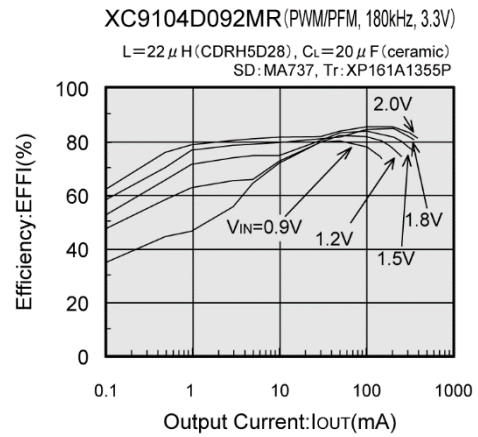
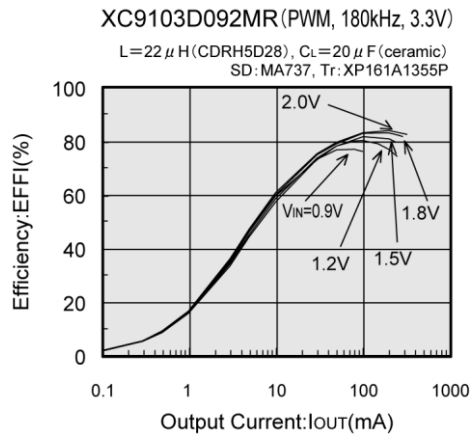
TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(1) Output Voltage vs. Output Current (Continued)



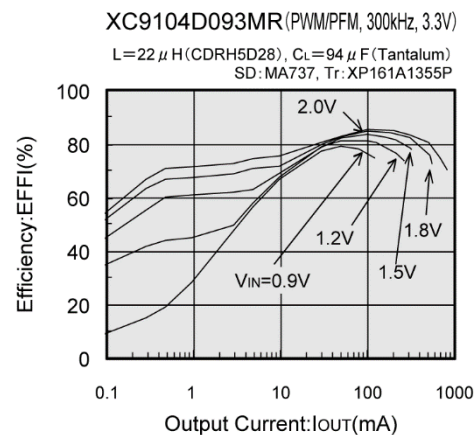
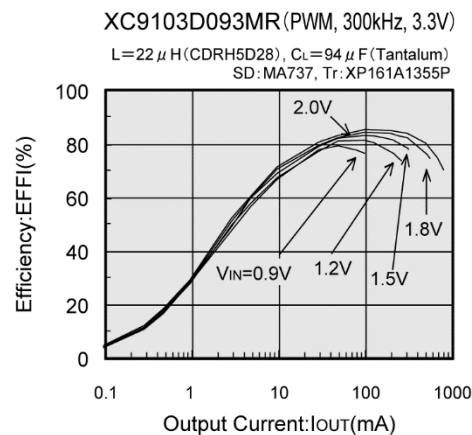
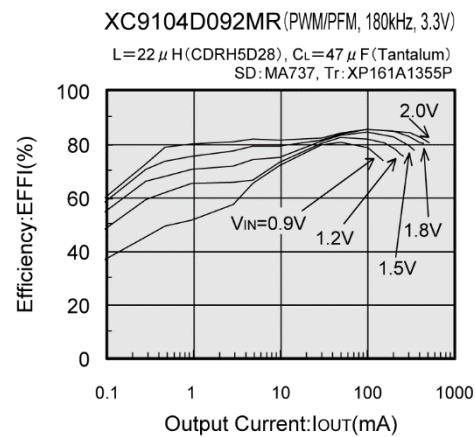
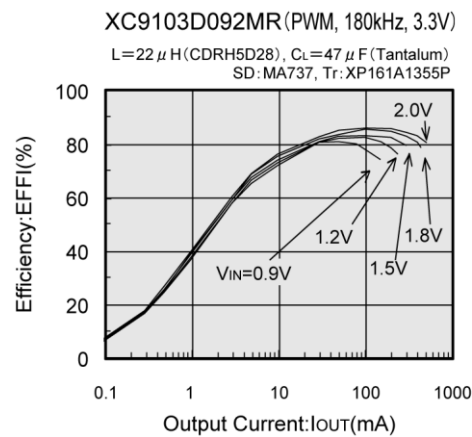
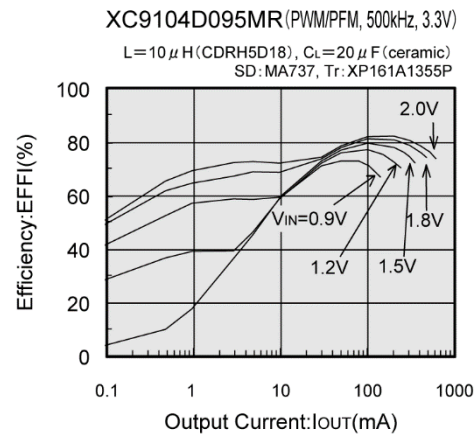
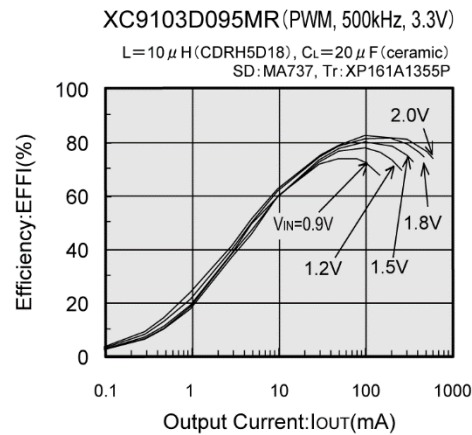
TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(2) Efficiency vs. Output Current (Continued)



■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

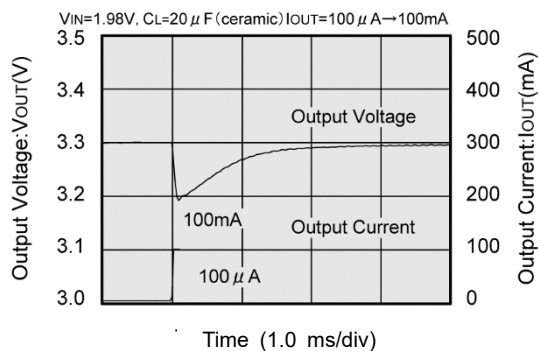
(2) Efficiency vs. Output Current (Continued)



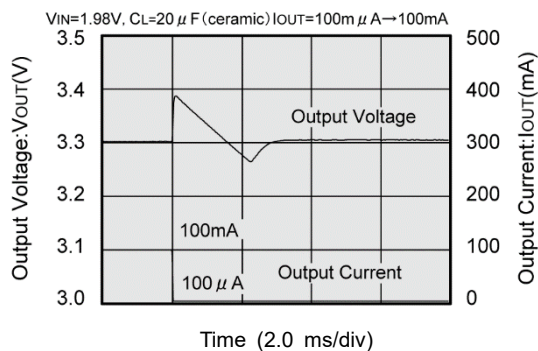
TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(3) Load Transient Response

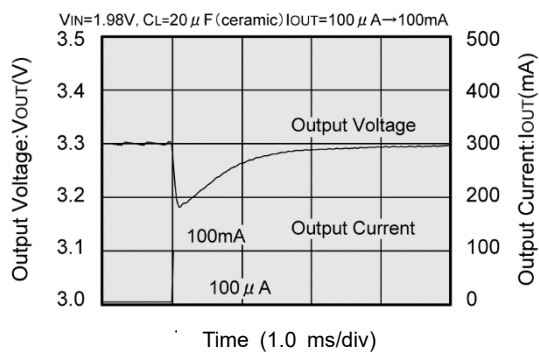
XC9103D092MR (PWM, 180kHz, 3.3V)



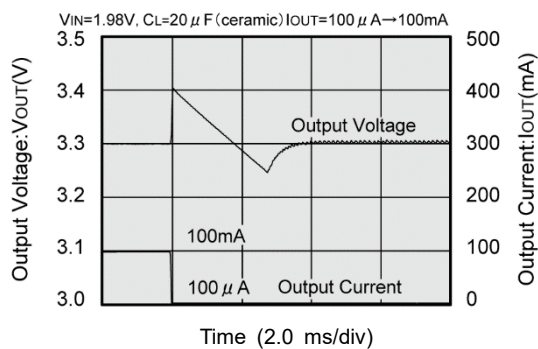
XC9103D092MR (PWM, 180kHz, 3.3V)



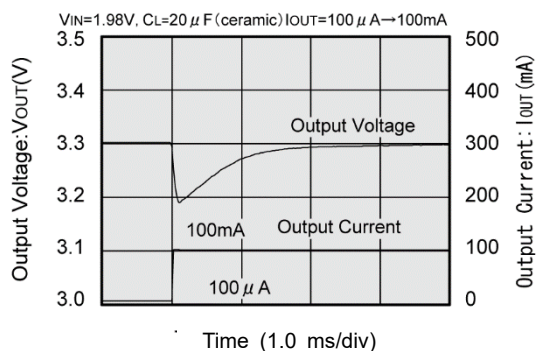
XC9104D092MR (PWM/PFM, 180kHz, 3.3V)



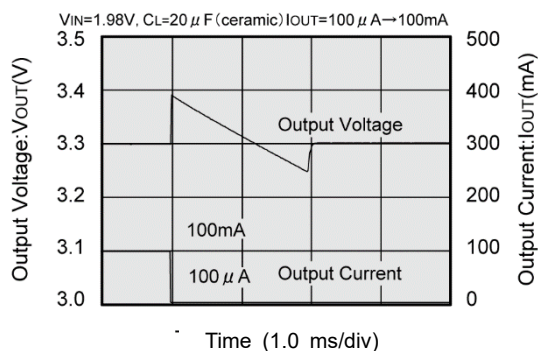
XC9104D092MR (PWM/PFM, 180kHz, 3.3V)



XC9103D093MR (PWM, 300kHz, 3.3V)

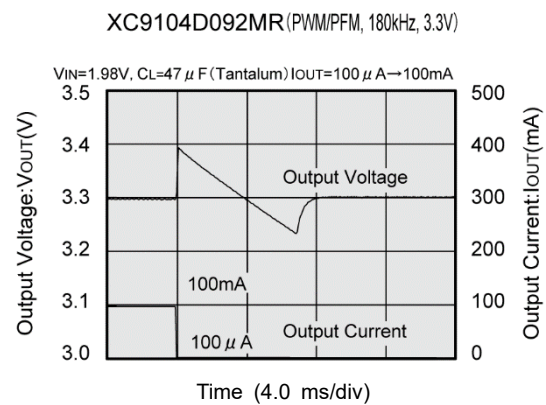
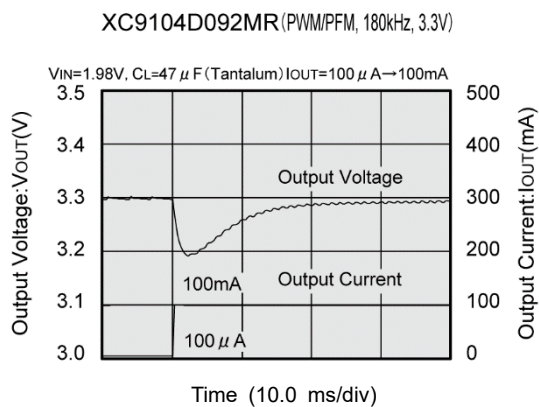
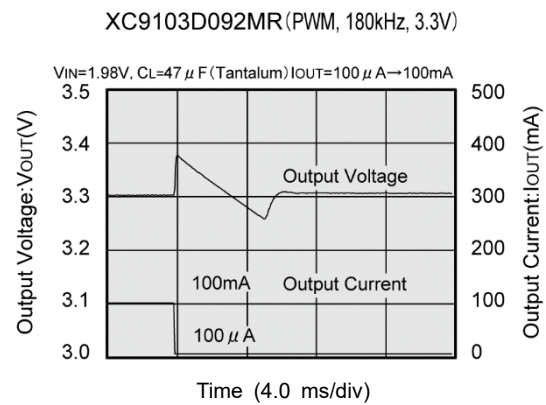
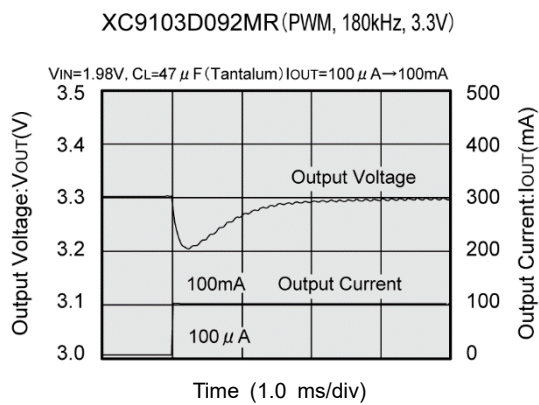
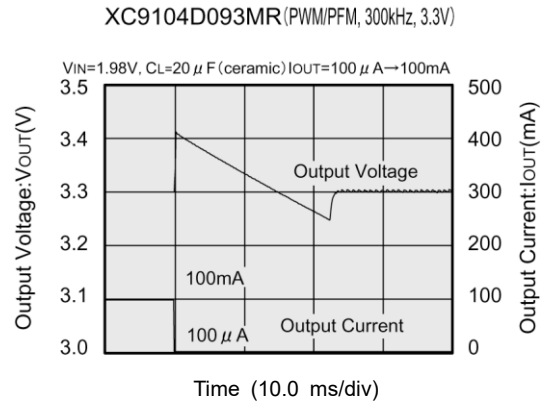
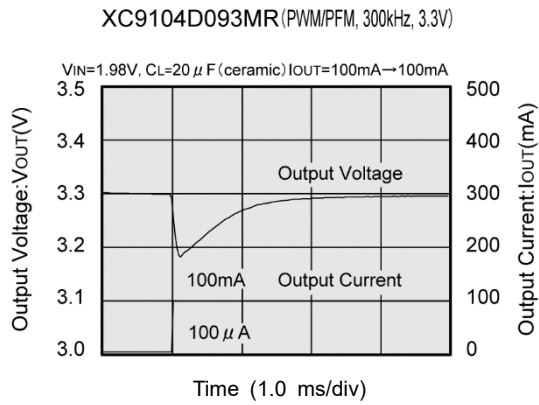


XC9103D093MR (PWM, 300kHz, 3.3V)



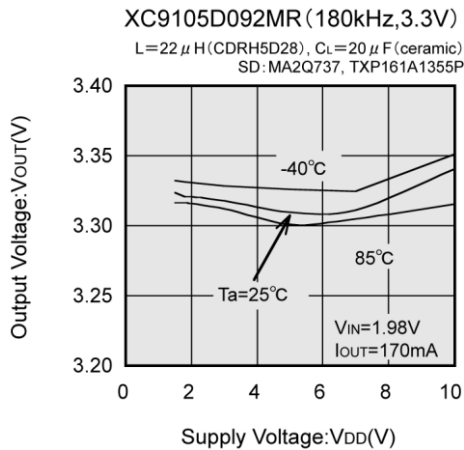
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(3) Load Transient Response (Continued)

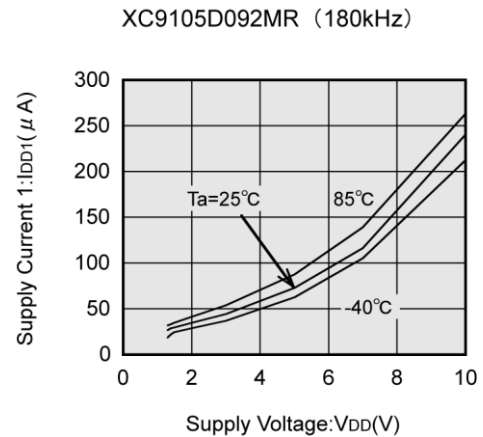


TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

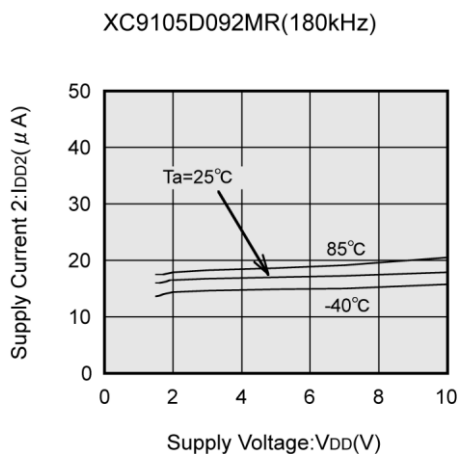
(4) Output Voltage vs. Power Supply Voltage



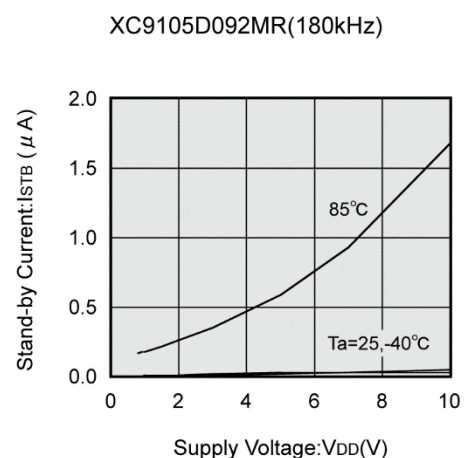
(5) Supply Current 1 vs. Power Supply Voltage



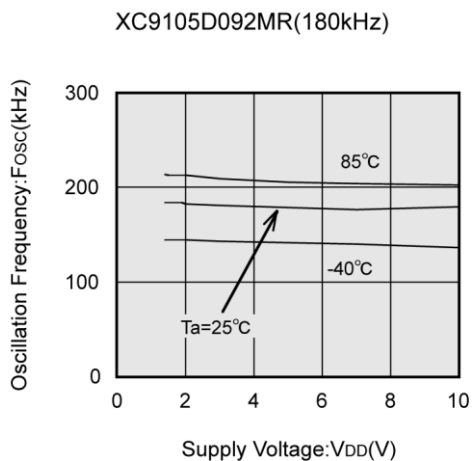
(6) Supply Current 2 vs. Power Supply Voltage



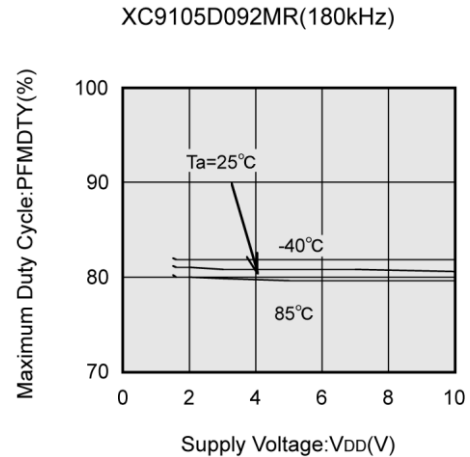
(7) Stand-By Current vs. Power Supply Voltage



(8) Oscillation Frequency vs. Power Supply Voltage

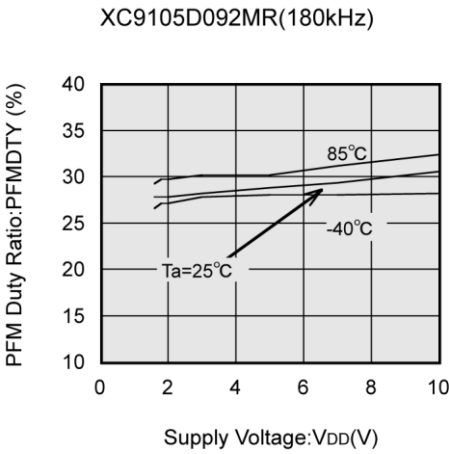


(9) Maximum Duty Ratio vs. Power Supply Voltage

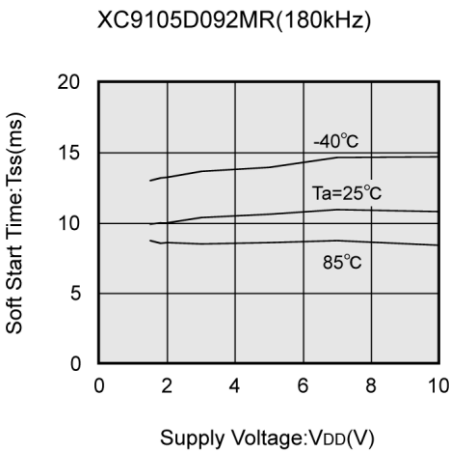


■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

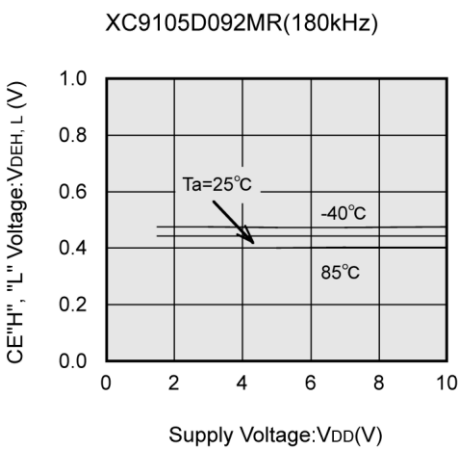
(10) PFM Duty Ratio vs. Power Supply Voltage



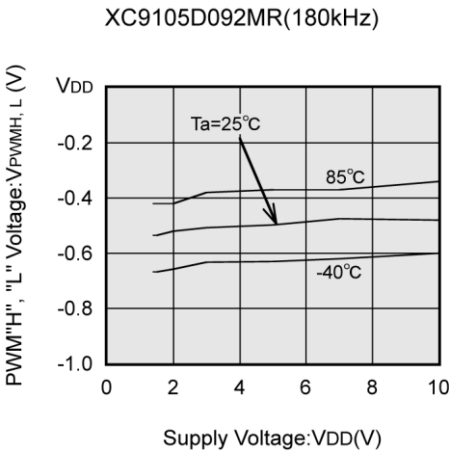
(11) Soft Start Time vs. Power Supply Voltage



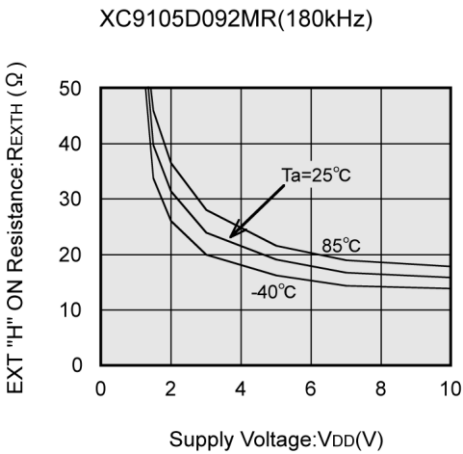
(12) CE "H" "L" Voltage vs. Power Supply Voltage



(13) PWM "H" "L" Voltage vs. Power Supply Voltage

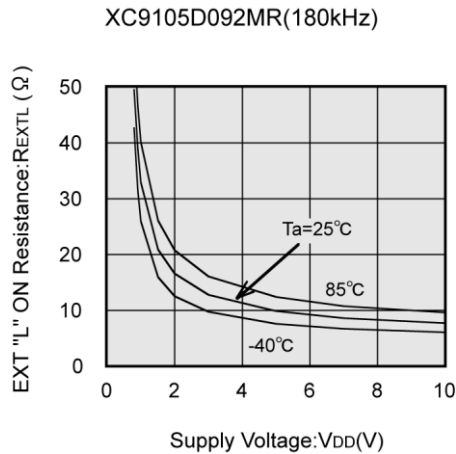


(14) EXT "H" On Resistance vs. Power Supply Voltage

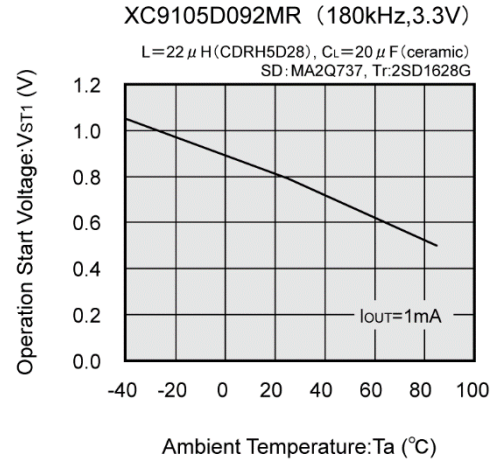


TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

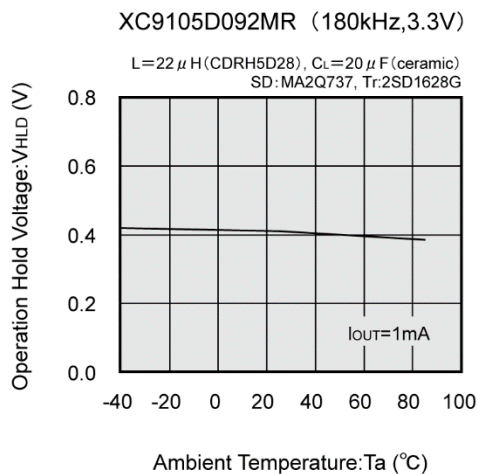
(15) EXT "L" On Resistance vs. Power Supply Voltage



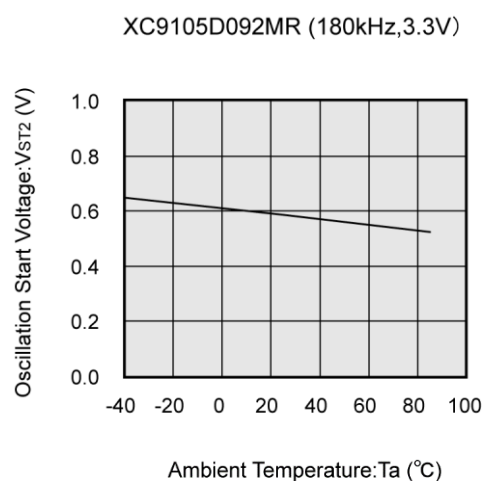
(16) Operation Start Voltage vs. Ambient Temperature



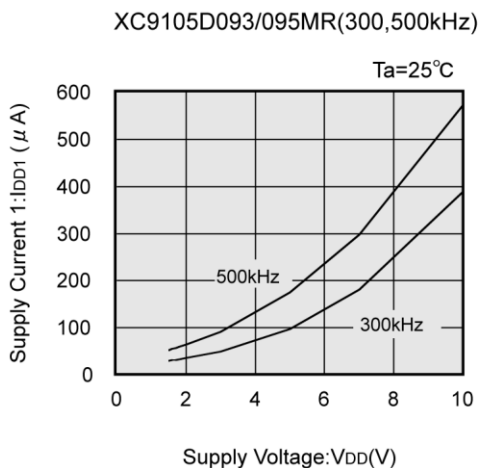
(17) Operation Hold Voltage vs. Ambient Temperature



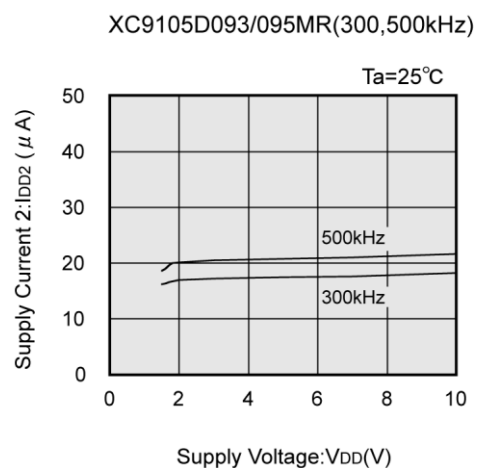
(18) Oscillation Start Voltage vs. Ambient Temperature



(19) Supply Current 1 vs. Power Supply Voltage

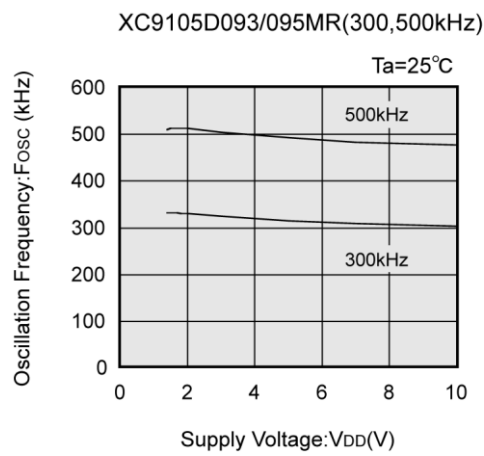


(20) Supply Current 2 vs. Power Supply Voltage

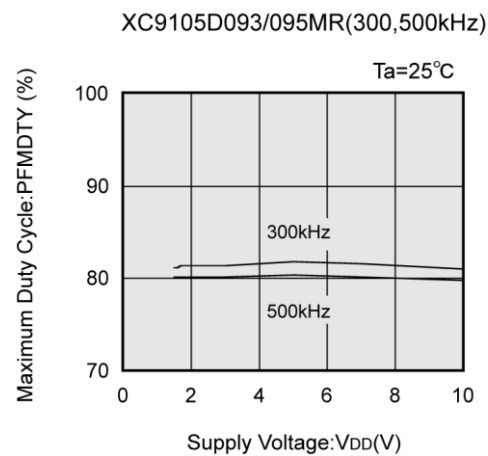


■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

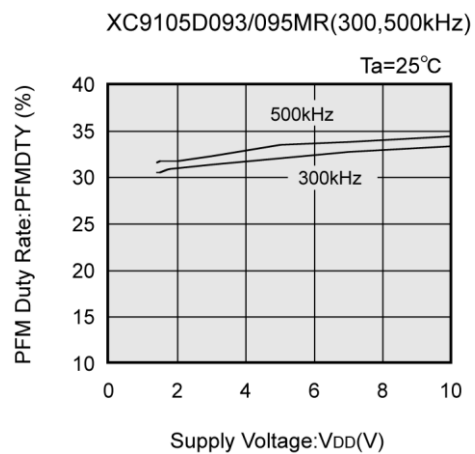
(21) Oscillation Frequency vs. Power Supply Voltage



(22) Maximum Duty Cycle vs. Power Supply Voltage



(23) PFM Duty Ratio vs. Power Supply Voltage



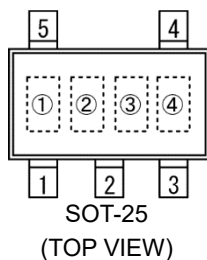
■ PACKAGING INFORMATION

For the latest package information go to, www.torexsemi.com/technical-support/packages

PACKAGE	OUTLINE / LAND PATTERN	THERMAL CHARACTERISTICS
SOT-25	SOT-25 PKG	SOT-25 Power Dissipation
USP-6B	USP-6B PKG	USP-6B Power Dissipation

■ MARKING RULE

● SOT-25



① represents product series

MARK	PRODUCT SERIES
3	XC9103x09xMx
4	XC9104x09xMx
5	XC9105x09xMx

② represents current limit function

MARK	FUNCTIONS	PRODUCT SERIES
D	Without current limit function	XC9103/9104/9105D09xMx

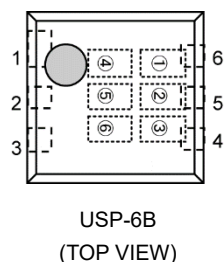
③ represents oscillation frequency

MARK	OSCILLATION FREQUENCY	PRODUCT SERIES
1	100	XC9103/9104/9105x091Mx
2	180	XC9103/9104/9105x092Mx
3	300	XC9103/9104/9105x093Mx
5	500	XC9103/9104/9105x095Mx

④ represents production lot number

0 to 9 and A to Z, reversed character of 0 to 9 and A to Z repeated.
(G, I, J, O, Q, W excluded)

● USP-6B



① represents product series

MARK	PRODUCT SERIES
6	XC9103x09xDx
Y	XC9104x09xDx
9	XC9105x09xDx

② represents current limit function

MARK	FUNCTIONS	PRODUCT SERIES
D	Without current limit function	XC9103/9104/9105D09xDx

③④ represents FB voltage value

MARK		FB VOLTAGE	PRODUCT SERIES
③	④		
0	9	09	XC9103/9104/9105x09xDx

⑤ represents oscillation frequency

MARK	OSCILLATION FREQUENCY	PRODUCT SERIES
1	100	XC9103/9104/9105x091Dx
2	180	XC9103/9104/9105x092Dx
3	300	XC9103/9104/9105x093Dx
5	500	XC9103/9104/9105x095Dx

⑥□ represents production lot number

0 to 9 and A to Z repeated. (G, I, J, O, Q, W excluded)

Note: No character inversion used.

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