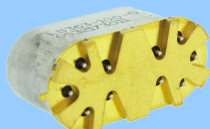


LoopBack Relay

GLB363 Series

*With Built-in AC Bypass Capacitors /
DC LoopBack Relay*



SERIES DESIGNATION	RELAY TYPE
GLB363	LoopBack Relay, Sensitive Coil, Surface Mount Ground Shield and Stub pins with AC Bypass Capacitors or No capacitor

DESCRIPTION

The LoopBack Series relay combines two DPDT electromechanical relays in one package that includes an internal bypass path for Automated Test Equipment (ATE) applications. The LoopBack combines the technology of two Teledyne RF/GRF303 Series relays which eliminates the need for external PCB traces in loop back test applications. This innovation results in superior signal integrity and RF performance, while taking minimal board space. The GLB363 Series is designed for digital signaling application and provides excellent signal integrity up to 16 Gbps data rates.

The internal in-line capacitors allow you to eliminate the footprints for external components, while performing AC bypass functions internal to the relay. The capacitors used feature excellent signal integrity as well as low loss high frequency performance.

A typical loop-back load board application uses the Device Under Test (DUT) to test itself. In this method, the transmitter from the DUT is connected through a loop back path to the receiver of the DUT. The double

pole design of the LoopBack relay is perfectly suited for differential signaling, allowing the relay to provide transmit and receive signals and their inversions to the DUT or through the bypass path.

The internal structure of the LoopBack relay reduces the number of discontinuities and shortens the signal path during loop back testing, providing lower insertion loss and higher signal integrity performance than two SRF303 Series relays. In the normally closed mode (de-energized) the LoopBack relay provides four normally closed contacts. When energized, the moving contacts are connected together across the loop back structure to provide two through paths. The normally closed contacts have similar performance to SGRF303 Series relays.

This LoopBack Relay is available with a surface mount ground shield and J-lead configurations to provide improved high data rate and high frequency performance and ease of surface mount attachment.

The DC LoopBack configuration provides internally shorted paths to allow "auxiliary" full spectrum signal routing.

ENVIRONMENTAL AND PHYSICAL SPECIFICATIONS		
Temperature (Ambient)	Storage	-65°C to +125°C
	Operating	-55°C to +85°C
Vibration (Note 1)		10 g's to 500 Hz
Shock (Note 1)		30 g's, 6ms half sine
Enclosure		Hermetically sealed
Weight	GLB363	0.18 oz. (5.10g) max.

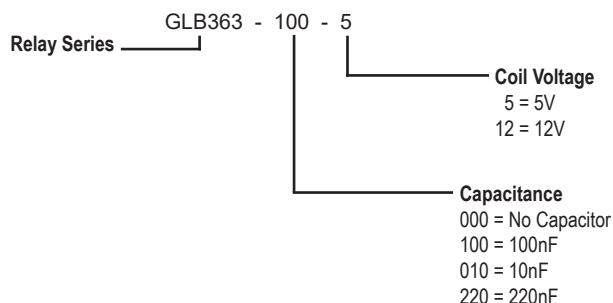
GENERAL ELECTRICAL SPECIFICATIONS (-65°C to +125°C unless otherwise noted)(Notes 2 & 3)

Contact Arrangement	Special (See Schematic on page 5)
Rated Duty	Continuous
Contact Resistance	0.200 Ω max. initial (measured 1/8" from the header)
Contact Load Rating	Resistive: 1Amp/28Vdc Low level: 10 to 50 μ A, 10 to 50 mV
Contact Life Ratings	5,000,000 cycles (typical) at low level
Operate Time	4.0 ms max.
Release Time	3.0 ms max.
Insulation Resistance	1,000 M Ω min. between mutually isolated terminals
Dielectric Strength	350 Vrms (60 Hz) @ atmospheric pressure

DETAILED ELECTRICAL SPECIFICATIONS (-65°C to +125°C unless otherwise noted)(Note 3)

BASE PART NUMBERS	GLB363-x-5	GLB363-x-12
Coil Voltage, Nominal (Vdc)	5.0	12.0
Coil Resistance (Ohms $\pm$20%)	56	400
Pick-up Voltage (Vdc max.)	3.6	9.0
Coil Operating Power (mW)	450	360

Part Numbering System (Note 4)



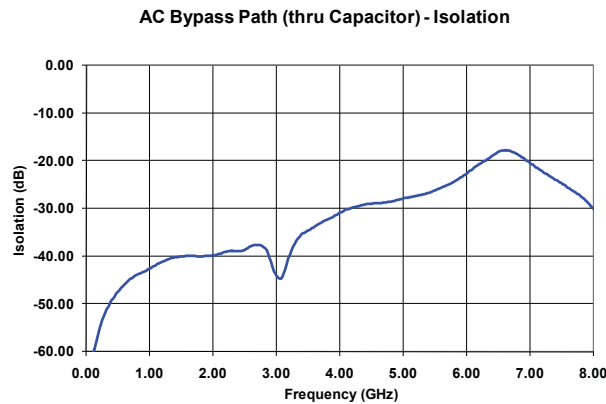
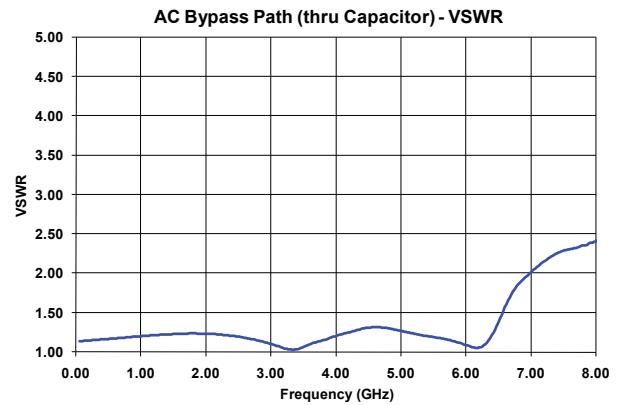
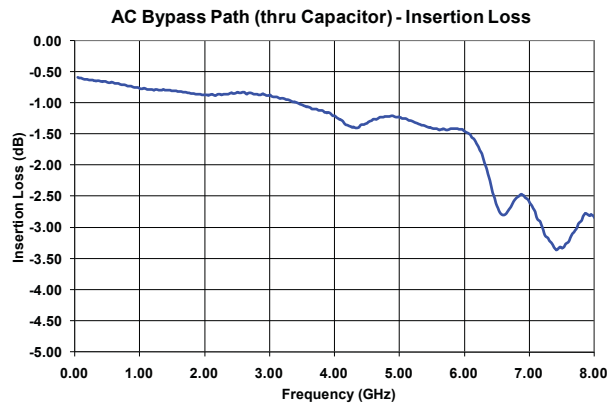
NOTES

1. Relays will exhibit no contact chatter in excess of 10 μ s or transfer in excess of 1 μ s.
2. "Typical" characteristics are based on available data and are best estimates. No on-going verification tests are performed.
3. Unless otherwise specified, parameters are initial values.
4. Unless otherwise specified, relays will be supplied with gold-plated leads.

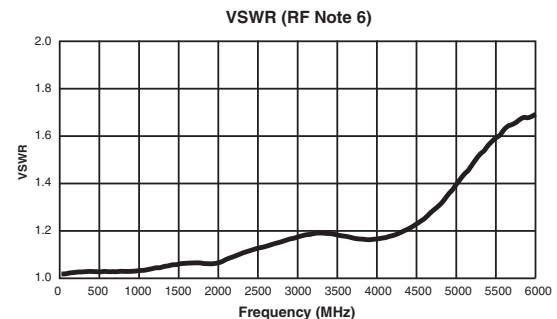
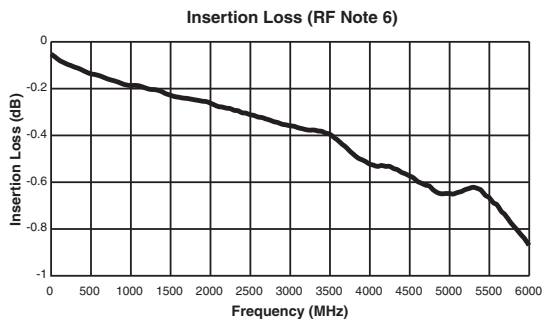
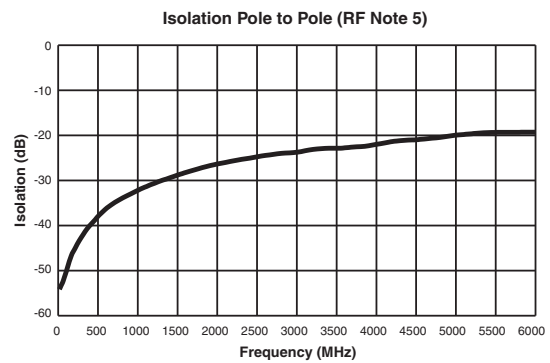
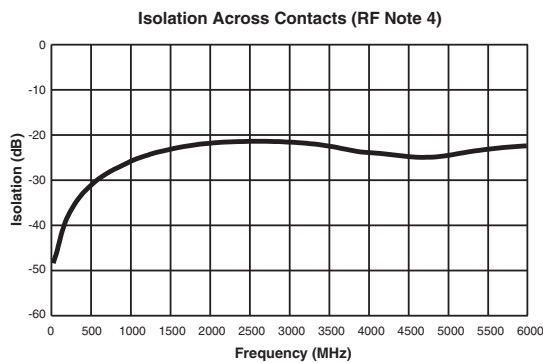
RF NOTES (for following page)

1. Test conditions:
 - a. Fixture: .031" copper clad, reinforced PTFE, RT/duroid® 6002 with SMA connectors. (RT/duroid® is a registered trademark of Rogers Corporation.)
 - b. Room ambient temperature.
 - c. Terminals not tested were terminated with 50-ohm load.
 - d. Contact signal level: -10 dBm.
 - e. No. of test samples: 4.
2. Data presented herein represents typical characteristics and is not intended for use as specification limits.
3. Data is per pole, except for pole-to-pole data.
4. Data is the average from readings taken on all open contacts.
5. Data is the average from readings taken on poles with coil energized and de-energized.
6. Data is the average from readings taken on all closed contacts.
7. Test fixture effect de-embedded from frequency and time response data.

TYPICAL RF Characteristics (See RF Notes on next page)

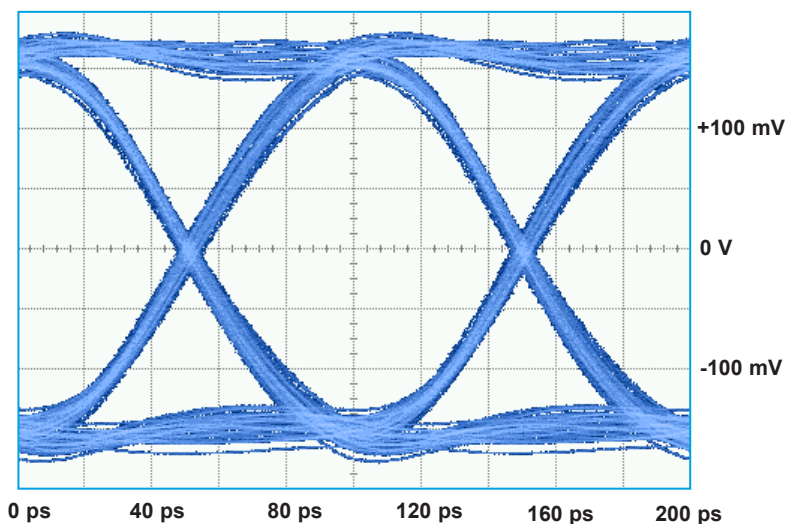


Normally Closed RF Performance



TYPICAL Single-Ended Signal Integrity Characteristics @ 10 Gbps

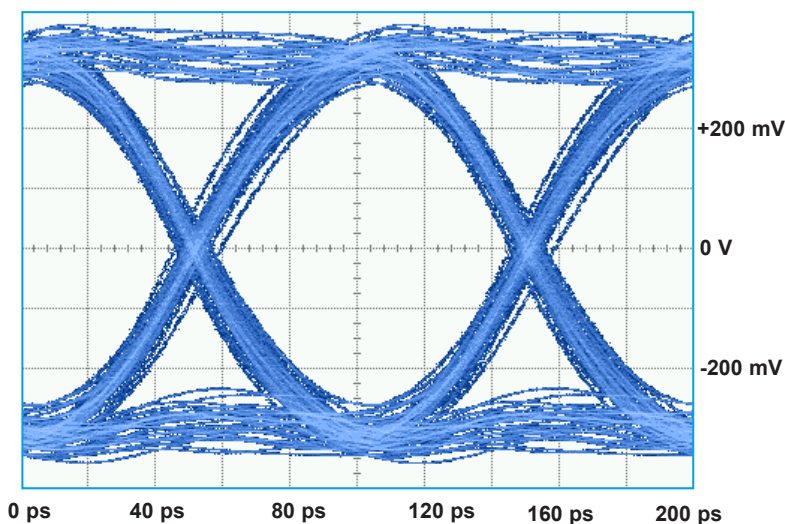
AC Bypass Path



Bit Rate	Eye Height	Eye Width	Jitter _{P,P}
10 Gbps	254.3 mV	88.64 ps	8.89 ps

TYPICAL Differential Signal Integrity Characteristics @ 10 Gbps

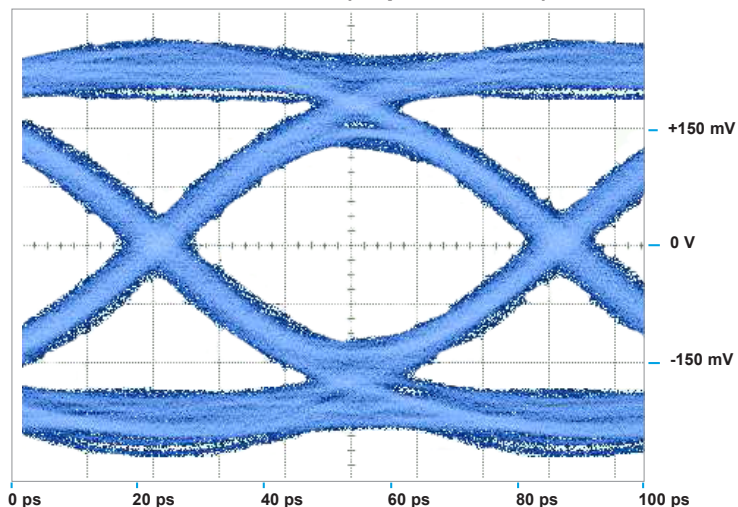
AC Bypass Path



Bit Rate	Eye Height	Eye Width	Jitter _{P,P}
10 Gbps	492.1 mV	84.29 ps	15.55 ps

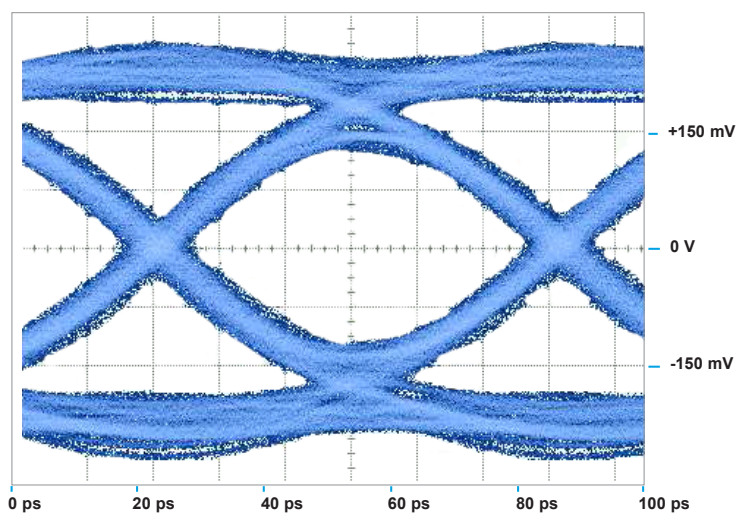
TYPICAL Single-Ended Signal Integrity Characteristics @ 16 Gbps

AC BYPASS PATH (Capacitor Path)



Bit Rate	Eye Height	Eye Width	Jitter _{P-P}
16 Gbps	173 mV	48.3 ps	12.67 ps

Normally Closed Path (Through Path)

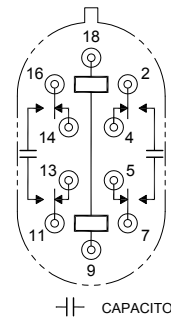
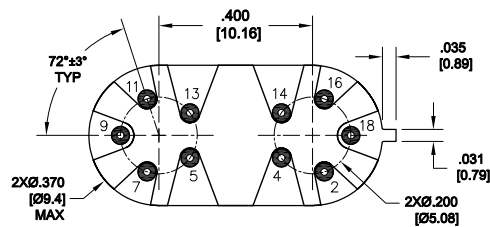
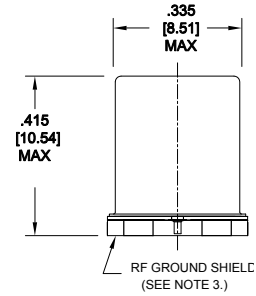
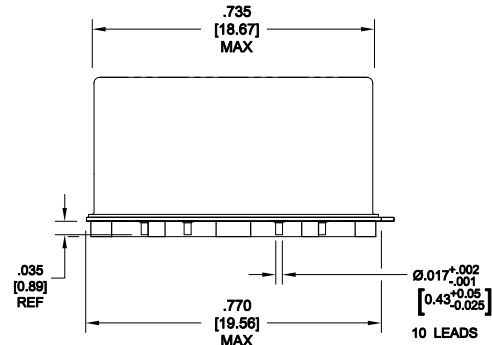


Bit Rate	Eye Height	Eye Width	Jitter _{P-P}
16 Gbps	173 mV	48.3 ps	12.67 ps

PATTERN GENERATOR SETTINGS

- 16 Gbps Random Pulse Pattern Generator
- $2^{31} - 1$ PRBS signal
- PRBS output of 500 mV_{P-P} (nominal)
- RF PCB effect (negligible) not removed from measurement
- Data shown is typical of both poles

OUTLINE DIMENSIONS

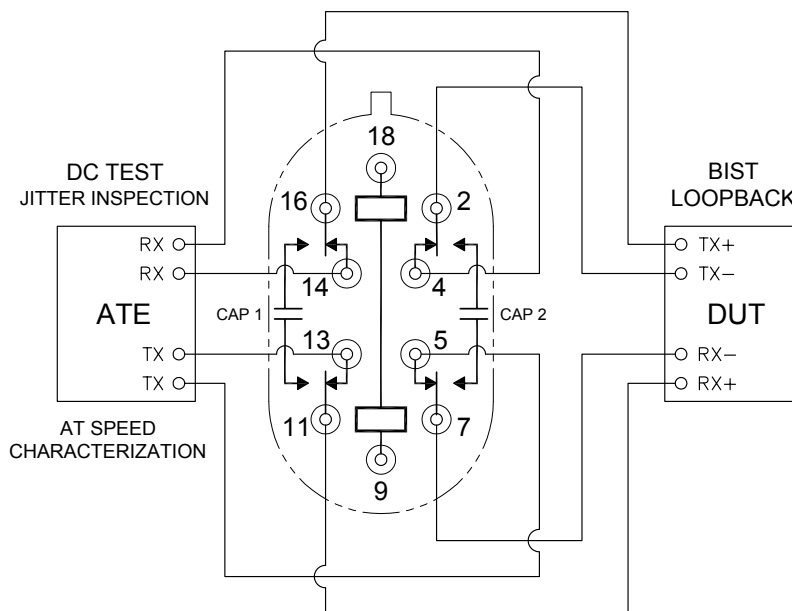


SCHEMATIC - TERMINAL VIEW
PIN NUMBERS ARE FOR REFERENCE
ONLY, NOT MARKED ON RELAY

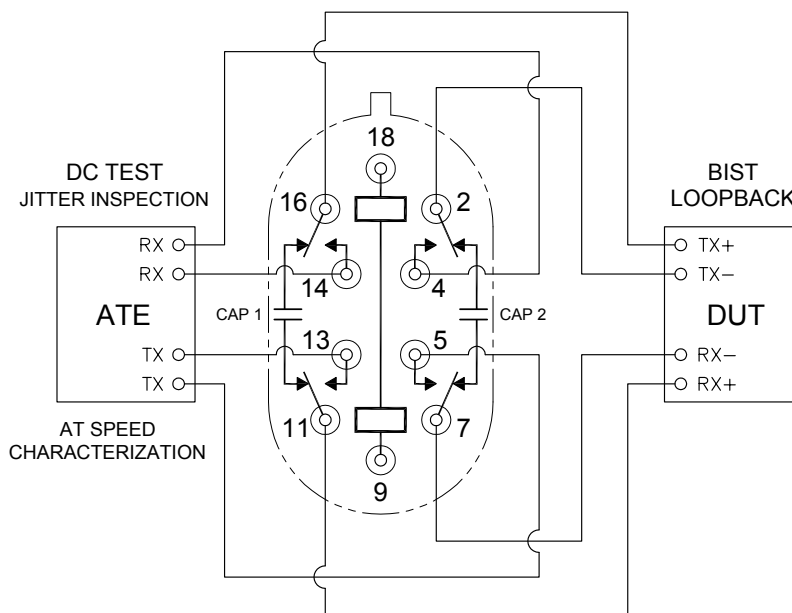
NOTES:

1. DIMENSIONS ARE IN INCHES. METRIC EQUIVALENTS (MILLIMETERS) ARE SHOWN IN [X.XX].
2. TOLERANCE (UNLESS OTHERWISE SPECIFIED): ±.010 INCH [±0.025 mm]
3. FOR BEST RF PERFORMANCE, SOLDER BOTTOM OF RF GROUND SHIELD TO RF GROUND PLANE OF PC BOARD.

APPLICATION NOTE



(Contacts shown in *De-Energized* position)

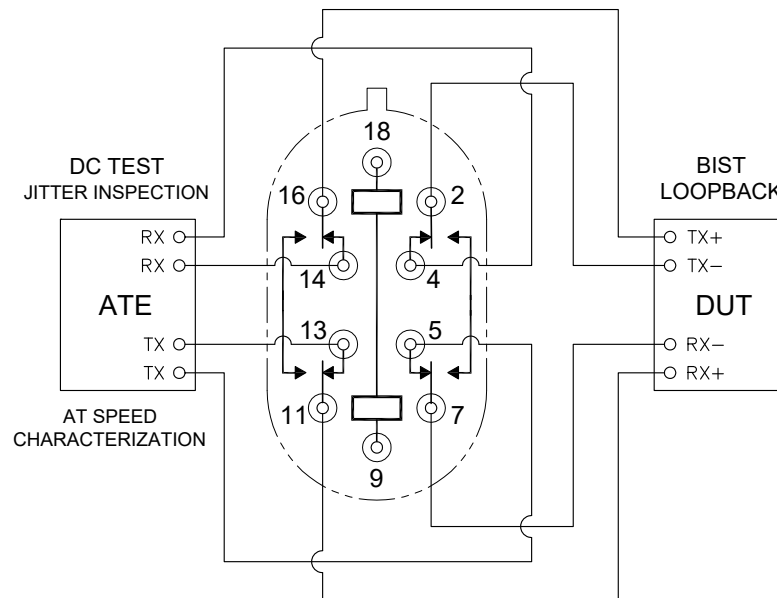


(Contacts shown in *Energized* position)

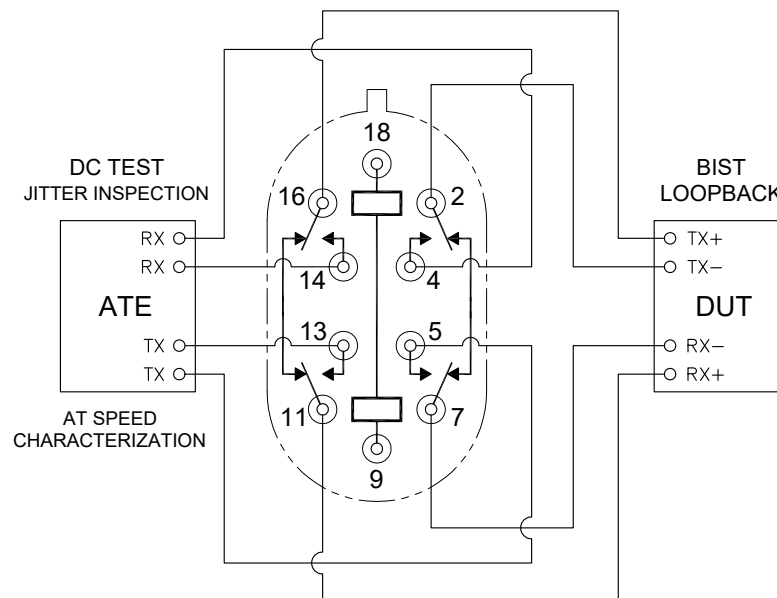
NOTES:

Coil Pins 9&18 are not polarity sensitive.
Pin assignment shown as terminal view.

APPLICATION NOTE (Applicable to No Capacitor Model)



(Contacts shown in *De-Energized* position)



(Contacts shown in *Energized* position)

NOTES:
Coil Pins 9&18 are not polarity sensitive.
Pin assignment shown as terminal view.