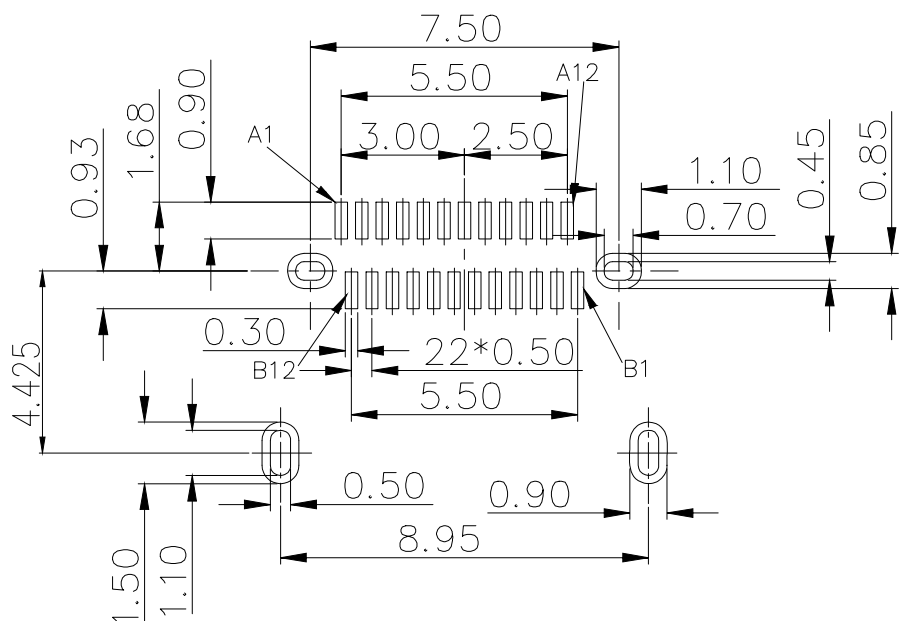


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A	<table><tr><th>PIN</th><th>Signal Name</th><th>PIN</th><th>Signal Name</th></tr><tr><td>A1</td><td>GND</td><td>M2</td><td>GND</td></tr><tr><td>A2</td><td>SSTXp1</td><td>B12</td><td>GND</td></tr><tr><td>A3</td><td>SSTXn1</td><td>B11</td><td>SSRXp1</td></tr><tr><td>A4</td><td>VBUS</td><td>B10</td><td>SSRXn1</td></tr><tr><td>A5</td><td>CC1</td><td>B9</td><td>VBUS</td></tr><tr><td>A6</td><td>Dp1</td><td>B8</td><td>SBU2</td></tr><tr><td>A7</td><td>Dn1</td><td>B7</td><td>Dn2</td></tr><tr><td>A8</td><td>SBU1</td><td>B6</td><td>Dp2</td></tr><tr><td>A9</td><td>VBUS</td><td>B5</td><td>CC2</td></tr><tr><td>A10</td><td>SSRXn2</td><td>B4</td><td>VBUS</td></tr><tr><td>A11</td><td>SSRXp2</td><td>B3</td><td>SSTXn2</td></tr><tr><td>A12</td><td>GND</td><td>B2</td><td>SSTXp2</td></tr><tr><td></td><td></td><td>B1</td><td>GND</td></tr><tr><td></td><td></td><td>M1</td><td>GND</td></tr></table>							PIN	Signal Name	PIN	Signal Name	A1	GND	M2	GND	A2	SSTXp1	B12	GND	A3	SSTXn1	B11	SSRXp1	A4	VBUS	B10	SSRXn1	A5	CC1	B9	VBUS	A6	Dp1	B8	SBU2	A7	Dn1	B7	Dn2	A8	SBU1	B6	Dp2	A9	VBUS	B5	CC2	A10	SSRXn2	B4	VBUS	A11	SSRXp2	B3	SSTXn2	A12	GND	B2	SSTXp2			B1	GND			M1	GND	A						
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