

# **Multilayer Ceramic Chip Capacitor**

Part Number: 2220YA300103KJTS3X

2220 305Vac (X2) 50/60Hz / 1000Vdc 10nF ±10% X7R (2R1) **Description:** 

IEC/EN60384-14:2013+A1 Approval

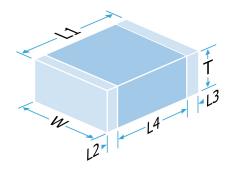
Specifications: UL60384-14, CAN/CSA E60384-14:14

TÜV R60166537 / ID11111261162 Certification:

UL/cUL E228790-20200928

IEC/EN 60384-14:2013+A1 Class X2 Classification:

UL/cUL FOWX2, FOWX8



Component Marking and Certification Bodies:





Material Group I: CTI >= 600

### Mechanical Specification

Size Code

Length (L1) in mm (")

Width (W) in mm (")

Thickness (T) in mm (")

Minimum Termination Band (L2,L3) in mm (")

Maximum Termination Band (L2,L3) in mm (")

Minimum Band Gap (L4) in mm (") (per IEC/EN 60384-14)

**Termination Material** 

Solderability Packaging

2220

 $5.7 \pm 0.40 \ (0.225 \pm 0.016)$ 

 $5.0 \pm 0.40 (0.197 \pm 0.016)$ 

2.54 Max (0.1 Max)

0.25 (0.010)

1.00 (0.040)

4.0 (0.158)

FlexiCap™ Polymer termination, Nickel barrier, Sn Plated Solder

(RoHS compliant)

IEC-60068-2-58

7" Reel Horizontal Orientation, 500 per reel

## **General Electrical Specification**

Rated Voltage

**Humidity Grade** 

Maximum DC Working Voltage Nominal Capacitance Value

Capacitance Tolerance

Tangent of Loss Angle (Tan  $\delta$ )

Capacitance and Tan δ Test Conditions

Voltage Proof

(50mA max charging current for DC tests. 50% Max, RH)

Min Insulation Resistance (IR) Dielectric Classification Rated Temperature Range

Maximum Capacitance Change over Temperature Range

Climatic Category (IEC) Ageing Characteristic

Class X2 (305Vac), 50/60Hz, 2.5kV impulse

Not applicable

1000Vdc to Annex H / (1500Vdc outside scope of any specification)

10nF +10%

≤0.025

1.0Vrms @ 1kHz

100% test: 3000Vdc 1s min / 5s max

AQL test: 3225Vdc / 1505Vac 60s min / 2.5kV 1.2x50µs impulse

100.00GOhm @ 100Vdc

X7R (2R1) -55°C / +125°C

No DC Voltage +15%

Rated DC Voltage -

55/125/56

<2% per decade (nominal capacitance is 1000 hour value)

This datasheet is for a standard item and is confirmed valid on the date generated, the latest published data

for this part may differ and is available at http://www.knowlescapacitors.com or by contacting us.

#### **Knowles Precision Devices - Sales**

Europe: KPD-Europe-sales@knowles.com Asia: KPD-Asia-sales@knowles.com

USA: KPD-NA-sales@knowles.com

www.knowlescapacitors.com

The information contained on this drawing is

confidential and may not be copied in whole or part ir any form or disclosed to a third party without the consent of Knowles and any customer mentioned within this specification.

Data is correct to the best of our knowledge, errors and

omissions excepted.

Date: Sunday, February 23, 2025



# **Multilayer Ceramic Chip Capacitor**

Part Number: 2220YA300103KJTS3X

**Description:**  $^{2220~305\text{Vac}}_{\pm 10\%}$  X7R (2R1)

#### **Environmental**

RoHS Compliant to 2011/65/EC as amended by 2015/863/EU

Compliant

**REACH Compliant** 

241 compliant

California Proposition 65

No exposure risk

### **Board Layout**

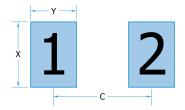
Knowles' conventional 2-terminal chip capacitors can generally be mounted using pad designs in accordance with international specification IPC-7351, Generic Requirements for Surface Mount Design and Land Pattern Standards, but there are some other factors that have been shown to reduce mechanical stress, such as reducing the pad width to less than the chip width. In addition, the position of the chip on the board should be considered.

Some high voltage parts may require modifications to the board layout and/or the addition of a conformal coating to prevent flashover, especially under high humidity conditions. Board cleanliness and environmental conditions can also impact this. Refer to application note AN0043 for further information.

Dimensions given are for guidance. It is ultimately the customers responsibility to confirm that the circuit layout is in accordance with their own product requirements.

#### IPC-7351 pad design

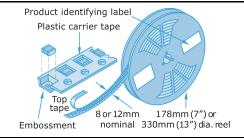
	2220	
С	5.30mm	0.209"
Y	1.20mm	0.047"
X	5.40mm	0.213"



### **Packaging**

Tape packaging information for tape-and-reel parts:

Tape and reel packing of surface mounting chip capacitors for automatic placement are in accordance with IEC60286-3.



#### Soldering

Reflow solder in accordance with IPC-A-610. Recommended reflow profile as laid down in IPC/JEDEC J-STD-020.

Wave soldering is also possible, but care must be taken for case sizes 1210 and larger and component thickness >1.0mm. Trials are encouraged.

Hand soldering is not recommended and can lead to component damage through thermal shock.

DLI

**Temperature** Preheat Time

Application notes with mounting and handling guidance are available on request.

# **Knowles Precision Devices - Sales**

Europe: KPD-Europe-sales@knowles.com

Compex

Asia: KPD-Asia-sales@knowles.com

USA: KPD-NA-sales@knowles.com

www.knowlescapacitors.com

Johanson MFG

specification.

Novacap

Syfer

Voltronics

This datasheet is for a standard item and is confirmed valid on the date generated, the latest published data for this part may differ and is available at http://www.knowlescapacitors.com or by contacting us.

The information contained on this drawing is confidential and may not be copied in whole or part ir any form or disclosed to a third party without the consenof Knowles and any customer mentioned within this

Data is correct to the best of our knowledge, errors and

Date: Sunday, February 23, 2025