Product data sheet

1. General description

Planar Schottky barrier rectifier encapsulated in a CFP2-HP (SOD323HP) power flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low forward voltage
- High power capability due to clip-bond package
- Power flat lead plastic package with exposed heatsink for optimal thermal connection

3. Applications

- Low voltage rectification
- · High efficiency DC-to-DC conversion
- Freewheeling
- · Reverse polarity protection
- OR-ing

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 172 °C		-	-	1	A
V_R	reverse voltage	T _j = 25 °C		-	-	40	V
V _F	forward voltage	I _F = 1 A; pulsed; T _j = 25 °C	[1]	-	450	530	mV
I _R	reverse current	V _R = 40 V; pulsed; T _j = 25 °C	[1]	-	8	50	μΑ
		V _R = 40 V; pulsed; T _j = 125 °C	[1]	-	4	25	mA

^[1] Very short pulse, in order to maintain a stable junction temperature.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		
2	А	anode	Transparent top view CFP2-HP (SOD323HP)	K _F € A sym001

[1] The marking bar indicates the cathode.



6. Ordering information

Table 3. Ordering information

Type number	Package	Package							
	Name	Description	Version						
PMEG4010EXD		SOD323HP: plastic surface-mounted package with solderable lead ends; 2.2 mm x 1.3 mm x 0.68 mm body	SOD323HP						

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG4010EXD	8N

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	40	V
I _F	forward current	δ = 1; $T_{sp} \le 171 ^{\circ}\text{C}$		-	1.4	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 172 °C		-	1	Α
I _{FSM}	non-repetitive peak forward current	$t_p = 8.3 \text{ ms}$; half sine wave; $T_{j(init)} = 25 \text{ °C}$		-	25	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.2	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

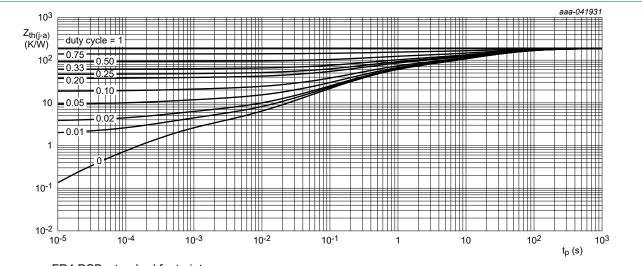
^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

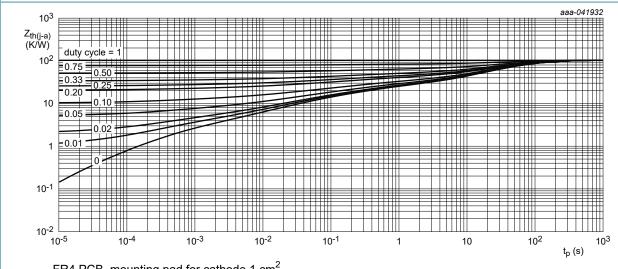
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1] [2]	-	-	230	K/W
	junction to ambient		[1] [3]	-	-	125	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	6	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [4] Soldering point of cathode tab.



FR4 PCB, standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm²

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

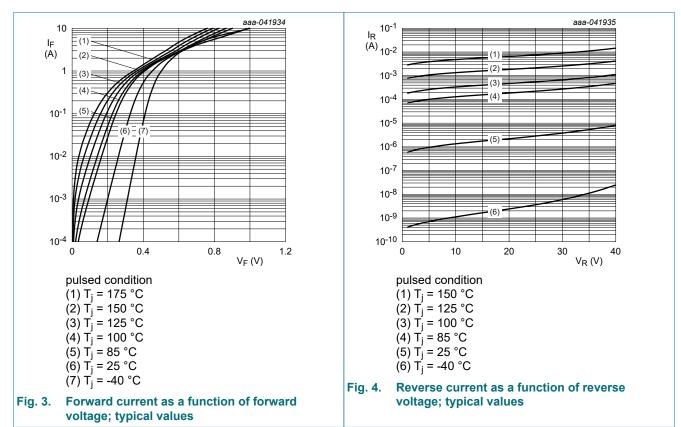
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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	$I_R = 3 \text{ mA}$; pulsed; $T_j = 25 \text{ °C}$	[1]	40	-	-	V
V _F	forward voltage	I _F = 0.5 A; pulsed; T _j = 25 °C	[1]	-	400	470	mV
		I _F = 1 A; pulsed; T _j = 25 °C	[1]	-	450	530	mV
		I _F = 1 A; pulsed; T _j = -40 °C	[1]	-	505	580	mV
		I _F = 1 A; pulsed; T _j = 125 °C	[1]	-	385	460	mV
I _R	reverse current	V _R = 40 V; pulsed; T _j = 25 °C	[1]	-	8	50	μΑ
		V _R = 40 V; pulsed; T _j = 125 °C	[1]	-	4	25	mA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C		-	83	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	29	-	pF
t _{rr}	reverse recovery time step recovery	$I_F = 0.5 \text{ A}$; $I_R = 1 \text{ A}$; $I_{R(meas)} = 0.25 \text{ A}$; $I_j = 25 \text{ °C}$		-	2.7	-	ns
	reverse recovery time ramp recovery	$dI_F/dt = 100 \text{ A/}\mu\text{s}; I_F = 1 \text{ A}; V_R = 30 \text{ V};$ $T_j = 25 ^{\circ}\text{C}$		-	5.6	-	ns
I _{RM}	peak reverse recovery current			-	0.24	-	Α
Q _{rr}	reverse recovery charge			-	0.8	-	nC
V_{FRM}	peak forward recovery voltage	$I_F = 0.5 \text{ A}; dI_F/dt = 20 \text{ A/}\mu\text{s}; T_j = 25 ^{\circ}\text{C}$		-	400	-	mV

[1] Very short pulse, in order to maintain a stable junction temperature.



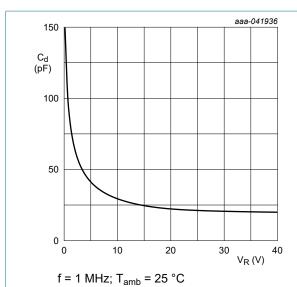
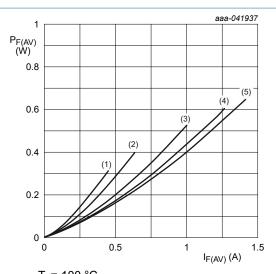
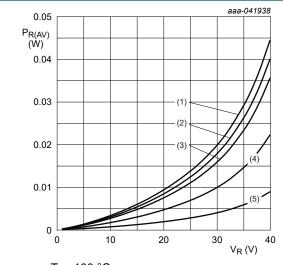


Fig. 5. Diode capacitance as a function of reverse voltage; typical values



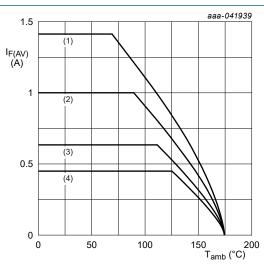
$$\begin{split} T_j &= 100 \text{ °C} \\ (1) \, \delta &= 0.1 \\ (2) \, \delta &= 0.2 \\ (3) \, \delta &= 0.5 \\ (4) \, \delta &= 0.8 \\ (5) \, \delta &= 1 \, (DC) \end{split}$$

Fig. 6. Average forward power dissipation as a function of average forward current; typical values



 $T_j = 100 \,^{\circ}\text{C}$ $(1) \, \delta = 1$ $(2) \, \delta = 0.9$ $(3) \, \delta = 0.8$ $(4) \, \delta = 0.5$ $(5) \, \delta = 0.2$

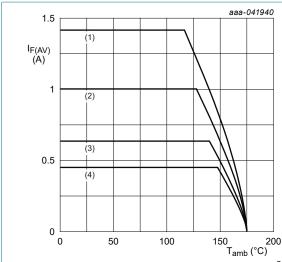
Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

 $T_j = 175$ °C (1) $\delta = 1$ (DC) (2) $\delta = 0.5$; f = 20 kHz (3) $\delta = 0.2$; f = 20 kHz (4) $\delta = 0.1$; f = 20 kHz

Fig. 8. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm²

T_i = 175 °C

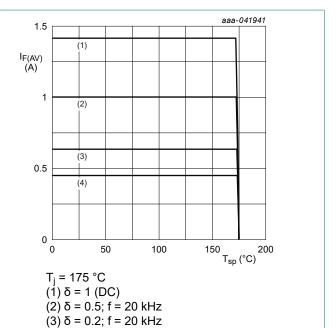
 $(1) \delta = 1 (DC)$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

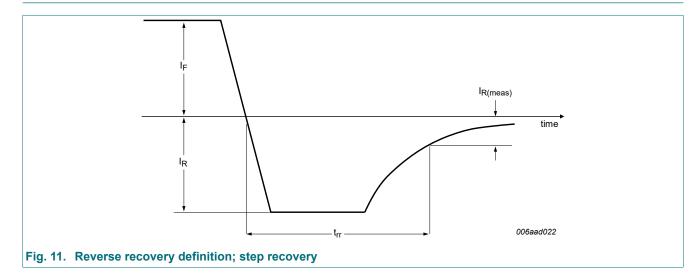
 $(4) \delta = 0.1$; f = 20 kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values



(4) δ = 0.1; f = 20 kHz
 Fig. 10. Average forward current as a function of solder point temperature; typical values

11. Test information



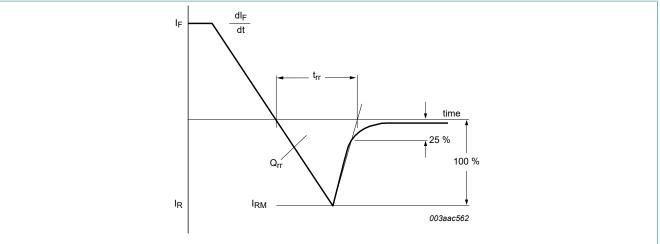


Fig. 12. Reverse recovery definition; ramp recovery

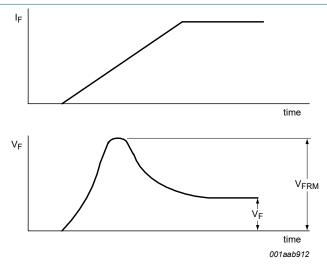


Fig. 13. Forward recovery definition

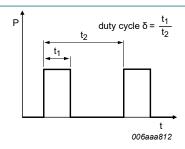


Fig. 14. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:

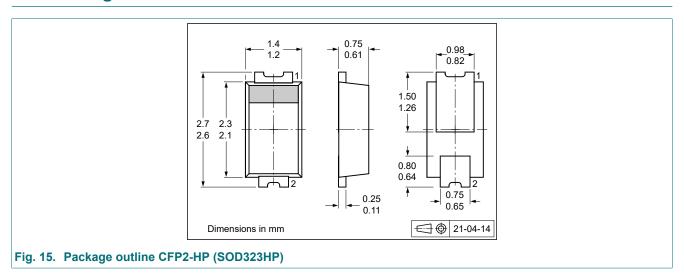
 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

 $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_{M} \times \sqrt{\delta}$

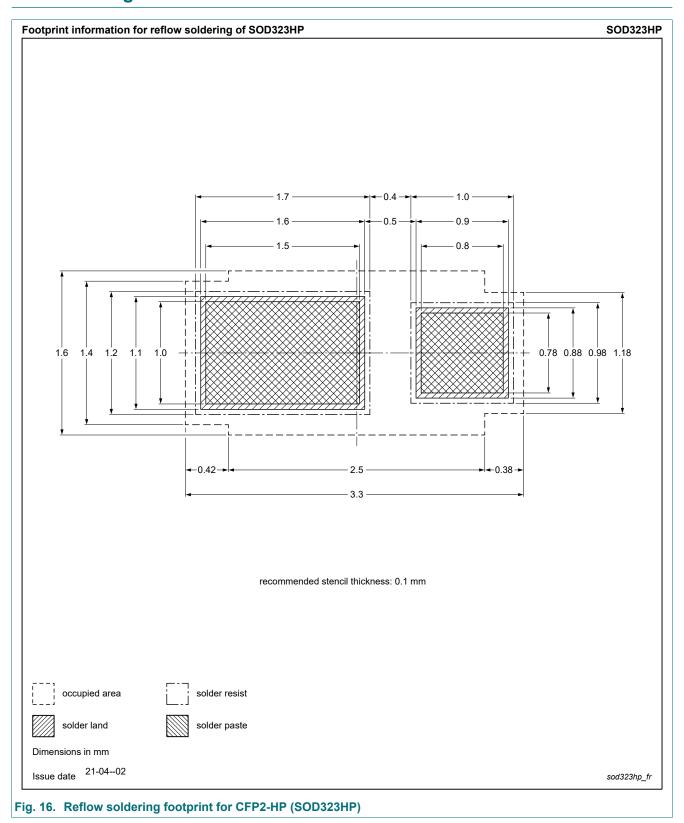
with $I_{\mbox{\scriptsize RMS}}$ defined as RMS current.

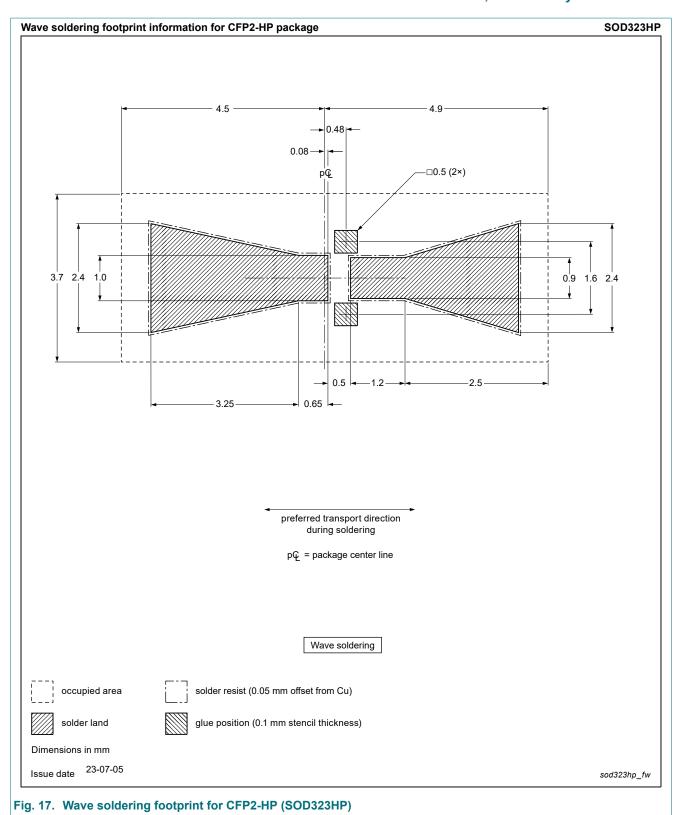
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12. Package outline



13. Soldering





14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG4010EXD v.1	20250121	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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