



60V 175°C DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _C = +25°C
60V	11mΩ @ V _{GS} = 10V	47.6A
000	$16m\Omega @ V_{GS} = 4.5V$	39.5A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

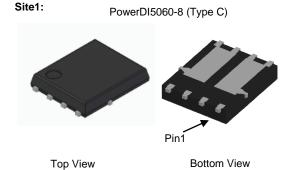
- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

Features and Benefits

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMTH6010LPDQ</u>)

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ³
- Weight: 0.097 grams (Approximate)



S1 G2 G1 D1 G2 G1 Pin out Top View Equivalent Circuit



S1 G2 G1 D1 D2 G1 Equivalent Circuit

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

Top View

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

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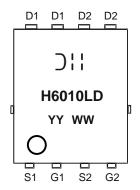
Ordering Information (Note 4)

Part Number	Pookogo	Packing		
Fait Number	Package	Qty.	Carrier	
DMTH6010LPD-13	PowerDI5060-8 (Type C)	2,500	Tape & Reel	
DMTH6010LPD-13	PowerDI5060-8/SWP (Type UXD)	2,500	Tape & Reel	

Note:

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



☐ Section 1: Section 2: Section

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6)		T _C = +25°C T _C = +100°C	lo	47.6 33.7	А
Continuous Drain Current (Note 5)	Steady State	$T_A = +25$ °C $T_A = +70$ °C	ΙD	13.1 10.9	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	170	Α
Maximum Continuous Body Diode Forward Current (Note 6)			ls	31	Α
Avalanche Current, L = 0.1mH			las	20	Α
Avalanche Energy, L = 0.1mH			Eas	20	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	2.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	53	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	PD	37.5	W
Thermal Resistance, Junction to Case (Note 6)	Rejc	4	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +175	°C	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

6. Thermal resistance from junction to soldering point (on the exposed drain pad).

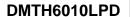


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

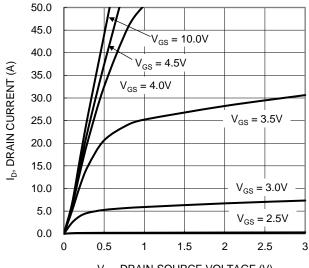
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	60	_	_	V	VGS = 0V, ID = 1mA	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μA	V _{DS} = 48V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(TH)	1		3	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	Proyen	_	8.5	11	mΩ	$V_{GS} = 10V, I_{D} = 20A$	
Static Dialit-Source Off-Resistance	RDS(ON)	_	10.9	16		$V_{GS} = 4.5V, I_D = 20A$	
Diode Forward Voltage	VsD	_	0.9	1.2	V	Vgs = 0V, Is = 20A	
DYNAMIC CHARACTERISTICS (Note 8)	DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	2615	_	pF	V _{DS} = 30V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	1415	_	pF		
Reverse Transfer Capacitance	Crss	_	58	_	pF		
Gate Resistance	Rg	_	0.67	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	20.3	_	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	_	40.2	_	nC],, and and	
Gate-Source Charge	Qgs	_	5.9	_	nC	$V_{DS} = 30V, I_{D} = 20A$	
Gate-Drain Charge	Qgd	_	9.3	_	nC	1	
Turn-On Delay Time	t _{D(ON)}	_	5.7	_	ns		
Turn-On Rise Time	t _R	_	8.8	_	ns	V _{DD} = 30V, V _{GS} = 10V,	
Turn-Off Delay Time	tD(OFF)	_	20.8	_	ns	$I_D = 20A$, $R_G = 3\Omega$	
Turn-Off Fall Time	t _F	_	7.4	_	ns	1	
Body Diode Reverse Recovery Time	trr	_	34.5	_	ns		
Body Diode Reverse Recovery Charge	Q _{RR}	_	37.5	_	nC	IF = 20A, di/dt = 100A/μs	

Notes:

^{7.} Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.







V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 1. Typical Output Characteristic

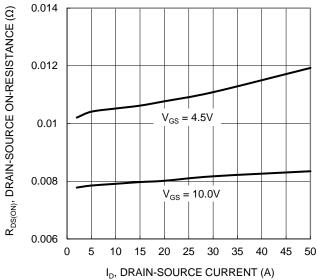


Figure 3. Typical On-Resistance vs Drain Current and Gate Voltage

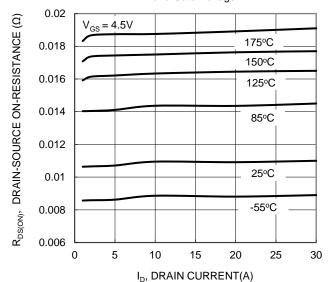
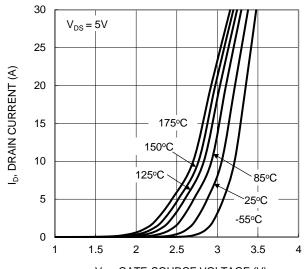


Figure 5. Typical On-Resistance vs Drain Current and Junction Temperature



V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

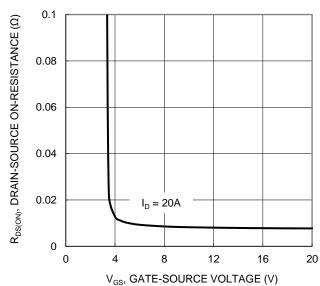


Figure 4. Typical Transfer Characteristic

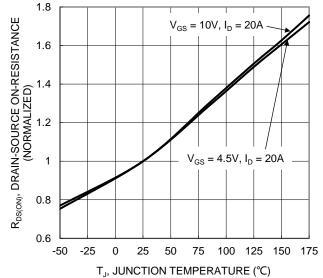


Figure 6. On-Resistance Variation with Junction Temperature



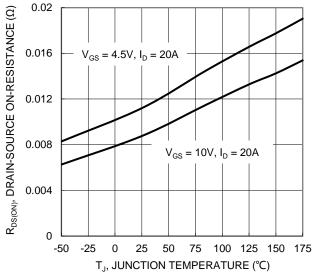


Figure 7. On-Resistance Variation with Junction Temperature

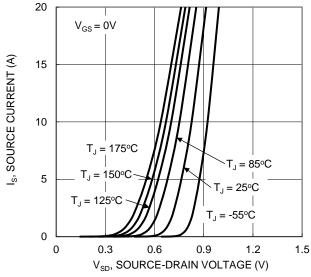


Figure 9. Diode Forward Voltage vs Current

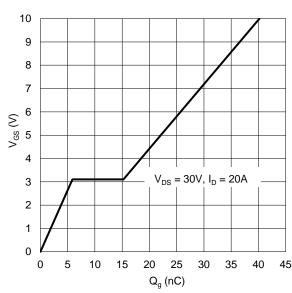


Figure 11. Gate Charge

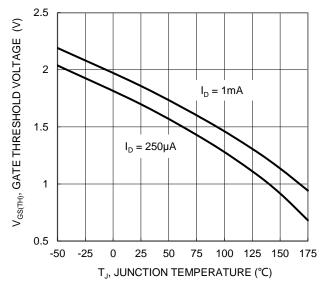


Figure 8. Gate Threshold Variation vs Junction Temperature

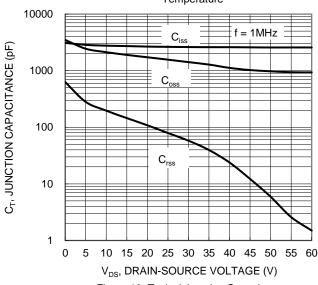


Figure 10. Typical Junction Capacitance

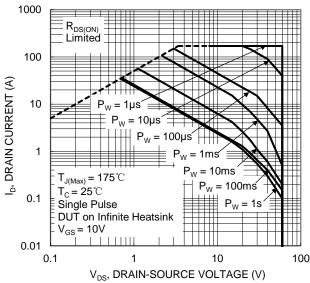


Figure 12. SOA, Safe Operation Area



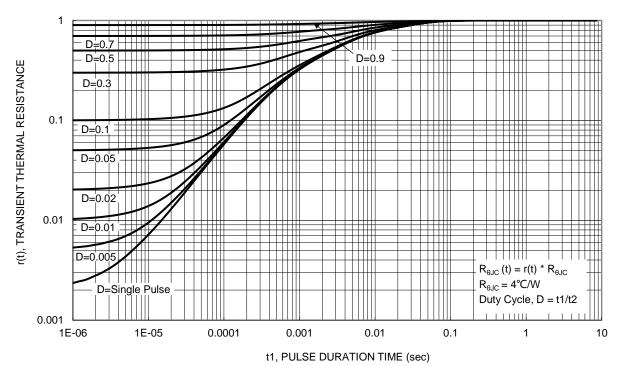


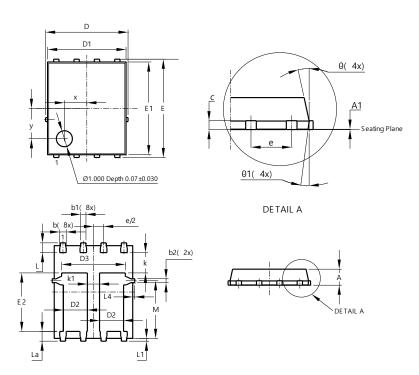
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version. **Site1:**

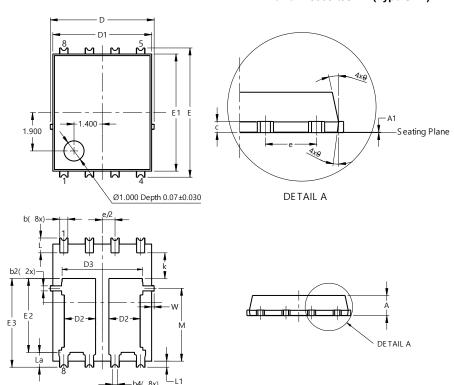
PowerDI5060-8 (Type C)



Dim Mi A 0.8 A1 0 b 0.3 b1 0.3 b2 0.2 c 0.2 D D1 4.8 D2 1.4 D3 - E E1 5.7 E2 3.8	90	Max 1.10 0.05	Typ 1.00		
A1 00 0.3 b1 0.3 b2 0.2 c 0.2 D D1 4.8 D2 1.4 E E1 5.7 E2 3.8 e)		1.00		
b 0.3 b1 0.3 b2 0.2 c 0.2 D 1 4.6 D2 1.4 D3 - E E1 5.7 E2 3.6		0.05	1.00		
b1 0.3 b2 0.2 c 0.2 D 1 4.8 D2 1.4 D3 - E E1 5.7 E2 3.8		0.05	0.02		
b2 0.2 c 0.2 D 1 4.8 D2 1.4 D3 - E E1 5.7 E2 3.8	33	0.51	0.41		
C 0.2 D1 4.8 D2 1.4 D3 - E E1 5.7 E2 3.8	00	0.366	0.333		
D 4.8 D2 1.4 D3 - E E1 5.7 E2 3.8	20	0.35	0.25		
D1 4.8 D2 1.4 D3 - E E1 5.7 E2 3.5 e	23	0.33	0.277		
D2 1.4 D3 - E E1 5.7 E2 3.5 e	5	5.15 BSC			
D3 - E	35	4.95	4.90		
E	10	1.60	1.50		
E1 5.7 E2 3.5 e		-	3.98		
E2 3.5	6.15 BSC				
е	75	5.85	5.80		
	56	3.76	3.66		
k	1.27BSC				
r		-	1.27		
k1 0.5	56	-	-		
L 0.5	51	0.71	0.61		
La 0.5	51	0.71	0.61		
L1 0.0)5	0.20	0.175		
L4 -		-	0.125		
M 3.5	50	3.71	3.605		
х -		-	1.400		
у -		-	1.900		
θ 10		12°	11°		
01 6		8°	7°		
All Dimensions in mm					

Site2:

PowerDI5060-8/SWP (Type UXD)



Dim	Min	Max	Тур
Α	0.90	1.10	1.00
A1	0.00	0.05	
b	0.30	0.50	0.41
b2	0.20	0.35	0.25
b4).25REF	-
С	0.230	0.330	0.277
D	5	.15 BS0	
D1	4.70	5.10	4.90
D2	1.46	1.66	1.55
D3	3.78	4.18	3.98
E	6.40 BSC		
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
E2a	4.195	4.595	4.395
е	1	.27BSC)
k	1.05		
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
M	3.205	4.005	3.605
W	0.025	0.225	0.125
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

PowerDI5060-8/SWP (Type UXD)

DMTH6010LPD

Document number: DS38244 Rev. 3 - 2

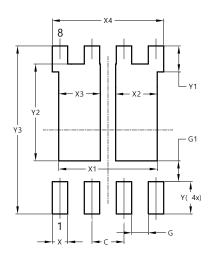


Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

Site1:

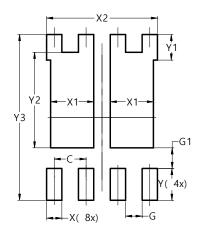
PowerDI5060-8 (Type C)



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	3.910
X2	1.650
Х3	1.650
X4	4.420
Υ	1.270
Y1	1.020
Y2	3.810
Y3	6.610

Site2:

PowerDI5060-8/SWP (Type UXD)



Dimensions	Value (in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	1.720		
X2	4.420		
Υ	1.270		
Y1	1.020		
Y2	3.810		
V3	6 610		



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