

# Multilayer Ceramic Chip Capacitors

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## C3225X7S2A475M200AB

### Product Status

Production (NRND)

Recommended Alternate Part No. : [C3225X7S2A475K200AB](#)  
(Interchangeability is not guaranteed.)

### TDK Item Description

C3225X7S2A475MT\*\*\*\*

### Applications

Commercial Grade  
Please refer to Part No. [CGA6M3X7S2A475M200AB](#) for Automotive use.

### Feature

Mid Mid Voltage (100 to 630V)

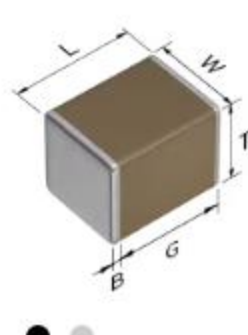
### Series

C3225 [EIA 1210]

### Brand

TDK

### Environmental Compliance



Images are for reference only and show example products.

### Size

Length(L)	3.20mm ±0.40mm
Width(W)	2.50mm ±0.30mm
Thickness(T)	2.00mm ±0.20mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	
Recommended Land Pattern (PA)	2.00mm to 2.40mm
Recommended Land Pattern (PB)	1.00mm to 1.20mm
Recommended Land Pattern (PC)	1.90mm to 2.50mm

### Electrical Characteristics

Capacitance	4.7μF ±20%
Rated Voltage	100VDC
Temperature Characteristic	X75(±22%)
Dissipation Factor (Max.)	5%
Insulation Resistance (Min.)	106MΩ

### Other

Operating Temp. Range	-55 to 125°C
Soldering Method	Reflow
AEC-Q200	NO
Packing	Embossed (Plastic)Taping [180mm Reel]
Package Quantity	1000pcs

**Catalog** Update

Distributor Stock Info

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Distributor List

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### Tech Info

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#### DOCUMENTS

- Specification
- RoHS Certificate
- SVHC / REACH Certificate
- Characterization Sheet
- Sample Kits

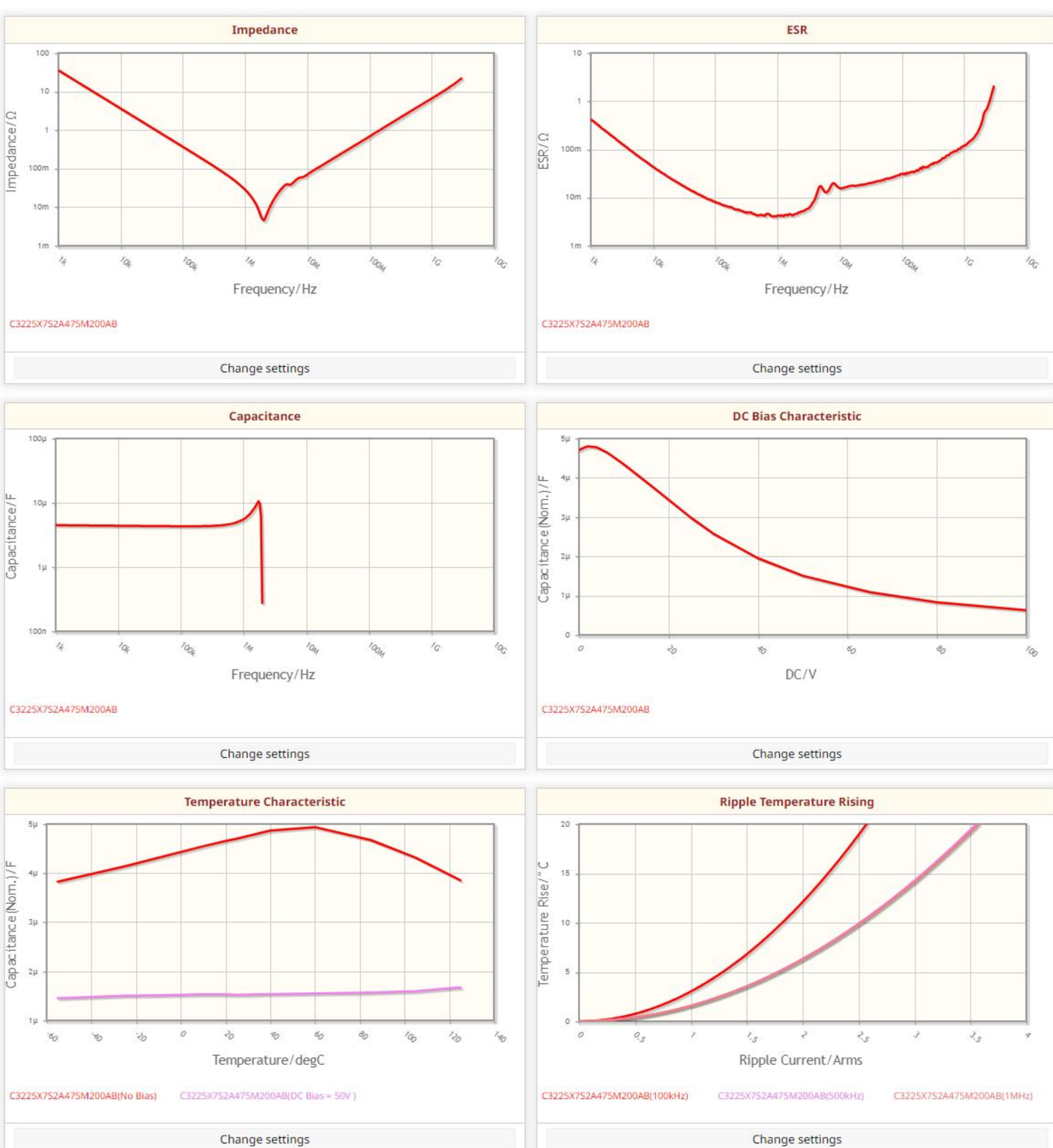
#### TECHNICAL SUPPORT TOOLS

- S-parameter
- SPICE Netlist (Simple)
- SPICE Netlist (Precision)
- Equivalent Circuit Model
- DC Bias / Superimposition Model (HSpice)
- DC Bias / Superimposition Model (LTSpice)
- DC Bias / Superimposition Model (PSpice)

#### 3RD PARTY ECAD MODELS

SnapMagic Special features: 3D model

Characteristic Graph (This is reference data, and does not guarantee the products characteristics.)



## Customer who viewed this product also viewed

**AMIC CHIP CAPACITORS**

**105M160AA**

0.6mm  
630V

**MULTILAYER CERAMIC CHIP CAPACITORS**

**C3225C0G2J682J200AA**

Capacitance=6.8nF  
Edc=630V  
T.C.=C0G  
LxWxT:3.2x2.5x2mm  
Mid Volt. (100 to 630V)

**MULTILAYER CERAMIC CHIP CAPACITORS**

**C3225X7R2A225M230AB**

Capacitance=2.2μF  
Edc=100V  
T.C.=X7R  
LxWxT:3.2x2.5x2.3mm  
Mid Volt. (100 to 630V)

**MULTILAYER**

**C3225X7S**

Capacitance=100V  
Edc=100V  
T.C.=X7S  
LxWxT:3.2x2.5x2.3mm  
Mid Volt. (100 to 630V)



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