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FJY4002R PNP Epitaxial Silicon Transistor with Bias Resistor

Features

- 100 mA Output Current Capability
- Built-in Bias Resistor ($R_1 = 10 \text{ k}\Omega$, $R_2 = 10 \text{ k}\Omega$)

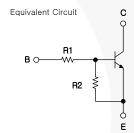
Application

- Switching, Interface, and Driver Circuits
- Inverters
- · Digital Applications in Industrial Segments



Description

Transistors with built-in resistors can be excellent space- and cost-saving solutions by reducing component count and simplifying circuit design.



Ordering Information

Part Number	Top Mark	Package	Packing Method	
FJY4002R	S52	SOT-523F 3L	Tape and Reel	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	-50	V
V _{CEO}	Collector-Emitter Voltage	-50	V
V _{EBO}	Emitter-Base Voltage	-10	V
I _C	Collector Current	-100	mA
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C

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FJY4002R Rev. 1.1.2

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Thermal Characteristics(1)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
D.	Power Dissipation	200	mW
P _D	Derate Above T _A = 25°C	1.60	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	625	°C/W

Note:

1. PCB size: FR-4 76 x 114 x 0.6T mm³ (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Electrical Characteristics(2)

Values are at $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V _{(BR)CBO}	Collector-Emitter Breakdown Voltage	$I_C = -10 \mu\text{A}, I_E = 0$	-50			V
V _{(BR)CEO}	Collector-Base Breakdown Voltage	$I_C = -100 \mu\text{A}, I_B = 0$	-50			V
I _{CBO}	Collector Cut-Off Current	$V_{CB} = -40 \text{ V}, I_{E} = 0$			-0.1	μΑ
h _{FE}	DC Current Gain	$V_{CE} = -5 \text{ V}, I_{C} = -5 \text{ mA}$	30			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$			-0.3	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	1	200		MHz
C _{cb}	Output Capacitance	$V_{CB} = -10 \text{ V}, I_{E} = 0,$ f = 1.0 MHz		5.5		pF
V _I (off)	Input-Off Voltage	$V_{CE} = -5 \text{ V}, I_{C} = -100 \mu\text{A}$			-0.5	V
V _I (on)	Input-On Voltage	$V_{CE} = -0.3 \text{ V}, I_{C} = -10 \text{ mA}$	-3			V
R ₁	Input Resistor		7	10	13	kΩ
R ₁ /R ₂	Resistor Ratio		0.9	1.0	1.1	

Note:

2. Pulse test: pulse width $\leq 300~\mu s,$ duty cycle $\leq 2\%.$

Typical Performance Characteristics

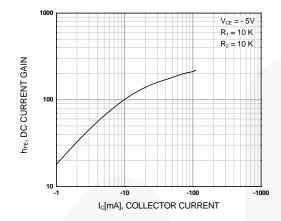


Figure 1. DC Current Gain

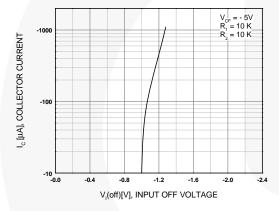


Figure 3. Input-Off Voltage

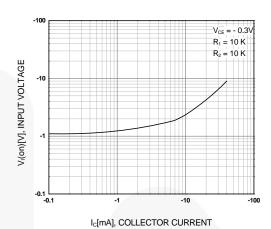


Figure 2. Input-On Voltage

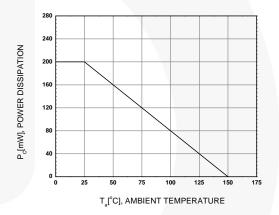


Figure 4. Power Derating

Physical Dimensions

SOT-523F

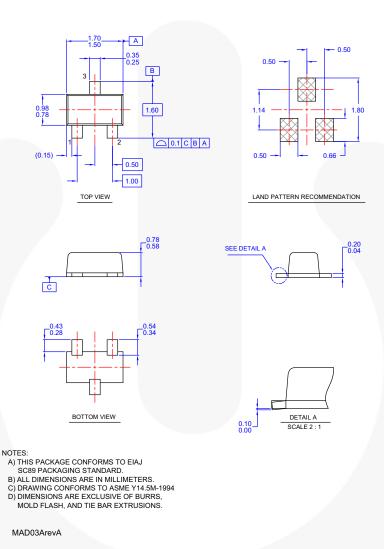


Figure 5. 3-LEAD, SC89, EIAJ-SC89, 0.88 MM WIDE, SOT523F (ACTIVE)

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Definition of Terms		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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