

CD4528BC Dual Monostable Multivibrator

General Description

The CD4528BC is a dual monostable multivibrator. Each device is retriggerable and resettable. Triggering can occur from either the rising or falling edge of an input pulse, resulting in an output pulse over a wide range of widths. Pulse duration and accuracy are determined by external timing components Rx and Cx.

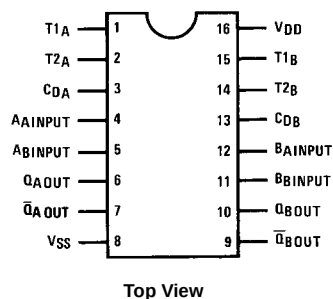
Features

- Wide supply voltage range: 3.0V to 18V
- Separate reset available
- Quiescent current = 5.0 nA/package (typ.) at 5.0 V_{DC}
- Diode protection on all inputs
- Triggerable from leading or trailing edge pulse
- Capable of driving two low-power TTL loads or one low-power Schottky TTL load over the rated temperature range

Ordering Code:

Order Number	Package Number	Package Description
CD4528BCM	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
CD4528BCN	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide

Connection Diagram

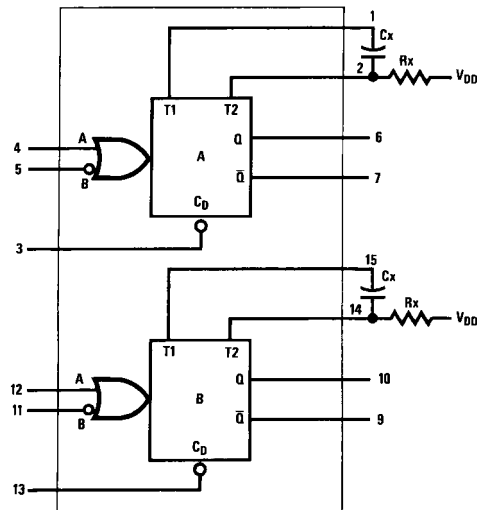


Truth Table

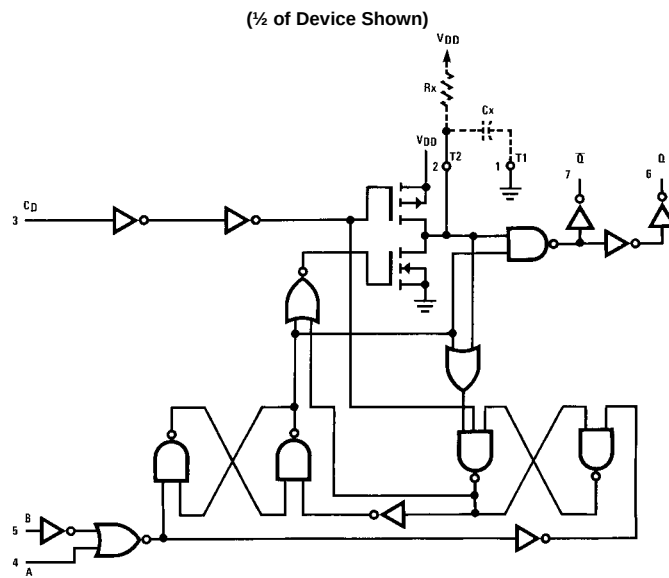
Inputs			Outputs	
Clear	A	B	Q	Q
L	X	X	L	H
X	H	X	L	H
X	X	L	L	H
H	L	↓	⌋	⌋
H	↑	H	⌋	⌋

H = HIGH Level
L = LOW Level
↑ = Transition from LOW-to-HIGH
↓ = Transition from HIGH-to-LOW
⌋ = One HIGH Level Pulse
⌋ = One LOW Level Pulse
X = Irrelevant

Block Diagram



Logic Diagram



Note: Externally ground pins 1 and 15 to pin 8.

Absolute Maximum Ratings(Note 1)

(Note 2)

DC Supply Voltage (V_{DD})	$-0.5 V_{DC}$ to $+18 V_{DC}$
Input Voltage, All Inputs (V_{IN})	$-0.5 V_{DC}$ to $V_{DD} + 0.5 V_{DC}$
Storage Temperature Range (T_S)	-65°C to $+150^{\circ}\text{C}$
Power Dissipation (P_D)	
Dual-In-Line	700 mW
Small Outline	500 mW
Lead Temperature (T_L)	
(Soldering, 10 seconds)	260°C

Recommended Operating Conditions (Note 2)

DC Supply Voltage (V_{DD})	3V to 15V
Input Voltage (V_{IN})	0V to V_{DD} V_{DC}
Operating Temperature Range (T_A)	-55°C to $+125^{\circ}\text{C}$

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range", they are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

Note 2: $V_{SS} = 0\text{V}$ unless otherwise specified.

DC Electrical Characteristics (Note 3)

Symbol	Parameter	Conditions	-55°C		$+25^{\circ}\text{C}$			$+125^{\circ}\text{C}$		Units
			Min	Max	Min	Typ	Max	Min	Max	
I_{DD}	Quiescent Device Current	$V_{DD} = 5\text{V}$ $V_{DD} = 10\text{V}$ $V_{DD} = 15\text{V}$		5 10 20		0.005 0.010 0.015	5 10 20		150 300 600	μA
V_{OL}	LOW Level Output Voltage	$V_{DD} = 5\text{V}$ $V_{DD} = 10\text{V}$ $V_{DD} = 15\text{V}$		0.05 0.05 0.05			0.05 0.05 0.05		0.05 0.05 0.05	V
V_{OH}	HIGH Level Output Voltage	$V_{DD} = 5\text{V}$ $V_{DD} = 10\text{V}$ $V_{DD} = 15\text{V}$	4.95 9.95 14.95		4.95 9.95 14.95	5.0 10.0 15.0		4.95 9.95 14.95		V
V_{IL}	LOW Level Input Voltage	$V_{DD} = 5\text{V}, V_O = 0.5\text{V or } 4.5\text{V}$ $V_{DD} = 10\text{V}, V_O = 1\text{V or } 9\text{V}$ $V_{DD} = 15\text{V}, V_O = 1.5\text{V or } 13.5\text{V}$		1.5 3.0 4.0		2.25 4.50 6.75	1.5 3.0 4.0		1.5 3.0 4.0	V
V_{IH}	HIGH Level Input Voltage	$V_{DD} = 5\text{V}, V_O = 0.5\text{V or } 4.5\text{V}$ $V_{DD} = 10\text{V}, V_O = 1\text{V or } 9\text{V}$ $V_{DD} = 15\text{V}, V_O = 1.5\text{V or } 13.5\text{V}$	3.5 7.0 11.0		3.5 7.0 11.0	2.75 5.50 8.25		3.5 7.0 11.0		V
I_{OL}	LOW Level Output Current (Note 4)	$V_{DD} = 5\text{V}, V_O = 0.4\text{V}$ $V_{DD} = 10\text{V}, V_O = 0.5\text{V}$ $V_{DD} = 15\text{V}, V_O = 1.5\text{V}$	0.64 1.6 4.2		0.51 1.3 3.4	0.88 2.25 8.8		0.36 0.9 2.4		mA
I_{OH}	HIGH Level Output Current (Note 4)	$V_{DD} = 5\text{V}, V_O = 4.6\text{V}$ $V_{DD} = 10\text{V}, V_O = 9.5\text{V}$ $V_{DD} = 15\text{V}, V_O = 13.5\text{V}$	-0.25 -0.62 -1.8		-0.2 -0.5 -1.5	-0.36 -0.9 -3.5		-0.14 -0.35 -1.1		mA
I_{IN}	Input Current	$V_{DD} = 15\text{V}, V_{IN} = 0\text{V}$ $V_{DD} = 15\text{V}, V_{IN} = 15\text{V}$		-0.1 0.1		-10^{-5} 10^{-5}	-0.1 0.1		-1.0 1.0	μA

Note 3: $V_{SS} = 0\text{V}$ unless otherwise specified.

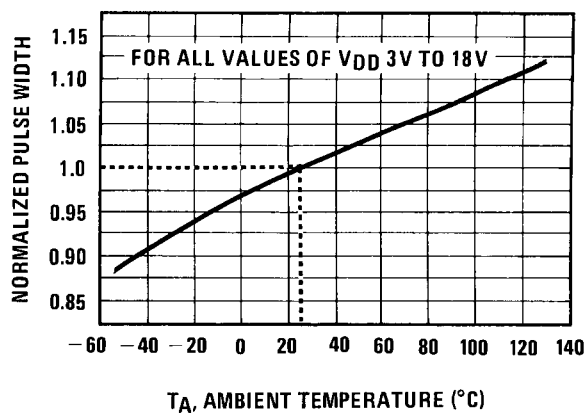
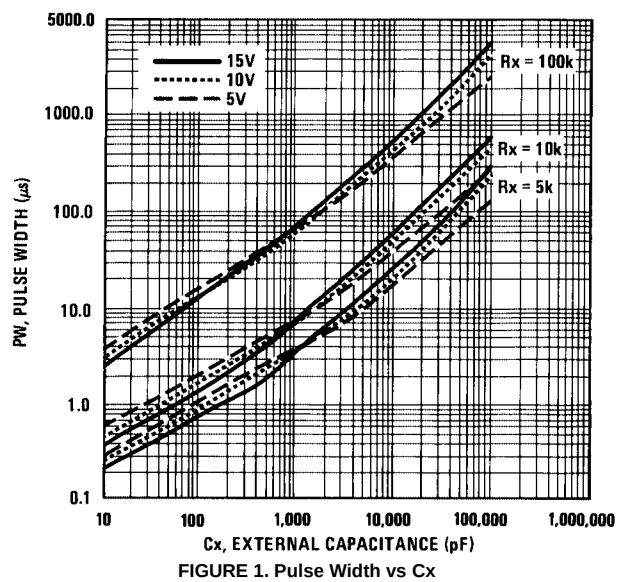
Note 4: I_{OH} and I_{OL} are tested one output at a time.

AC Electrical Characteristics (Note 5) $T_A = 25^\circ\text{C}$, $C_L = 50\text{ pF}$, $R_L = 200\text{ k}\Omega$, Input $t_r = t_f = 20\text{ ns}$, unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Units
t_r	Output Rise Time	$t_r = (3.0\text{ ns/pF}) C_L + 30\text{ ns}$, $V_{DD} = 5.0\text{V}$ $t_r = (1.5\text{ ns/pF}) C_L + 15\text{ ns}$, $V_{DD} = 10.0\text{V}$ $t_r = (1.1\text{ ns/pF}) C_L + 10\text{ ns}$, $V_{DD} = 15.0\text{V}$		180 90 65	400 200 160	ns
t_f	Output Fall Time	$t_f = (1.5\text{ ns/pF}) C_L + 25\text{ ns}$, $V_{DD} = 5.0\text{V}$ $t_f = (0.75\text{ ns/pF}) C_L + 12.5\text{ ns}$, $V_{DD} = 10\text{V}$ $t_f = (0.55\text{ ns/pF}) C_L + 9.5\text{ ns}$, $V_{DD} = 15.0\text{V}$		100 50 35	200 100 80	ns
t_{PLH} t_{PHL}	Turn-Off, Turn-On Delay A or B to Q or $\bar{Q}$ $C_x = 15\text{ pF}$, $R_x = 5.0\text{ k}\Omega$	$t_{PLH}, t_{PHL} = (1.7\text{ ns/pF}) C_L + 240\text{ ns}$, $V_{DD} = 5.0\text{V}$ $t_{PLH}, t_{PHL} = (0.66\text{ ns/pF}) C_L + 8\text{ ns}$, $V_{DD} = 10.0\text{V}$ $t_{PLH}, t_{PHL} = (0.5\text{ ns/pF}) C_L + 65\text{ ns}$, $V_{DD} = 15.0\text{V}$		230 100 65	500 250 150	ns
	Turn-Off, Turn-On Delay A or B to Q or $\bar{Q}$ $C_x = 100\text{ pF}$, $R_x = 10\text{ k}\Omega$	$t_{PLH}, t_{PHL} = (1.7\text{ ns/pF}) C_L + 620\text{ ns}$, $V_{DD} = 5.0\text{V}$ $t_{PLH}, t_{PHL} = (0.66\text{ ns/pF}) C_L + 257\text{ ns}$, $V_{DD} = 10.0\text{V}$ $t_{PLH}, t_{PHL} = (0.5\text{ ns/pF}) C_L + 185\text{ ns}$, $V_{DD} = 15.0\text{V}$		230 100 65	500 250 150	ns
t_{WL} t_{WH}	Minimum Input Pulse Width A or B $C_x = 15\text{ pF}$, $R_x = 5.0\text{ k}\Omega$	$V_{DD} = 5.0\text{V}$ $V_{DD} = 10.0\text{V}$ $V_{DD} = 15\text{V}$		60 20 20	150 50 50	ns
	$C_x = 1000\text{ pF}$, $R_x = 10\text{ k}\Omega$	$V_{DD} = 5.0\text{V}$ $V_{DD} = 10.0\text{V}$ $V_{DD} = 15.0\text{V}$		60 20 20	150 50 50	ns
PW_{OUT}	Output Pulse Width Q or $\bar{Q}$ For $C_x < 0.01\text{ }\mu\text{F}$ (See Graph for Appropriate V_{DD} Level) $C_x = 15\text{ pF}$, $R_x = 5.0\text{ k}\Omega$	$V_{DD} = 5.0\text{V}$ $V_{DD} = 10.0\text{V}$ $V_{DD} = 15.0\text{V}$		550 350 300		ns
	For $C_x > 0.01\text{ }\mu\text{F}$ Use $PW_{out} = 0.2 R_x C_x \ln [V_{DD} - V_{SS}]$ $C_x = 10,000\text{ pF}$, $R_x = 10\text{ k}\Omega$	$V_{DD} = 5.0\text{V}$ $V_{DD} = 10.0\text{V}$ $V_{DD} = 15.0\text{V}$	15 10 15	29 37 42	45 90 95	μs
t_{PLH} t_{PHL}	Reset Propagation Delay, t_{PLH}, t_{PHL} $C_x = 15\text{ pF}$, $R_x = 5.0\text{ k}\Omega$	$V_{DD} = 5.0\text{V}$ $V_{DD} = 10.0\text{V}$ $V_{DD} = 15.0\text{V}$		325 90 60	600 225 170	ns
	$C_x = 1000\text{ pF}$, $R_x = 10\text{ k}\Omega$	$V_{DD} = 5.0\text{V}$ $V_{DD} = 10.0\text{V}$ $V_{DD} = 15.0\text{V}$		7.0 6.7 6.7		μs
t_{RR}	Minimum Retrigger Time $C_x = 15\text{ pF}$, $R_x = 5.0\text{ k}\Omega$	$V_{DD} = 5.0\text{V}$ $V_{DD} = 10.0\text{V}$ $V_{DD} = 15.0\text{V}$		0 0 0		ns
	$C_x = 1000\text{ pF}$, $R_x = 10\text{ k}\Omega$	$V_{DD} = 5.0\text{V}$ $V_{DD} = 10.0\text{V}$ $V_{DD} = 15.0\text{V}$		0 0 0		
	Pulse Width Match between Circuits in the Same Package $C_x = 10,000\text{ pF}$, $R_x = 10\text{ k}\Omega$	$V_{DD} = 5.0\text{V}$ $V_{DD} = 10.0\text{V}$ $V_{DD} = 15.0\text{V}$		6 8 8	25 35 35	%

Note 5: AC parameters are guaranteed by DC correlated testing.

Pulse Widths



AC Test Circuits and Waveforms

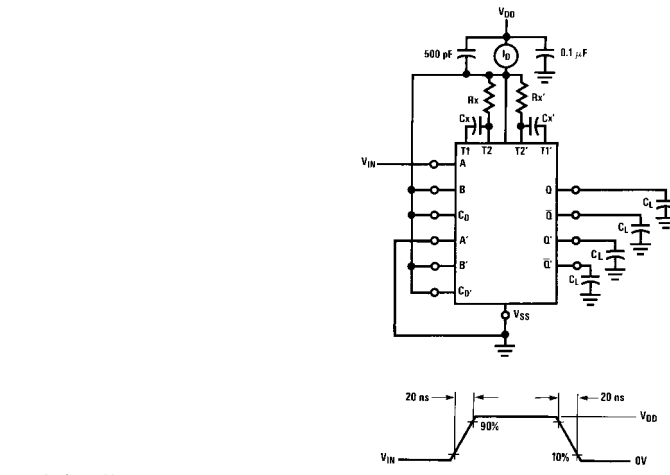
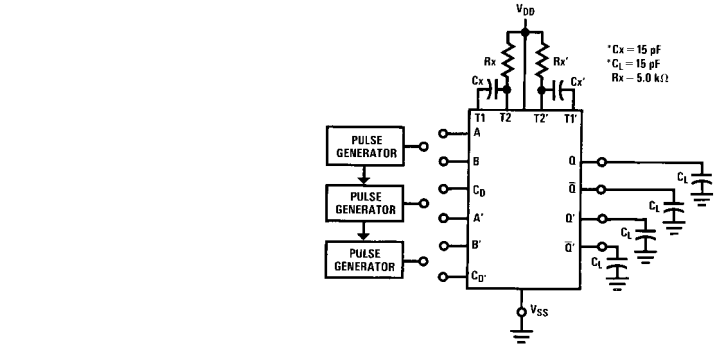


FIGURE 3. Power Dissipation Test Circuit and Waveforms



*Includes capacitance of probes, wiring, and fixture parasitic.
Note: AC test waveforms for PG1, PG2, and PG3 in Figure 4.

Input Connections

Characteristics	C _D	A	B
t _{PLH} , t _{PHL} , t _r , t _f , PW _{out} , PW _{in}	V _{DD}	PG1	V _{DD}
t _{PLH} , t _{PHL} , t _r , t _f , PW _{out} , PW _{in}	V _{DD}	V _{SS}	PG2
t _{PLH(R)} , t _{PHL(R)} , PW _{in}	PG3	PG1	PG2

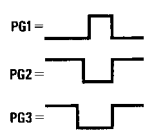


FIGURE 4. AC Test Circuit

AC Test Circuits and Waveforms (Continued)

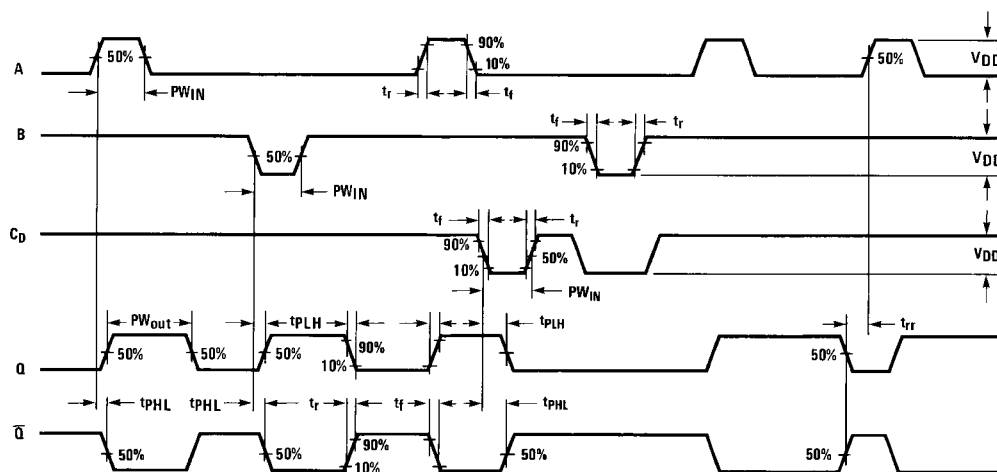
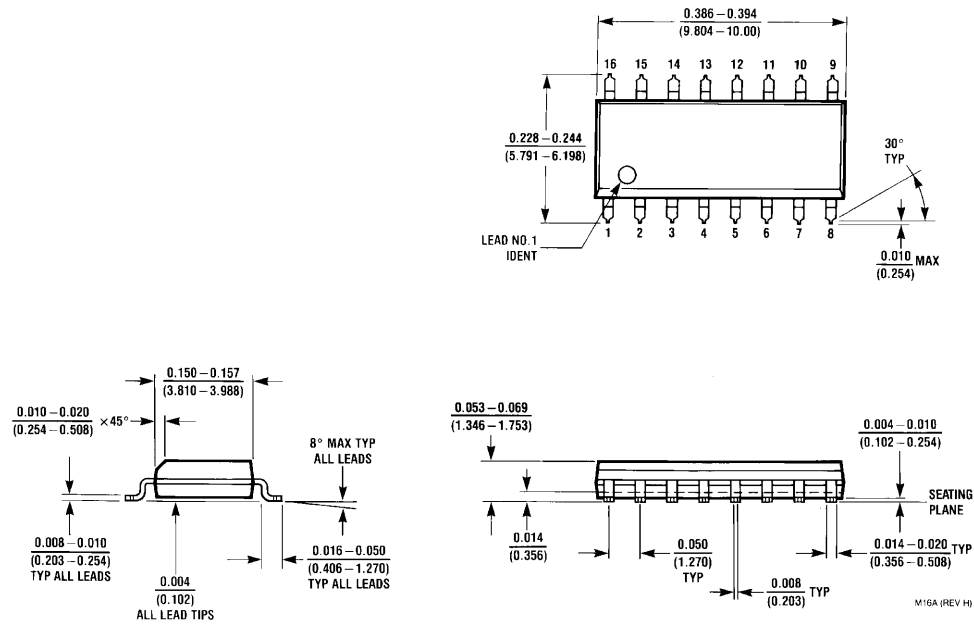


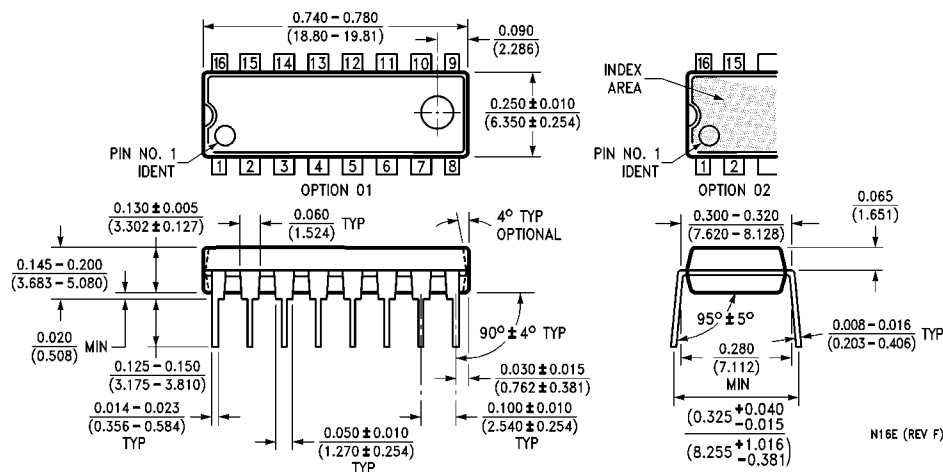
FIGURE 5. AC Test Waveforms

Physical Dimensions inches (millimeters) unless otherwise noted



16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
Package Number M16A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



**16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide
Package Number N16E**

Fairchild does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and Fairchild reserves the right at any time without notice to change said circuitry and specifications.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
 2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.
- www.fairchildsemi.com**

www.fairchildsemi.com