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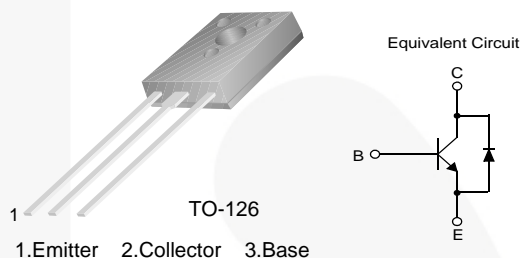
June 2014

# FJE5304D

## NPN Triple Diffused Planar Silicon Transistor

### Features

- High-Voltage, High-Speed Power Switch Applications
- Wide Safe Operating Area
- Built-in Free-Wheeling diode
- Suitable for Electronic Ballast Applications
- Small Variance in Storage Time



### Ordering Information

Part Number	Top Mark	Package	Packing Method
FJE5304D	J5304D	TO-126 3L	Bulk
FJE5304DTU	J5304D	TO-126 3L	Rail

### Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_C = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
$V_{CBO}$	Collector-Base Voltage	700	V
$V_{CEO}$	Collector-Emitter Voltage	400	V
$V_{EBO}$	Emitter-Base Voltage	12	V
$I_C$	Collector Current (DC)	4	A
$I_{CP}$	Collector Current (Pulse) <sup>(1)</sup>	8	A
$I_B$	Base Current (DC)	2	A
$I_{BP}$	Base Current (Pulse) <sup>(1)</sup>	4	A
$T_{STG}$	Storage Temperature Range	-65 to 150	$^\circ\text{C}$

#### Note:

1. Pulse test: pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .

## Thermal Characteristics

Values are at  $T_C = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
$P_C$	Collector Dissipation ( $T_C = 25^\circ\text{C}$ )	30	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	4.17	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	83.3	$^\circ\text{C/W}$

## Electrical Characteristics<sup>(2)</sup>

Values are at  $T_C = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 1 mA, I <sub>E</sub> = 0	700			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 5 mA, I <sub>B</sub> = 0	400			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 1 mA, I <sub>C</sub> = 0	12			V
I <sub>CES</sub>	Collector Cut-Off Current	V <sub>CE</sub> = 700 V, V <sub>EB</sub> = 0			100	μA
I <sub>CEO</sub>	Collector Cut-Off Current	V <sub>CE</sub> = 400 V, I <sub>B</sub> = 0			250	μA
I <sub>EBO</sub>	Emitter Cut-Off Current	V <sub>EB</sub> = 12 V, I <sub>C</sub> = 0			100	μA
h <sub>FE</sub>	DC Current Gain	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 10 mA	10			
		V <sub>CE</sub> = 5 V, I <sub>C</sub> = 2 A	8		40	
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 0.5 A, I <sub>B</sub> = 0.1 A			0.7	V
		I <sub>C</sub> = 1 A, I <sub>B</sub> = 0.2 A			1.0	
		I <sub>C</sub> = 2.5 A, I <sub>B</sub> = 0.5 A			1.5	
V <sub>BE(sat)</sub>	Collector-Base Saturation Voltage	I <sub>C</sub> = 0.5 A, I <sub>B</sub> = 0.1 A			1.1	V
		I <sub>C</sub> = 1 A, I <sub>B</sub> = 0.2 A			1.2	
		I <sub>C</sub> = 2.5 A, I <sub>B</sub> = 0.5 A			1.3	
V <sub>f</sub>	Internal Diode Forward Voltage Drop	I <sub>F</sub> = 2 A			2.5	V
Inductive Load Switching (V <sub>CC</sub> = 200 V)						
t <sub>stg</sub>	Storage Time	I <sub>C</sub> = 2 A, I <sub>B1</sub> = 0.4 A, V <sub>BE(off)</sub> = -5 V, L = 200 μH		0.6		μs
t <sub>f</sub>	Fall Time			0.1		μs
Resistive Load Switching (V <sub>CC</sub> = 250 V)						
t <sub>stg</sub>	Storage Time	I <sub>C</sub> = 2 A, I <sub>B1</sub> = I <sub>B2</sub> = 0.4 A, T <sub>P</sub> = 30 μs			2.9	μs
t <sub>f</sub>	Fall Time			0.2		μs

### Note:

2. Pulse test: pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

## Typical Performance Characteristics

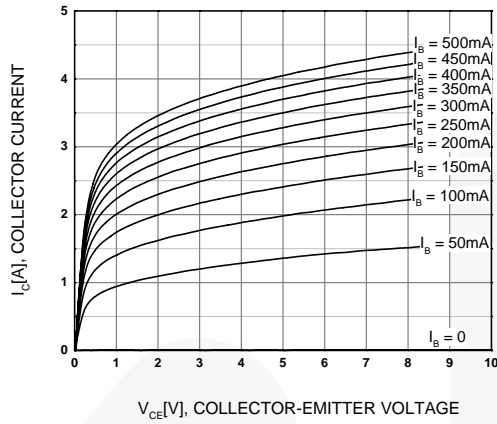


Figure 1. Static Characteristic

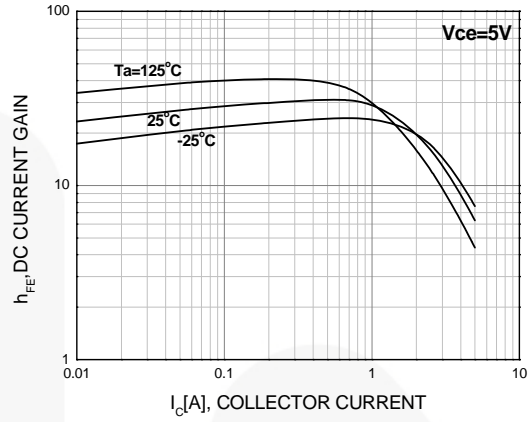


Figure 2. DC Current Gain

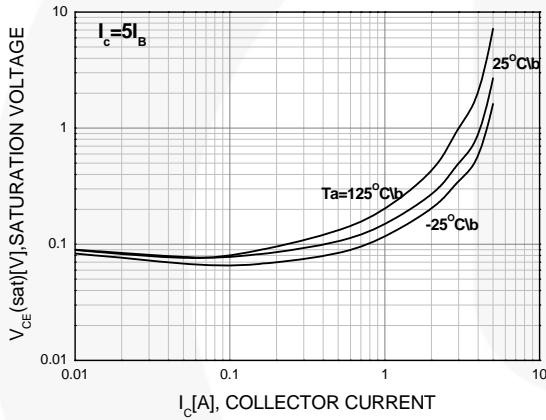


Figure 3. Collector-Emitter Saturation Voltage

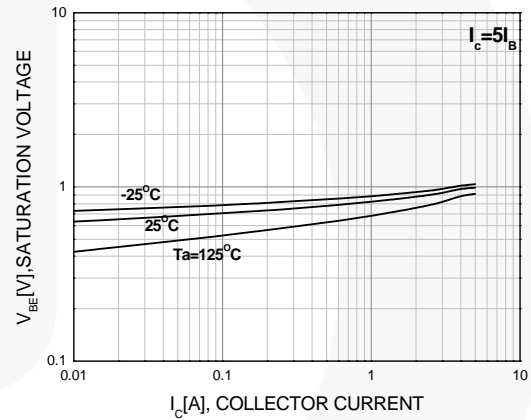


Figure 4. Base-Emitter Saturation Voltage

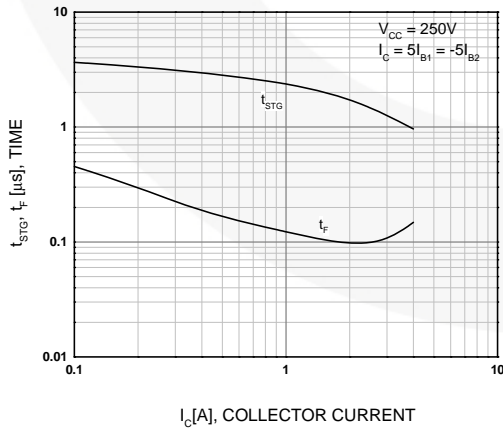


Figure 5. Resistive Load Switching Time

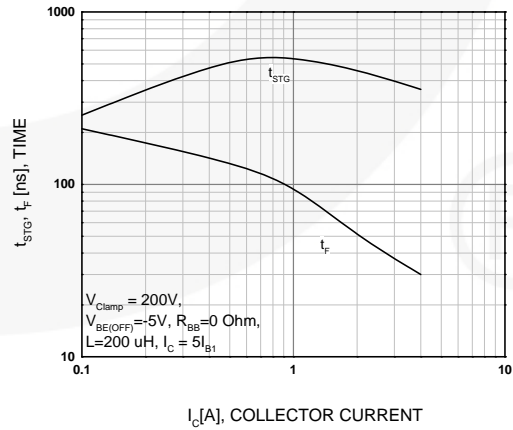


Figure 6. Inductive Load Switching Time

## Typical Performance Characteristics (Continued)

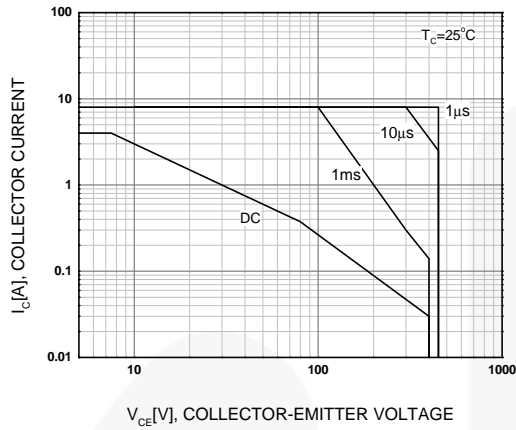


Figure 7. Forward Bias Safe Operating Area

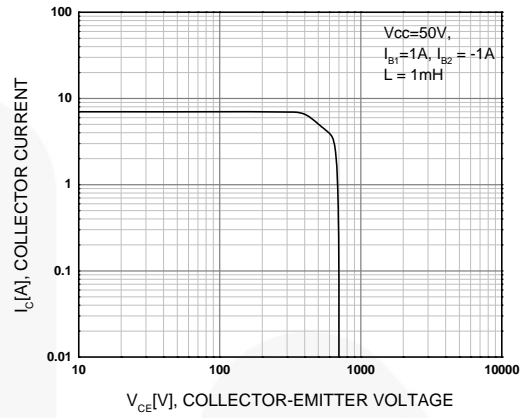


Figure 8. Reverse Bias Safe Operating Area

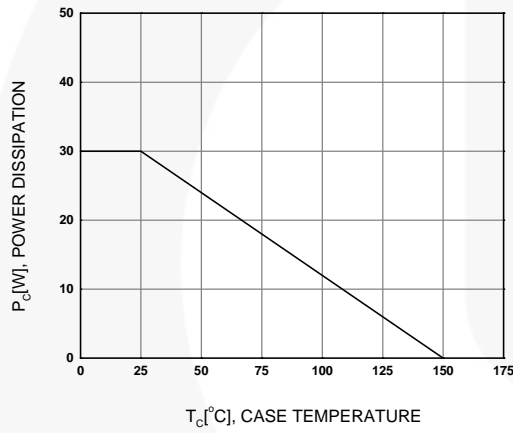
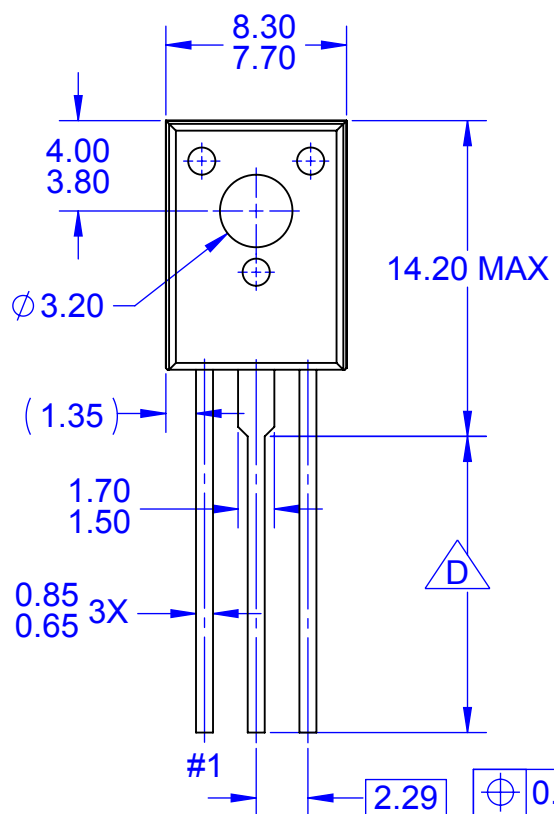
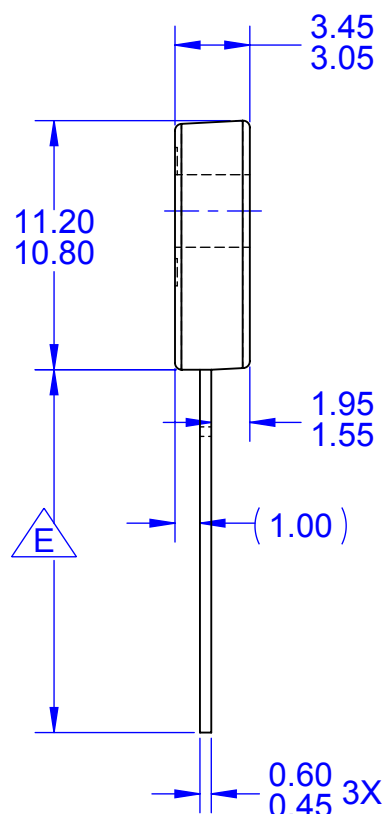


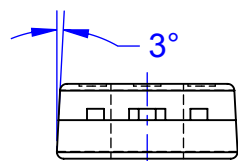
Figure 9. Power Derating



TOP VIEW



SIDE VIEW



FRONT VIEW

PRODUCTION CODE	TERMINAL LENGTH "D"	TERMINAL LENGTH "E"
TSSTU	3.45 - 4.05	6.45-7.45
TSTU	2.36 - 2.96	5.36-6.36
NONE (STD LENGTH)	12.76 - 13.36	15.76-16.76

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- A. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE
- B. ALL DIMENSIONS ARE IN MILLIMETERS
- C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR PROTRUSIONS

 FOR TERMINAL LENGTH "D", REFER TO TABLE

 FOR TERMINAL LENGTH "E", REFER TO TABLE

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