

ARK Home ☐

Intel® CV82524EFL Thunderbolt™ Controller

SPECIFICATIONS

Essentials

I/O Specifications

Package Specifications

ORDERING / SSPECS / STEPPINGS

SPECIFICATIONS	
Essentials	
Status	Launched
Launch Date	Q4'10
Embedded Options Available	No
Max TDP	3.2 W
Recommended Customer Price	
Description	4 channel CIO controller for Thunderbolt™ daisy chainable devices.
T.CASE.	110°C
I/O Specifications	
# of Thunderbolt™ Channels	4
# of Ports	Dual
PCI Express Configurations ‡	PCIe 2.0, 1x4 or 4x1
Display Port	DP 1.1a 2x Sink, 1x Source
Package Specifications	
Package Size	15mm x 15mm
Temperature Range	0°C - 65°C
Low Halogen Options Available	See MDDS

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Thunderbolt™ Products

Thunderbolt™ Controllers

Products formerly Light Ridge

QUICK LINKS

No Datasheet Available

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PCN/MDDS INFORMATION

SLHAJ

909815: [PCN](#) | [MDDS](#)

SLHAK

909816: [PCN](#) | [MDDS](#)

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"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability.

‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications (motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIOS, drivers, virtual machine monitor-VMM, platform software, and/or operating system) for feature compatibility. Functionality, performance, and other benefits of this feature may vary depending on system configuration.

your appropriate Intel representative to obtain a formal price quotation.

System and Maximum TDP is based on worst case scenarios. Actual TDP may be lower if not all I/Os for chipsets are used.

Low Halogen: Applies only to brominated and chlorinated flame retardants (BFRs/CFRs) and PVC in the final product. Intel components as well as purchased components on the finished assembly meet JS-709 requirements, and the PCB / substrate meet IEC 61249-2-21 requirements. The replacement of halogenated flame retardants and/or PVC may not be better for the environment.

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