Unit: mm

TOSHIBA Diode Silicon Epitaxial Planar Type

# HN2D01JE

#### **Ultra High Speed Switching Application**

The HN2D01JE is composed of 2 independent diodes.

Low forward voltage  $V_{F(3)} = 0.98V \text{ (typ.)}$ Fast reverse recovery time :  $t_{rr} = 1.6$ ns (typ.) Small total capacitance  $: C_T = 0.5pF (typ.)$ 

### Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse Voltage	$V_{RM}$	85	V	
Reverse voltage	V <sub>R</sub>	80	V	
Maximum (peak) forward current	I <sub>FM</sub>	200 *	mA	
Average forward current	Io	100 *	mA	
Surge current (10ms)	I <sub>FSM</sub>	1*	Α	
Power dissipation	Р	100 **	mW	
Junction temperature	Tj	150	°C	
Storage temperature	T <sub>stg</sub>	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

1.6±0.05 1.2±0.05  $0.12\pm0.05$ 1.ANODE1 2.NC 3.ANODE2 4.CATHODE2 5.CATHODE1 ESV **JEDEC** JEITA **TOSHIBA** 1-2W1B Weight: 0.003 mg (typ.)

temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

\*: Unit rating; total rating = unit rating × 1.5

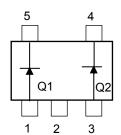
#### **Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V <sub>F (1)</sub>	_	I <sub>F</sub> = 1mA	_	0.62	_	
	V <sub>F (2)</sub>	_	I <sub>F</sub> = 10mA	_	0.75	_	V
	V <sub>F (3)</sub>	_	I <sub>F</sub> = 100mA	_	0.98	1.20	
Reverse current	I <sub>R (1)</sub>	_	V <sub>R</sub> = 30V	_	_	0.1	μА
	I <sub>R (2)</sub>	_	V <sub>R</sub> = 80V	_	_	0.5	
Total capacitance	C <sub>T</sub>	_	V <sub>R</sub> = 0, f = 1MHz	_	0.5	_	pF
Reverse recovery time	t <sub>rr</sub>	_	I <sub>F</sub> = 10mA, Fig.1	_	1.6	_	ns

Start of commercial production 2001-10

<sup>\*\*:</sup> Total rating.

# Pin Assignment (Top View)



## Marking

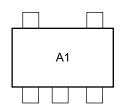
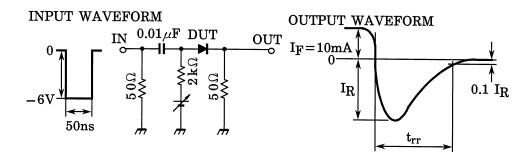
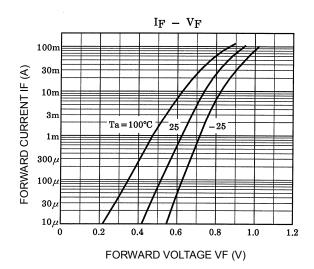
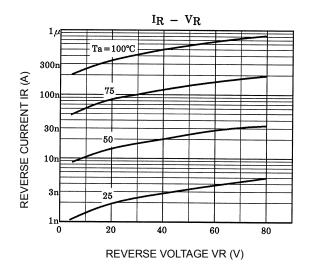
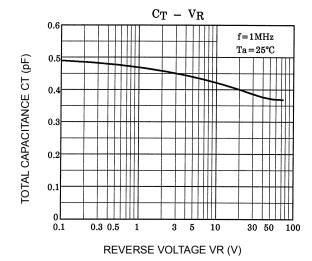


Fig. 1 Reverse Recovery Time (t<sub>rr</sub>) Test Circuit









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