#### Microchip Filter specification TFS500C 1/5

### **Measurement condition**

°C Ambient temperature: 23 Input power level: 0 dBm Terminating impedance: \*) Input: Output:

415  $\Omega$  || -1 pF 415  $\Omega$  || -1 pF External Coil: 33 nH

#### Characteristics

#### Remark:

The reference level for the relative attenuation  $a_{rel}$  of the TFS500C is the minimum of the pass band attenuation. This value is defined as the insertion loss  $a_e$ . The nominal frequency  $f_N$  is fixed at 500.0 MHz without any tolerance. The values of relative attenuation  $a_{rel}$  are guaranteed over the whole operating temperature range. The frequency shift of the filter within the operating temperature range is included in the production tolerance scheme.

Data		typ. value		tolerance / limit		
Insertion loss	a <sub>e</sub>	4.0	dB	max.	5.0	dB
(reference level)						
Nominal frequency	$f_N$				500.0	MHz
Passband	РВ	390	kHz	f <sub>N</sub> ±	0.1	MHz
Passband variation	PBV	1.3	dB	max.	2.0	dB
Relative attenuation	a <sub>rel</sub>					
f <sub>N</sub> - 100 MHz f <sub>N</sub> - 90	MHz	48	dB	min.	40	dB
f <sub>N</sub> - 90 MHz f <sub>N</sub> - 60	MHz	42	dB	min.	38	dB
f <sub>N</sub> - 60 MHz f <sub>N</sub> - 1	MHz	47	dB	min.	40	dB
$f_N$ + 1 MHz $f_N$ + 100	MHz	42	dB	min.	40	dB
Input power level		-		max.	15	dBm
Operating temperature range	OTR	-		-40 °C+85 °C		
Storage temperature range		-		-55 °C +125 °C		
Frequency inversion temperature	$T_{O}$	18	°C			
Temperature coefficient of frequency	<i>TC<sub>f</sub></i> **)	- 0.036	ppm/K²			

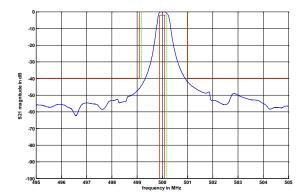
<sup>\*)</sup> The terminating impedances depend on parasitics and q-values of matching elements and the board used, and are to be understood as reference values only. Should there be additional questions do not hesitate to ask for an application note or contact our design team. \*\*)  $\Delta f = TC_f(T - T_0)^2 f_N$ 

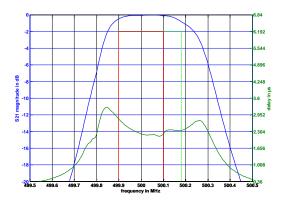
Generated:		
Checked / Approved:		

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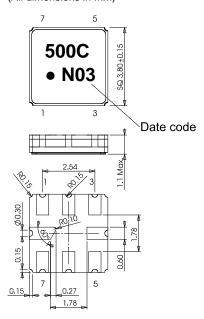
## Filter characteristic





# Construction and pin connection

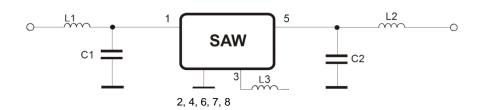
(All dimensions in mm)



1	Input
2	Ground
3	External Coil
4	Ground
5	Output
6	Ground
7	Ground
8	Ground

Date code: Year + week N 2021 P 2022 R 2023 ....

# 50 Ω Test circuit



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# Stability characteristics, reliability

After the following tests the filter shall meet the whole specification:

1. Shock: 500 g, 1 ms, half sine wave, 3 shocks each plane;

DIN IEC 60068 T2 - 27

2. Vibration: 10 Hz to 2000 Hz, 0.35 mm or 5 g respectively, 1 octave per min, 10 cycles per

plane, 3 planes; DIN IEC 60068 T2 - 6

3. Change of

temperature: -55 °C to 125 °C / 15 min. each / 100 cycles

DIN IEC 60068 part 2 - 14 Test N

4. Resistance to

solder heat (reflow): reflow possible: three times max.;

for temperature conditions refer to the attached "Air reflow temperature conditions" on page 4;

5. SAW devices are Electrostatic Discharge (ESD) sensitive devices.

This filter is RoHS compliant (2011/65/EU+2015/863/EU)

## **Packing**

Tape & Reel: IEC 286 – 3, with exception of value for N and minimum bending radius;

tape type II, embossed carrier tape with top cover tape on the upper side;

reel of empty components at start:

reel of empty components at start including leader:

min. 300 mm

min. 500 mm

trailer:

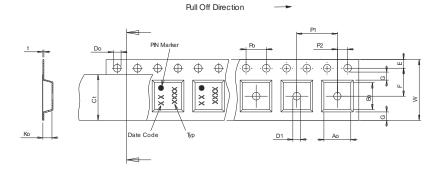
min. 300 mm

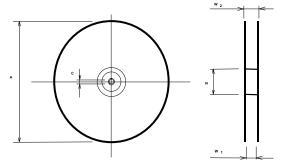
Tape (all dimensions in mm)

12.00 ±0.3 Ро 4.00 ±0.1 Do 1.50 +0.1/-0 Ε 1.75 + 0.15.50 ±0.05 G(min) 0.75 P2 2.00 ±0.05 8.00 ±0.1 D1(min) 1.50 Αo 4.30 ±0.1 Во 4.30 ±0.1 92 Ct +0.1 1.80 ±0.1 Ko 0.30 ±0.05

Reel (all dimensions in mm)
A :330 or 180
W1 : 12.4 +2/-0
W2(max) : 18.40
N(min) : 50.00

N(min) : 50.00 C : 13.0 +0.5/-0.2





The minimum bending radius is 45 mm.

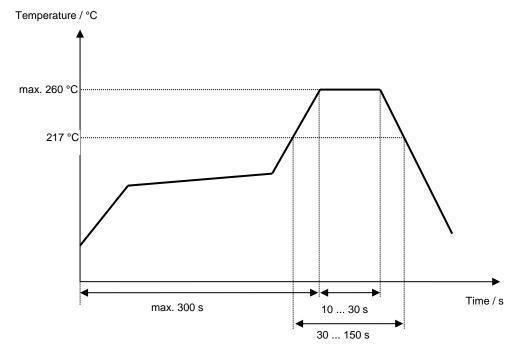
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Microchip Filter specification TFS500C 4/5

# Air reflow temperature conditions

Conditions	Exposure
Average ramp-up rate (30 °C to 217 °C)	less than 3 °C / second
> 100 °C	between 300 and 600 seconds
> 150 °C	between 240 and 500 seconds
> 217 °C	between 30 and 150 seconds
Peak temperature	max. 260 °C
Time within 5 °C of actual peak temperature	between 10 and 30 seconds
Cool-down rate (Peak to 50 °C)	less than 6 °C / second
Time from 30 °C to Peak temperature	no greater than 300 seconds

# Chip-mount air reflow profile



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History					
Version	Reason of Changes		Name	Date	
1.0	- Generation of development specific	ation	Bonnen	17.06.2014	
2.0	- Change to Filter specification - Change frequency inversion temper	rature	Bonnen	29.07.2014	
2.1	- Change storage temperature range		Bonnen	30.08.2016	
2.2	- Change Date code: Year + week		Bonnen	14.09.2016	
2.3	<ul><li>Changed input power level</li><li>Updated construction</li><li>Updated stability characteristics, rel</li></ul>	iability	P. Jaster	21.01.2021	

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