R5F109LAJFB

User's Manual: Hardware

Product Info | Design Support | Documentation | Sample Code | Software & Tools | Ordering

Product Spec

Basic Information Memory CPU (Bit, Clock, DMA, External Bus etc) Timers Analog Interfaces Operating Conditions Remarks Package

Basic Information	
Product Status	Mass Production
Family	RL78
Series	RL78/F1x
Group	RL78/F12

Memory	
Program memory	16 KB Flash memory
RAM	1 KB
Data Flash	4 KB

CPU (Bit, Clock, DMA, External Bus etc)	
CPU	RL78 (16-bit)
Max. Frequency	32 MHz
Sub-Clock (32.768 kHz)	YES
On-chip Oscillator Freq.	32M, 24M, 16M, 12M, 8M, 4M, 1M Low-speed Oscillator 15kHz
Real-Time Clock (RTC)	YES
Power-On Reset	YES
Low Voltage Detection	YES
DMA Remarks	DMAC x 2 ch
External Interrupt Pins	12
I/O Ports	58

Timers	
16-bit Timers	8 ch

Downloaded from Arrow.com.

Watchdog Timers	1 ch
Other Timers	Interval Timer x 1 ch, Wakeup Timer x 1ch
PWM Output	4

Analog

A/D Converters	10-bit x 12 ch

Interfaces

CSIs	8 ch
UARTs	5 ch
I2Cs	6 ch
LIN	2 ch

Operating Conditions

Operating Voltage	1.8 to 5.5 V
Power Supply	VDD=1.8-5.5V
Operating Temperature	-40 to 85 ℃ (Ambient Temperature)

Remarks

Remarks	Automotive

Package

Pins	64
Terminal pitch (mm)	0.5 mm
Package Type	LFQFP
Dimensions (mm)	10x10 mm^2
Drawing Link	p64gb-50-ueu
Mount pad	fig0009e
Mounting height (mm)[MAX]	1.6 mm
Mass (g) [TYP,]	0.35 g
Renesas code	PLQP0064KF-A (old: P64GB-50-UEU-2)
JEITA code	P-LFQFP64-10x10-0.50
Remarks	LQFP(matrix line)
Packing	Embossed Tape
Terminal material - Base	Cu alloy
Terminal material - Surface	Sn-Bi or Sn

Find similar products

Downloaded from Arrow.com.

Need More Help ?

Contact Us
Website Feedback
Distributors
Videos
Forums
Newsletter

Others

Mobile App
Privacy Policy
Legal
Sitemap

Follow Us

Facebook
Twitter
YouTube

© 2010-2016 Renesas Electronics Corporation. All rights reserved.

Downloaded from Arrow.com.