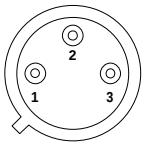
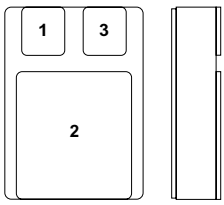
IP140MA SERIES
IP140M SERIES
IP78M00A SERIES
IP78M00 SERIES



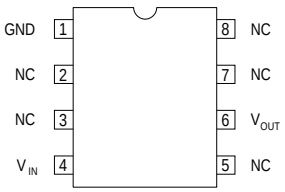
Pin 1 – V_{IN}
Pin 2 – V_{OUT}
Case – Ground

H Package – TO–39



Pin 1 – V_{IN}
Pin 2 – Ground
Pin 3 – V_{OUT}

SMD 1
CERAMIC SURFACE MOUNT



8 Pin J Package

0.5 AMP POSITIVE VOLTAGE REGULATOR

FEATURES

- **OUTPUT CURRENT UP TO 0.5A**
- **OUTPUT VOLTAGES OF 5, 12, 15V**
- **0.01% / V LINE REGULATION**
- **0.3% / A LOAD REGULATION**
- **THERMAL OVERLOAD PROTECTION**
- **SHORT CIRCUIT PROTECTION**
- **OUTPUT TRANSISTOR SOA PROTECTION**
- **1% VOLTAGE TOLERANCE (–A VERSIONS)**

Order Information

Part Number	H–Pack (TO–39)	J–Pack CERDIP	SG–Pack SMD	Temp. Range
IP78MxxAzz	✔	✔	✔	-55 to +150°C
IP78Mxxzz	✔	✔	✔	”
IP140MAzz–xx	✔		✔	”
IP140Mzz–xx	✔		✔	”

Note:

xx = Voltage Code (05, 12, 15)
zz = Package Code (H, J, SG)

eg.
IP78M05J
IP140MAH–12

DESCRIPTION

The IP140MA and IP78M00A series of voltage regulators are fixed output regulators intended for local, on-card voltage regulation. These devices are available in 5, 12, and 15 volt options and are capable of delivering in excess of 500mA over temperature.

The A-suffix devices are fully specified at 0.5A, provide 0.01% / V line regulation, 0.3% / A load regulation, and ±1% output voltage tolerance at room temperature. Protection features include safe operating area, current limiting and thermal shutdown.

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise stated)

V_I	DC Input Voltage	(for V_O = 5, 12, 15V)	35V
P_D	Power Dissipation		Internally limited [1]
$R_{\theta JC}$	Thermal Resistance Junction to Case	– H Package	20°C / W
$R_{\theta JC}$	Thermal Resistance Junction to Case	– SG Package	TBA °C / W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	– J Package	119°C / W
T_J	Operating Junction Temperature Range		–55 to 150°C
T_{stg}	Storage Temperature		–65 to 150°C

Note 1. Although power dissipation is internally limited, these specifications are applicable for maximum power dissipation P_{MAX} of 2W for the H–Package ,1.05W for the J–Package and 15W for the SG–Package.

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ELECTRICAL CHARACTERISTICS

Parameter		Test Conditions		IP78M05A IP140MA–05 Min.	Typ.	Max.	IP78M05 IP140M–05 Min.	Typ.	Max.	Units
V_O	Output Voltage	I_O = 100mA	V_{IN} = 10V	4.95	5	5.05	4.8	5	5.2	V
		I_O = 5mA to 350mA $P_D \le P_{MAX}$ V_{IN} = 7.5V to 20V	T_J = -55 to 150°C	4.85		5.15	4.75		5.25	
ΔV_O	Line Regulation	I_O = 200mA	V_{IN} = 7V to 25V		3	10			50	mV
			V_{IN} = 8V to 25V T_J = -55 to 150°C		3	10			25	
		I_O = 500mA	V_{IN} = 8V to 12V		3	10			50	
ΔV_O	Load Regulation	I_O = 5mA to 500mA V_{IN} = 10V	T_J = -55 to 150°C		5	50			50	mV
I_Q	Quiescent Current	V_{IN} = 10V	I_O = 350mA T_J = -55 to 150°C		4	6		4	6	mA
ΔI_Q	Quiescent Current Change	I_O = 5mA to 500mA V_{IN} = 10V	T_J = -55 to 150°C		0.1	0.5			0.5	mA
		I_O = 200mA	V_{IN} = 8V to 25V T_J = -55 to 150°C		0.2	0.8			0.8	
V_N	Output Noise Voltage	f = 10Hz to 100kHz			40	200		40	200	µV
$\frac{\Delta V_{IN}}{\Delta V_O}$	Ripple Rejection	f = 120Hz V_{IN} = 8V to 18V	I_O = 300mA	65	80		62			dB
			I_O = 100mA T_J = -55 to 150°C	65	80		62			
	Dropout Voltage	I_O = 350mA			2	2.5			2.5	V
I_{sc}	Short Circuit Current	V_{IN} = 35V			300	600		300	600	mA
I_{pk}	Peak Output Current	V_{IN} = 10V		0.7	1.0	1.4	0.7	1.0	1.6	A
Average Temperature Coefficient of V_O		I_O = 5mA			0.5	2.0		0.5		mV/°C

1) All characteristics are measured with a capacitor across the input of 0.22µF and a capacitor across the output of 0.1µF.

All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques ($t_p \le 10$ms, $\delta \le 5\%$). Output voltage changes due to changes in internal temperature must be taken into account separately.

2) Test Conditions unless otherwise stated: T_J = 25°C
P_{MAX} = 2W for H Package (TO–39)
P_{MAX} = 1.05W for J Package (CERDIP)
P_{MAX} = 15W for SG Package (SMD1)

ELECTRICAL CHARACTERISTICS

Parameter		Test Conditions		IP78M12A IP140MA–12 Min.	Typ.	Max.	IP78M12 IP140M–12 Min.	Typ.	Max.	Units
V_O	Output Voltage	I_O = 100mA	V_{IN} = 19V	11.88	12	12.12	11.50	12	12.50	V
		I_O = 5mA to 350mA $P_D \le P_{MAX}$ T_J = -55 to 150°C	V_{IN} = 14.8V to 27V	11.64		12.36	11.40		12.60	
ΔV_O	Line Regulation	I_O = 200mA	V_{IN} = 14.5V to 30V		4	18			60	mV
			V_{IN} = 16V to 30V T_J = -55 to 150°C		4	18			30	
		I_O = 500mA	V_{IN} = 16V to 22V		4	18			120	
ΔV_O	Load Regulation	I_O = 5mA to 500mA V_{IN} = 19V	T_J = -55 to 150°C		10	60			120	mV
I_Q	Quiescent Current	V_{IN} = 19V	I_O = 350mA T_J = -55 to 150°C		4	6		4	6	mA
ΔI_Q	Quiescent Current Change	I_O = 5mA to 500mA V_{IN} = 19V	T_J = -55 to 150°C		0.1	0.5			0.5	mA
		I_O = 200mA	V_{IN} = 14.8V to 30V T_J = -55 to 150°C		0.2	0.8			0.8	
V_N	Output Noise Voltage	f = 10Hz to 100kHz			75	480		75	480	µV
$\frac{\Delta V_{IN}}{\Delta V_O}$	Ripple Rejection	f = 120Hz V_{IN} = 15V to 25V	I_O = 300mA	58	72		55			dB
			I_O = 100mA T_J = -55 to 150°C	58	72		55			
	Dropout Voltage	I_O = 350mA			2	2.5			2.5	V
I_{sc}	Short Circuit Current	V_{IN} = 35V			300	600		300	600	mA
I_{pk}	Peak Output Current	V_{IN} = 19V		0.7	1.0	1.4	0.7	1.0	1.6	A
Average Temperature Coefficient of V_O		I_O = 5mA			1.2	4.8		1.2		mV/°C

1) All characteristics are measured with a capacitor across the input of 0.22µF and a capacitor across the output of 0.1µF.

All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques ($t_p \le 10ms$, $\delta \le 5\%$). Output voltage changes due to changes in internal temperature must be taken into account separately.

2) Test Conditions unless otherwise stated: T_J = 25°C
P_{MAX} = 2W for H Package (TO–39)
P_{MAX} = 1.05W for J Package (CERDIP)
P_{MAX} = 15W for SG Package (SMD1)

ELECTRICAL CHARACTERISTICS

Parameter		Test Conditions		IP78M15A IP140MA–15 Min.	Typ.	Max.	IP78M15 IP140M–15 Min.	Typ.	Max.	Units
V_O	Output Voltage	I_O = 100mA	V_{IN} = 23V	14.85	15	15.15	14.40	15	15.60	V
		I_O = 5mA to 350mA $P_D \le P_{MAX}$ V_{IN} = 18V to 30V	T_J = -55 to 150°C	14.55		15.45	14.25		15.75	
ΔV_O	Line Regulation	I_O = 200mA	V_{IN} = 17.5V to 30V		4	22			60	mV
			V_{IN} = 20V to 30V T_J = -55 to 150°C		4	22			30	
		I_O = 500mA	V_{IN} = 20V to 26V		4	22			150	
ΔV_O	Load Regulation	I_O = 5mA to 500mA V_{IN} = 23V	T_J = -55 to 150°C		12	75			150	mV
I_Q	Quiescent Current	V_{IN} = 23V	I_O = 350mA T_J = -55 to 150°C		4	6		4	6	mA
ΔI_Q	Quiescent Current Change	I_O = 5mA to 500mA V_{IN} = 23V	T_J = -55 to 150°C		0.1	0.5			0.5	mA
		I_O = 200mA	V_{IN} = 18V to 30V T_J = -55 to 150°C		0.2	0.8			0.8	
V_N	Output Noise Voltage	f = 10Hz to 100kHz			90	600		90	600	µV
$\frac{\Delta V_{IN}}{\Delta V_O}$	Ripple Rejection	f = 120Hz V_{IN} = 18.5V to 28.5V	I_O = 300mA	57	70		54			dB
			I_O = 100mA T_J = -55 to 150°C	57	70		54			
	Dropout Voltage	I_O = 350mA			2	2.5			2.5	V
I_{sc}	Short Circuit Current	V_{IN} = 35V			300	600		300	600	mA
I_{pk}	Peak Output Current	V_{IN} = 23V		0.7	1.0	1.4	0.7	1.0	1.6	A
Average Temperature Coefficient of V_O		I_O = 5mA			1.5	6.0		1.5		mV/°C

1) All characteristics are measured with a capacitor across the input of 0.22µF and a capacitor across the output of 0.1µF.

All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques ($t_p \le 10$ms, $\delta \le 5\%$). Output voltage changes due to changes in internal temperature must be taken into account separately.

2) Test Conditions unless otherwise stated: T_J = 25°C

P_{MAX} = 2W for H Package (TO–39)

P_{MAX} = 1.05W for J Package (CERDIP)

P_{MAX} = 15W for SG Package (SMD1)