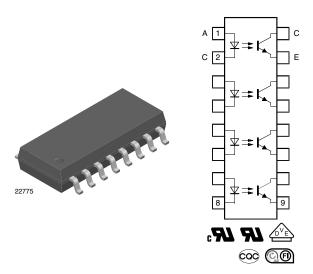


Vishay Semiconductors

Optocoupler, Phototransistor Output, Quad Channel, Half Pitch Mini-Flat Package



DESCRIPTION

The TCMT410. series consist of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 16 pin (quad channel) package.

FEATURES

- Low profile package (half pitch)
- AC isolation test voltage 3750 V_{RMS}
- · Low coupling capacitance of typical 0.3 pF
- Current transfer ratio (CTR) selected into groups
- · Low temperature coefficient of CTR
- Wide ambient temperature range
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





ROHS COMPLIANT HALOGEN

GREEN (5-2008)

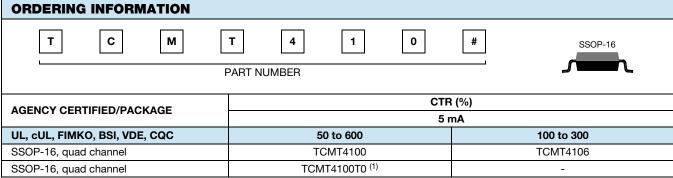
APPLICATIONS

- Programmable logic controllers
- Modems
- Answering machines
- · General applications

AGENCY APPROVALS

Safety application model number covering all products in this datasheet is TCMT4100. This model number should be used when consulting safety agency documents.

- UL1577, file no. E76222, double protection
- cUL, accordance to CSA component acceptance service no. 5A, double protection
- DIN EN 60747-5-5 (VDE 0884-5)
- FIMKO EN 60950-1
- CQC GB4943.1-2011 and GB8898-2011 (suitable for installation altitude below 2000 m)



Notes

- · Available only on tape and reel.
- (1) Product is rotated 180° in tape and reel cavity.



Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Reverse voltage		V_{R}	6	V		
Forward current		I _F	60	mA		
Forward surge current	t _p ≤ 10 μs	I _{FSM}	1.5	Α		
Power dissipation		P _{diss}	100	mW		
Junction temperature		Tj	125	°C		
OUTPUT						
Collector emitter voltage		V _{CEO}	70	V		
Emitter collector voltage		V _{ECO}	7	V		
Collector current		I _C	50	mA		
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA		
Power dissipation		P _{diss}	150	mW		
Junction temperature		Tj	125	°C		
COUPLER						
AC isolation test voltage (RMS)	Related to standard climate 23/50 DIN 50014	V _{ISO}	3750	V _{RMS}		
Total power dissipation per channel		P _{tot}	250	mW		
Operating ambient temperature range		T _{amb}	-40 to +100	°C		
Storage temperature range		T _{stg}	-40 to +125	°C		
Soldering temperature (1)		T _{sld}	260	°C		

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices. Also refer to "Assembly Instructions" (<u>www.vishay.com/doc?80054</u>).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	$I_F = 50 \text{ mA}$	V _F	-	1.35	1.6	V	
Junction capacitance	$V_R = 0$, $f = 1 MHz$	C _j	-	8	-	pF	
OUTPUT							
Collector emitter voltage	I _C = 100 μA	V_{CEO}	70	-	-	V	
Emitter collector voltage	I _E = 100 μA	V_{ECO}	7	-	-	V	
Collector dark current	$V_{CE} = 20 \text{ V}, I_F = 0 \text{ A}$	I _{CEO}	=	-	100	nA	
COUPLER							
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V _{CEsat}	-	-	0.3	V	
Cut-off frequency	V_{CE} = 5 V, I_F = 10 mA, R_L = 100 Ω	f _c	-	100	-	kHz	
Coupling capacitance	f = 1 MHz	C _k	-	0.3	-	pF	

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I _C /I _F	V - 5 V I - 5 m A	TCMT4100	CTR	50	-	600	%
	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCMT4106	CTR	100	-	300	%



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SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t _d	-	4	-	μs
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t _r	-	5.5	-	μs
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t _f	-	7.0	-	μs
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t _s	-	1.5	-	μs
Turn-on time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t _{on}	-	9.5	-	μs
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t _{off}	-	8.5	-	μs
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega,$ (see figure 2)	t _{on}	-	3	-	μs
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega,$ (see figure 2)	t _{off}	-	20	-	μs

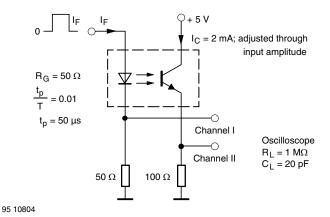


Fig. 1 - Test Circuit, Non-Saturated Operation

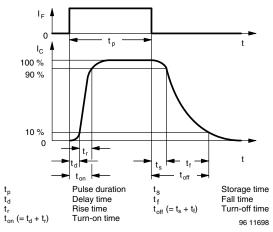


Fig. 3 - Switching Times

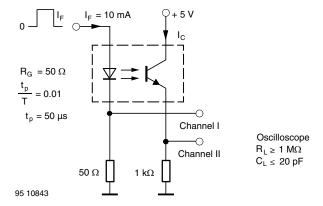


Fig. 2 - Test Circuit, Saturated Operation



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SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Climatic classification	According to IEC 68 part 1		40 / 110 / 21			
Comparative tracking index		CTI	175			
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	3750	V _{RMS}		
Maximum transient isolation voltage		V _{IOTM}	6000	V		
Maximum repetitive peak isolation voltage		V _{IORM}	707	V		
Apparent charge test voltage (method A)	V_{IORM} x 1.6 = V_{PR} , type and sample test, t _m = 60 s, partial discharge < 5 pC	V _{PR}	1132	V _{peak}		
Apparent charge test voltage (method B)	V_{IORM} x 1.875 = V_{PR} , 100 % production test with t_m = 1 s, partial discharge < 5 pC	V_{PR}	1326	V _{peak}		
Isolation resistance	V _{IO} = 500 V _{DC} , T _{amb} = 100 °C	R _{IO}	10 ¹¹	Ω		
Isolation resistance (under fault conditions)	$V_{IO} = 500 V_{DC}, T_{amb} = T_{SI}$	R _{IO}	10 ⁹	Ω		
Output safety power		Pso	265	mW		
Input safety current		I _{SI}	130	mA		
Input safety temperature		T _{SI}	150	°C		
Creepage distance			≥ 5	mm		
Clearance distance			≥ 5	mm		
Insulation thickness, reinforced rated	Per IEC 60950 2.10.5.1	DTI	≥ 0.4	mm		

Note

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

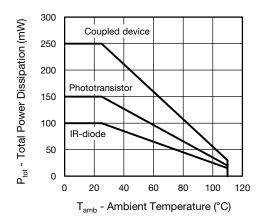


Fig. 4 - Total Power Dissipation vs. Ambient Temperature

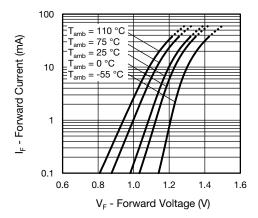


Fig. 5 - Forward Voltage vs. Forward Current

[•] As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



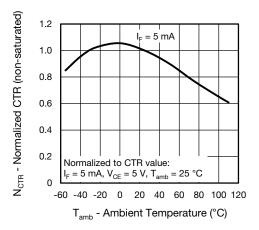


Fig. 6 - Normalized Current Transfer Ratio (non-saturated) vs.

Ambient Temperature

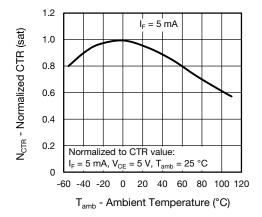


Fig. 7 - Normalized Current Transfer Ratio (saturated) vs.
Ambient Temperature

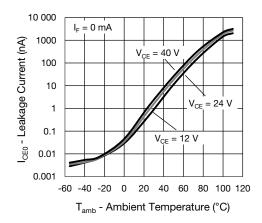


Fig. 8 - Collector Dark Current vs. Ambient Temperature

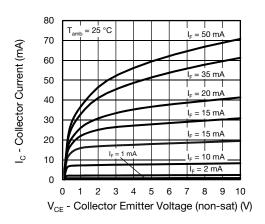


Fig. 9 - Collector Current vs. Collector Emitter Voltage (non-saturated)

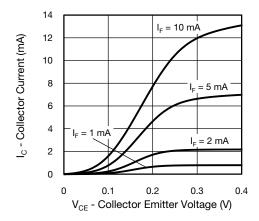


Fig. 10 - Collector Current vs. Collector Emitter Voltage (saturated)

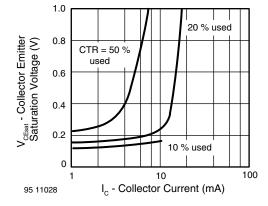


Fig. 11 - Collector Emitter Saturated Voltage vs. Collector Current



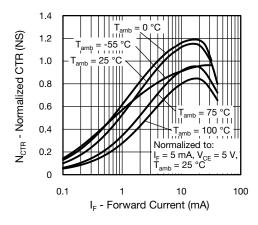


Fig. 12 - Normalized CTR (non-saturated) vs. Forward Current

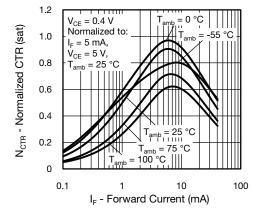


Fig. 13 - Normalized CTR (saturated) vs. Forward Current

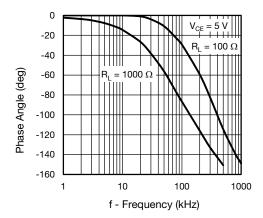


Fig. 14 - Phase Angle vs. Frequency

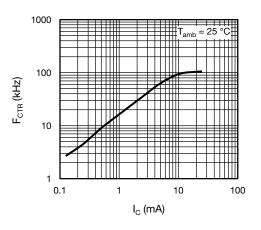


Fig. 15 - F_{CTR} vs. Collector Current

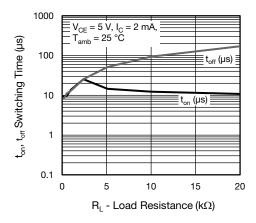


Fig. 16 - Switching Time vs. Load Resistance

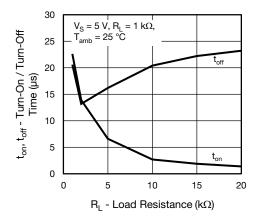


Fig. 17 - Turn-On / Turn-Off Time vs. Load Resistance

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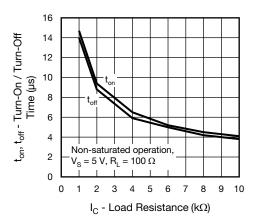
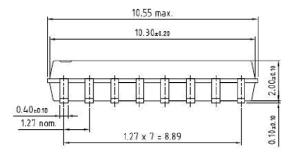
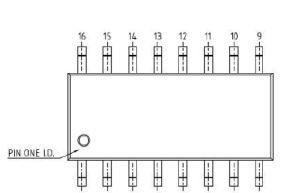
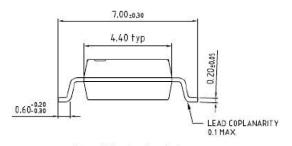


Fig. 18 - Switching Time vs. Load Resistance

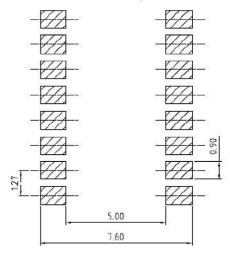
PACKAGE DIMENSIONS (in millimeters)



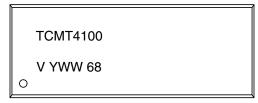




Possible footprint



PACKAGE MARKING (example)



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TAPE AND REEL PACKAGING FOR TCMT410X SERIES (in millimeters)

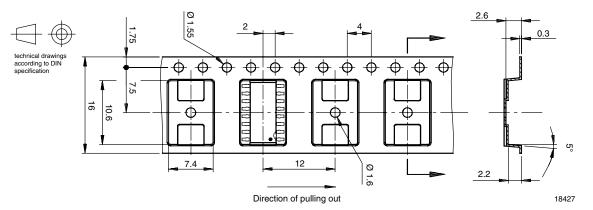


Fig. 19 - 2000 pcs/reel

TAPE AND REEL PACKAGING FOR TCMT410XT0 SERIES (in millimeters)

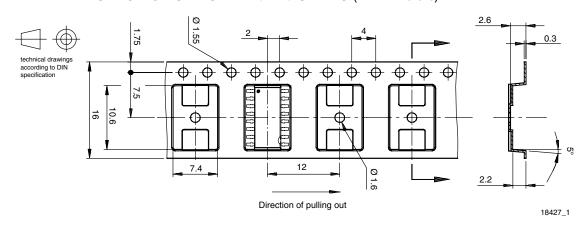


Fig. 20 - 2000 pcs/reel

SOLDER PROFILE

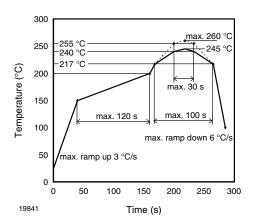


Fig. 21 - Lead (Pb)-free Reflow Solder Profile according to J-STD-020

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2

Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020

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