

Product Summary

| BV_{DSS} | $R_{DS(ON)} \text{ Max}$ | $I_D \text{ Max}$ $T_A = +25^\circ\text{C}$ |
|------------|---------------------------------------|--|
| 60V | 16m Ω @ $V_{GS} = 10\text{V}$ | 9.2A |
| | 21m Ω @ $V_{GS} = 4.5\text{V}$ | 7.5A |

Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$), maintain superior switching performance, making it ideal for high efficiency power management applications.

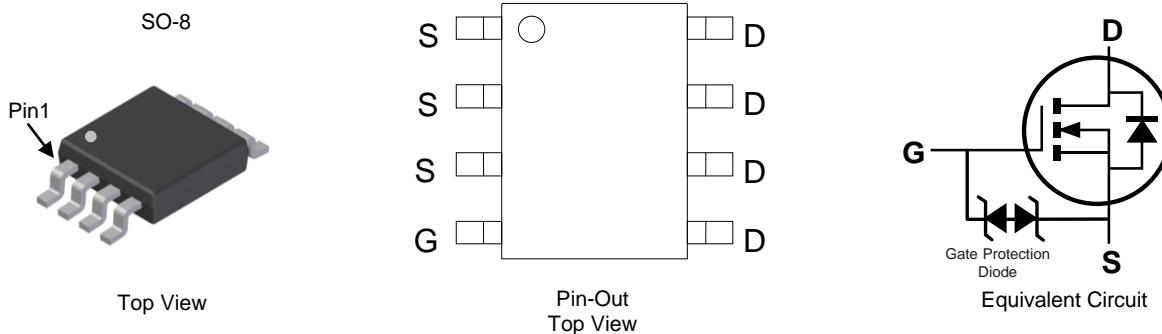
- Load Switch
- Adaptor Switch
- Notebook PC

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.076 grams (Approximate)



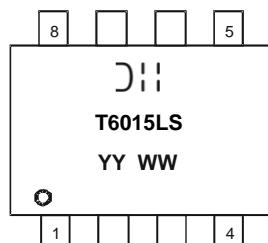
Ordering Information (Note 4)

| Part Number | Case | Packaging |
|---------------|------|-------------------|
| DMT6015LSS-13 | SO-8 | 2,500/Tape & Reel |

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



○ = Manufacturer's Marking
 T6015LS = Product Type Marking Code
 YYWW = Date Code Marking
 YY or YY = Year (ex: 16 = 2016)
 WW = Week (01 - 53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

| Characteristic | | | Symbol | Value | Units |
|--|------------------|--|-----------|-------------|-------|
| Drain-Source Voltage | | | V_{DSS} | 60 | V |
| Gate-Source Voltage | | | V_{GSS} | ± 16 | V |
| Continuous Drain Current (Note 6) $V_{GS} = 10\text{V}$ | Steady State | $T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$ | I_D | 9.2 7.4 | A |
| | $t < 10\text{s}$ | $T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$ | I_D | 11.9 9.5 | A |
| Continuous Drain Current (Note 6) $V_{GS} = 4.5\text{V}$ | Steady State | $T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$ | I_D | 7.5 6.0 | A |
| | $t < 10\text{s}$ | $T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$ | I_D | 9.7 7.7 | A |
| Pulsed Drain Current (10 μs Pulse, Duty Cycle = 1%) | | | I_{DM} | 60 | A |
| Maximum Continuous Body Diode Forward Current (Note 6) | | | I_S | 2 | A |
| Avalanche Current, $L = 0.1\text{mH}$ | | | I_{AS} | 15 | A |
| Avalanche Energy, $L = 0.1\text{mH}$ | | | E_{AS} | 11 | mJ |

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

| Characteristic | | Symbol | Value | Units |
|--|------------------|-----------------|-------------|--------------------|
| Total Power Dissipation (Note 5) | | P_D | 1.5 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | Steady State | $R_{\theta JA}$ | 85 | $^\circ\text{C/W}$ |
| | $t < 10\text{s}$ | | 45 | $^\circ\text{C/W}$ |
| Total Power Dissipation (Note 6) | | P_D | 2.1 | W |
| Thermal Resistance, Junction to Ambient (Note 6) | Steady State | $R_{\theta JA}$ | 74 | $^\circ\text{C/W}$ |
| | $t < 10\text{s}$ | | 37 | $^\circ\text{C/W}$ |
| Thermal Resistance, Junction to Case | | $R_{\theta JC}$ | 13 | $^\circ\text{C/W}$ |
| Operating and Storage Temperature Range | | T_J, T_{STG} | -55 to +150 | $^\circ\text{C}$ |

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|--|--------------|-----|-------|----------|------------------|---|
| OFF CHARACTERISTICS (Note 7) | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | 60 | — | — | V | $V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$ |
| Zero Gate Voltage Drain Current | I_{DSS} | — | — | 1 | μA | $V_{DS} = 48\text{V}, V_{GS} = 0\text{V}$ |
| Gate-Source Leakage | I_{GSS} | — | — | ± 10 | μA | $V_{GS} = \pm 16\text{V}, V_{DS} = 0\text{V}$ |
| ON CHARACTERISTICS (Note 7) | | | | | | |
| Gate Threshold Voltage | $V_{GS(TH)}$ | 0.5 | — | 2.5 | V | $V_{DS} = V_{GS}, I_D = 250\mu\text{A}$ |
| Static Drain-Source On-Resistance | $R_{DS(ON)}$ | — | 12.4 | 16 | $\text{m}\Omega$ | $V_{GS} = 10\text{V}, I_D = 10\text{A}$ |
| | | — | 15.8 | 21 | | $V_{GS} = 4.5\text{V}, I_D = 6\text{A}$ |
| Diode Forward Voltage | V_{SD} | — | 0.7 | 1.2 | V | $V_{GS} = 0\text{V}, I_S = 1\text{A}$ |
| DYNAMIC CHARACTERISTICS (Note 8) | | | | | | |
| Input Capacitance | C_{ISS} | — | 1,103 | — | pF | $V_{DS} = 30\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$ |
| Output Capacitance | C_{OSS} | — | 251 | — | | |
| Reverse Transfer Capacitance | C_{RSS} | — | 20 | — | | |
| Gate Resistance | R_G | — | 1.5 | — | Ω | $V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$ |
| Total Gate Charge ($V_{GS} = 4.5\text{V}$) | Q_G | — | 8.9 | — | nC | $V_{DS} = 30\text{V}, I_D = 10\text{A}$ |
| Total Gate Charge ($V_{GS} = 10\text{V}$) | Q_G | — | 18.9 | — | | |
| Gate-Source Charge | Q_{GS} | — | 3.0 | — | | |
| Gate-Drain Charge | Q_{GD} | — | 2.8 | — | | |
| Turn-On Delay Time | $t_{D(ON)}$ | — | 4.1 | — | ns | $V_{GS} = 10\text{V}, V_{DS} = 30\text{V}, R_G = 6\Omega, I_D = 10\text{A}$ |
| Turn-On Rise Time | t_R | — | 7.1 | — | | |
| Turn-Off Delay Time | $t_{D(OFF)}$ | — | 19.5 | — | | |
| Turn-Off Fall Time | t_F | — | 8.6 | — | | |
| Reverse Recovery Time | T_{RR} | — | 21.2 | — | ns | $I_F = 10\text{A}, di/dt = 100\text{A}/\mu\text{s}$ |
| Reverse Recovery Charge | Q_{RR} | — | 13.2 | — | nC | |

Notes:

5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

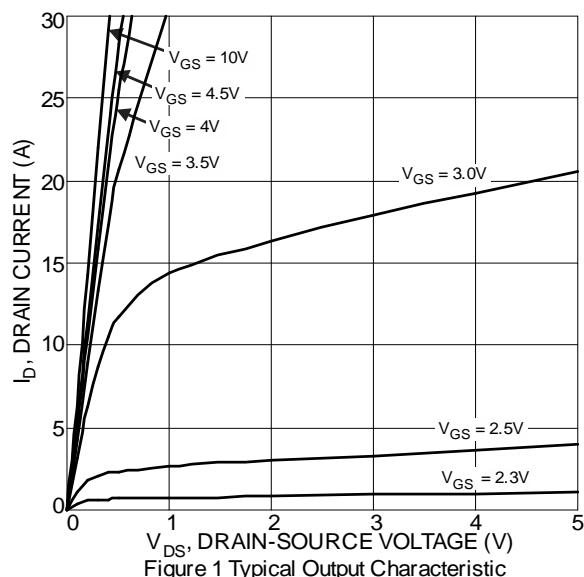


Figure 1 Typical Output Characteristic

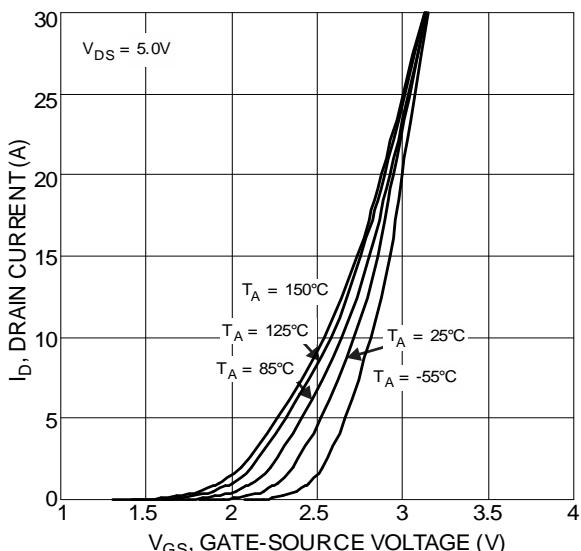


Figure 2 Typical Transfer Characteristics

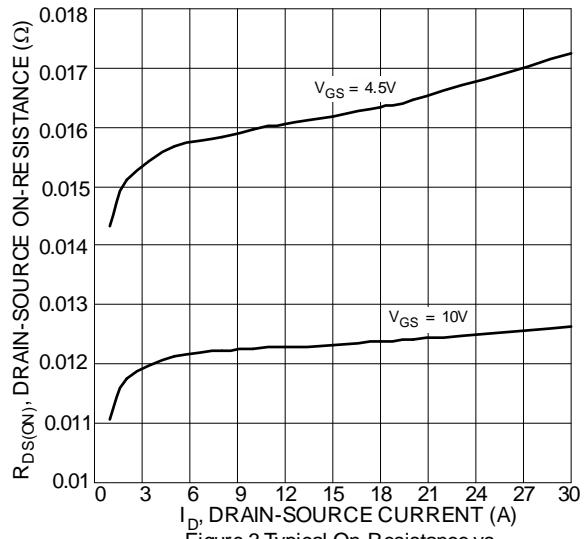


Figure 3 Typical On-Resistance vs.
Drain Current and Gate Voltage

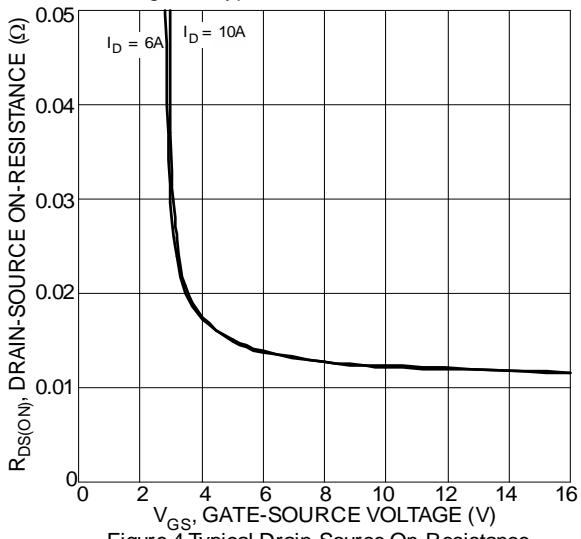


Figure 4 Typical Drain-Source On-Resistance
vs. Gate-Source Voltage

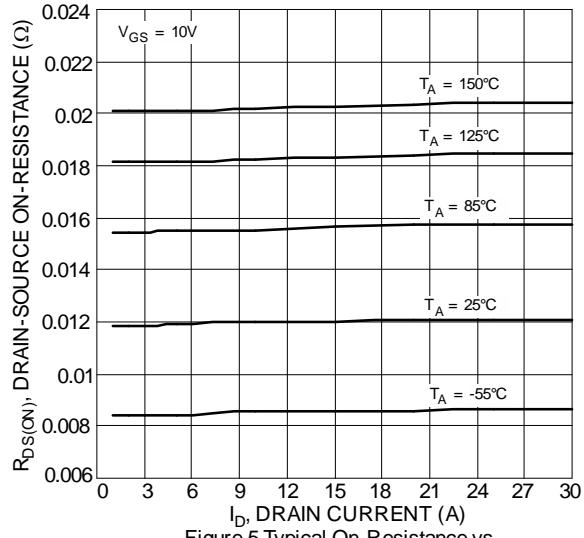


Figure 5 Typical On-Resistance vs.
Drain Current and Temperature

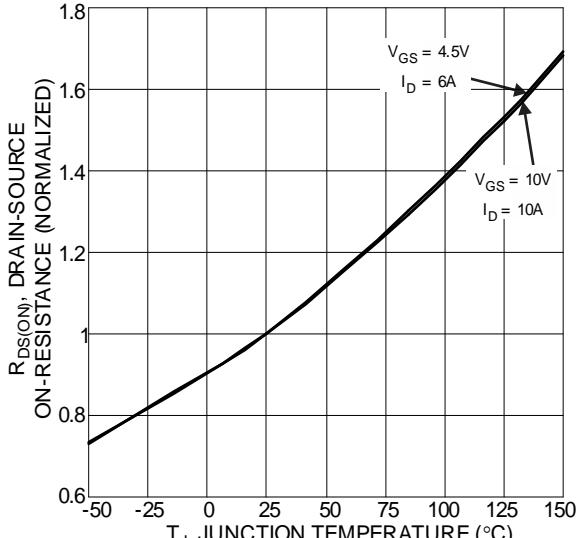
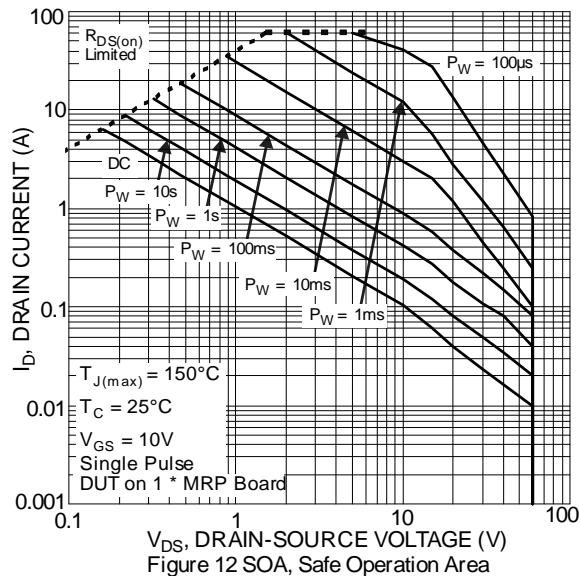
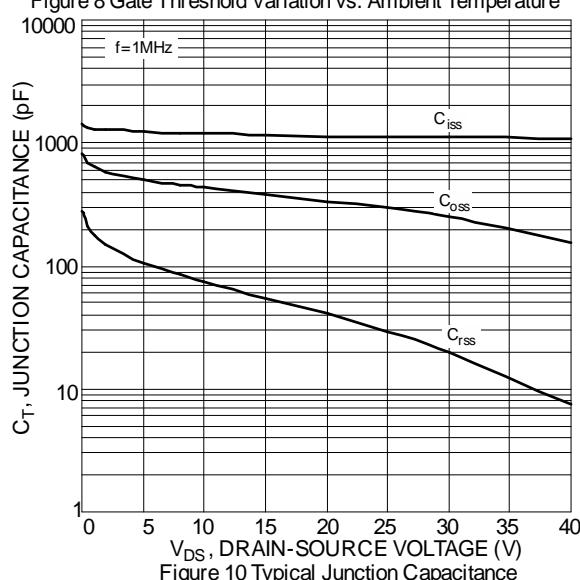
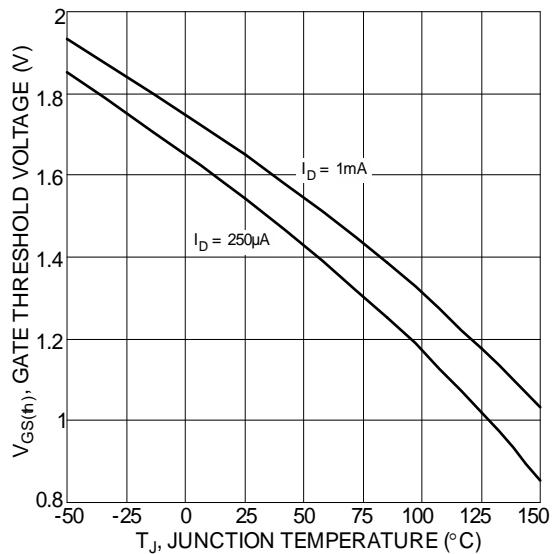
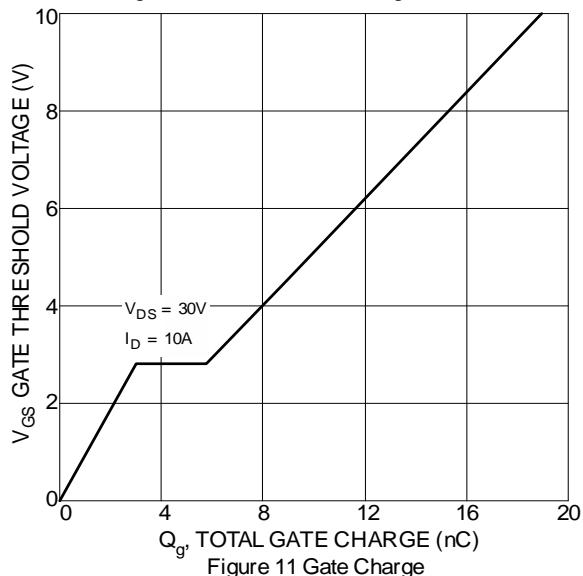
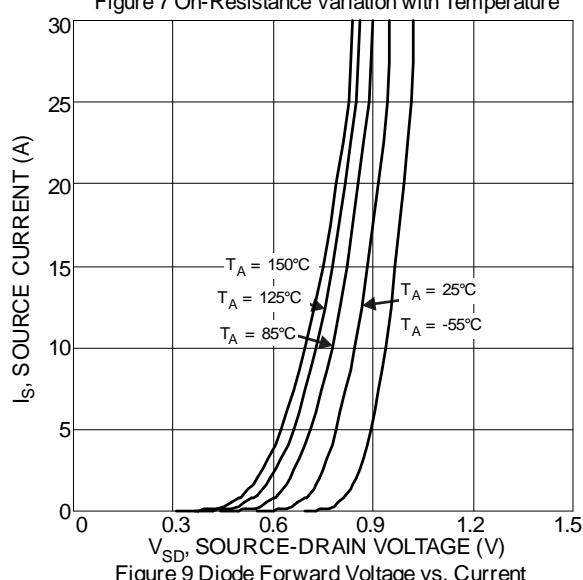
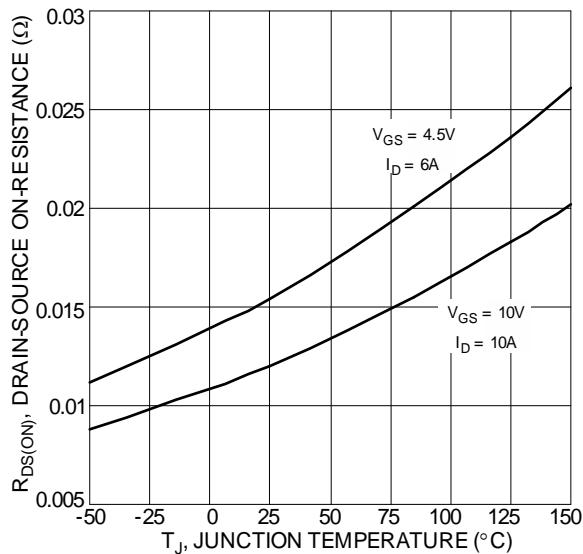


Figure 6 On-Resistance Variation with Temperature



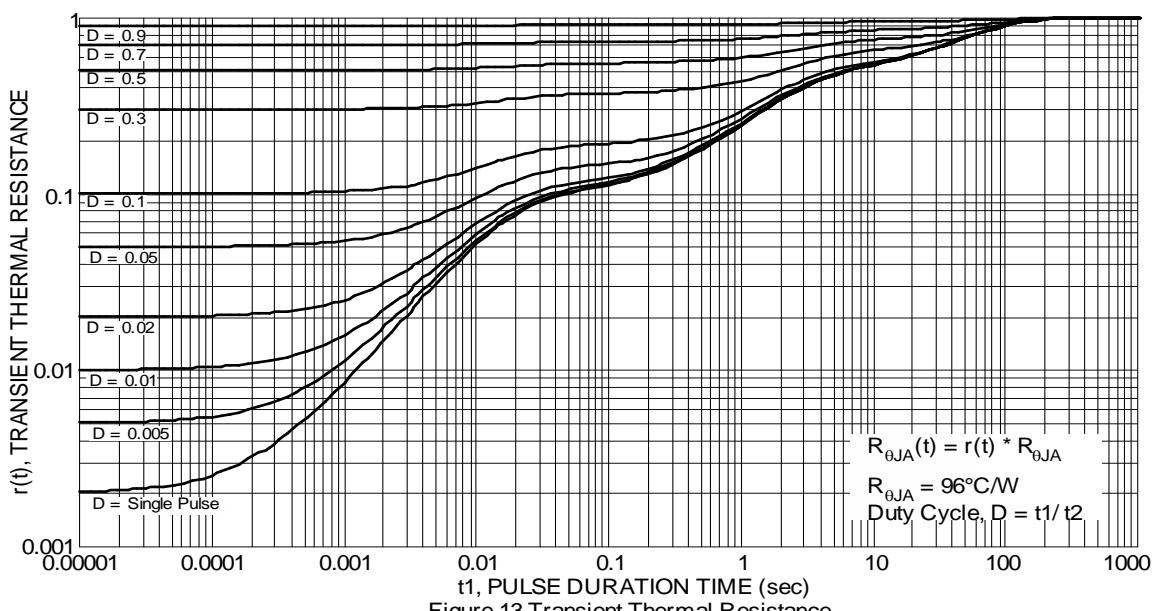
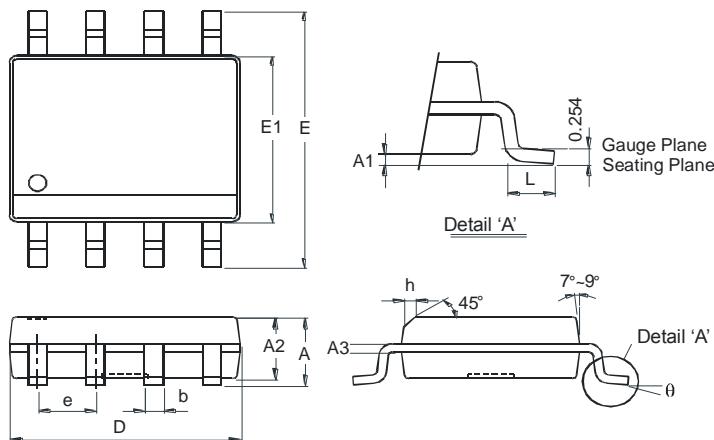


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8



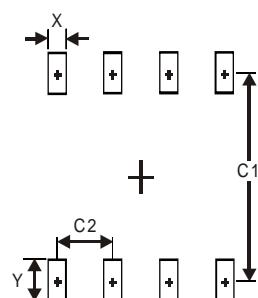
| SO-8 | | |
|-----------|----------|------|
| Dim | Min | Max |
| A | — | 1.75 |
| A1 | 0.10 | 0.20 |
| A2 | 1.30 | 1.50 |
| A3 | 0.15 | 0.25 |
| b | 0.3 | 0.5 |
| D | 4.85 | 4.95 |
| E | 5.90 | 6.10 |
| E1 | 3.85 | 3.95 |
| e | 1.27 Typ | |
| h | — | 0.35 |
| L | 0.62 | 0.82 |
| θ | 0° | 8° |

All Dimensions in mm

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8



| Dimensions | Value (in mm) |
|------------|---------------|
| X | 0.60 |
| Y | 1.55 |
| C1 | 5.4 |
| C2 | 1.27 |

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