

Features

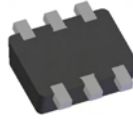
- Epitaxial Planar Die Construction
- Ideal for Low Power Amplification and Switching
- Ultra-Small Surface Mount Package
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **"Green" Device (Notes 4 and 5)**

Mechanical Data

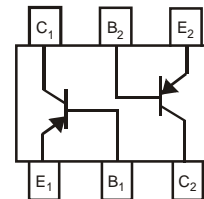
- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. (Note 5) UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram
- Terminals: Finish - Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 20
- Terminals: Lead bearing terminal plating available. See Ordering information Page 3
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.003 grams (approximate)



Top View



Bottom View



Device Schematic

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-40	V
Collector-Emitter Voltage	V_{CEO}	-40	V
Emitter-Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-200	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ $T_A = 25^\circ\text{C}$	P_D	150	mW
Thermal Resistance, Junction to Ambient (Note 3) @ $T_A = 25^\circ\text{C}$	$R_{\theta JA}$	833	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

- Notes:
1. No purposefully added lead.
 2. Package is non-polarized. Parts may be on reel in orientation illustrated, 180° rotated, or mixed (both ways).
 3. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 5. Product manufactured with Date Code UO (week 40, 2007) and newer are built with Green Molding Compound. Product manufactured prior to Date Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)					
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-40	—	V	I _C = -10μA, I _E = 0
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-40	—	V	I _C = -1.0mA, I _B = 0
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-5.0	—	V	I _E = -10μA, I _C = 0
Collector Cutoff Current	I _{CEX}	—	-50	nA	V _{CE} = -30V, V _{EB(OFF)} = -3.0V
Base Cutoff Current	I _{BL}	—	-50	nA	V _{CE} = -30V, V _{EB(OFF)} = -3.0V
ON CHARACTERISTICS (Note 6)					
DC Current Gain	h _{FE}	60	—	—	I _C = -100μA, V _{CE} = -1.0V
		80	—		I _C = -1.0mA, V _{CE} = -1.0V
		100	300		I _C = -10mA, V _{CE} = -1.0V
		60	—		I _C = -50mA, V _{CE} = -1.0V
		30	—		I _C = -100mA, V _{CE} = -1.0V
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	—	-0.25 -0.40	V	I _C = -10mA, I _B = -1.0mA I _C = -50mA, I _B = -5.0mA
Base-Emitter Saturation Voltage	V _{BE(SAT)}	-0.65 —	-0.85 -0.95	V	I _C = -10mA, I _B = -1.0mA I _C = -50mA, I _B = -5.0mA
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C _{obo}	—	4.5	pF	V _{CB} = -5.0V, f = 1.0MHz, I _E = 0
Input Capacitance	C _{ibo}	—	10	pF	V _{EB} = -0.5V, f = 1.0MHz, I _C = 0
Input Impedance	h _{ie}	2.0	12	kΩ	V _{CE} = 10V, I _C = 1.0mA, f = 1.0kHz
Voltage Feedback Ratio	h _{re}	0.1	10	x 10 ⁻⁴	
Small Signal Current Gain	h _{fe}	100	400	—	
Output Admittance	h _{oe}	3.0	60	μS	V _{CE} = -20V, I _C = -10mA, f = 100MHz
Current Gain-Bandwidth Product	f _T	250	—	MHz	
Noise Figure	NF	—	4.0	dB	V _{CE} = -5.0V, I _C = -100μA, R _S = 1.0kΩ, f = 1.0kHz
SWITCHING CHARACTERISTICS					
Delay Time	t _d	—	35	ns	V _{CC} = -3.0V, I _C = -10mA,
Rise Time	t _r	—	35	ns	V _{BE(off)} = 0.5V, I _{B1} = -1.0mA
Storage Time	t _s	—	225	ns	V _{CC} = -3.0V, I _C = -10mA,
Fall Time	t _f	—	75	ns	I _{B1} = I _{B2} = -1.0mA

Notes: 6. Short duration pulse test used to minimize self-heating effect.

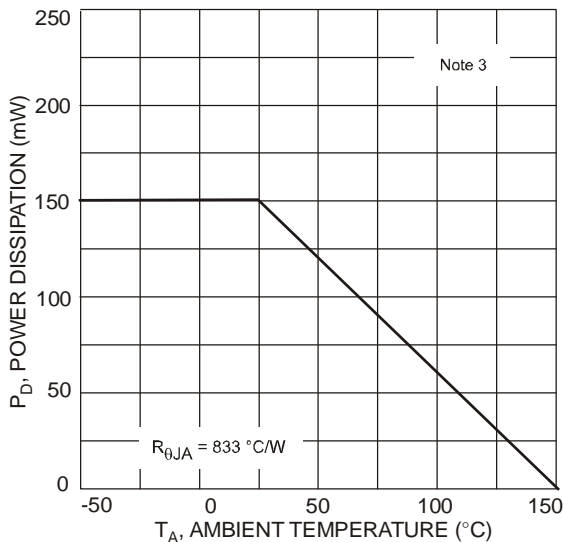


Fig. 1 Power Dissipation vs. Ambient Temperature (Note 3)

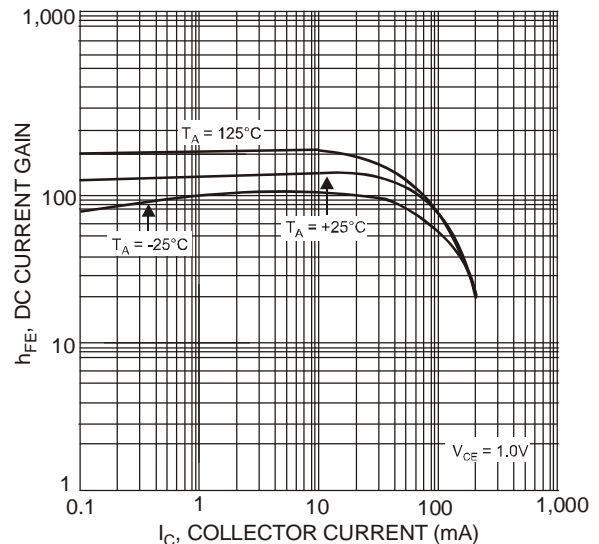


Fig. 2 Typical DC Current Gain vs. Collector Current

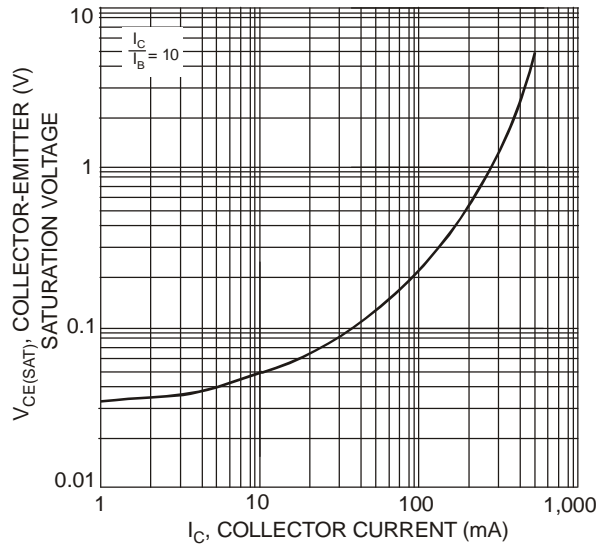


Fig. 3 Typical Collector-Emitter Saturation Voltage vs. Collector Current

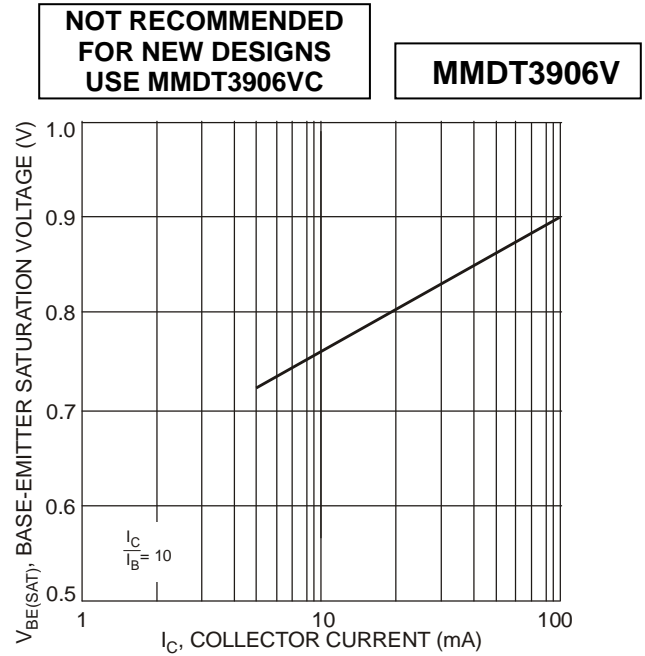


Fig. 4 Typical Base-Emitter Saturation Voltage vs. Collector Current

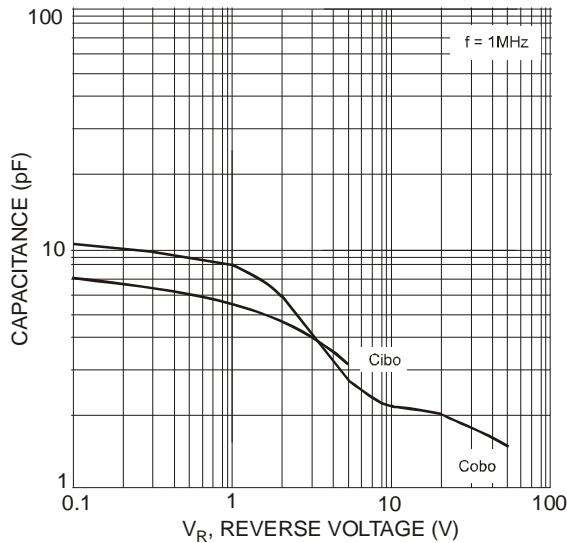


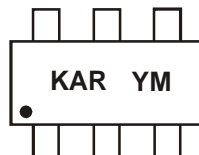
Fig. 5 Typical Capacitance Characteristics

Ordering Information (Note 7)

Part Number	Case	Packaging
MMDT3906V-7	SOT-563	3000/Tape & Reel

Notes: 7. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



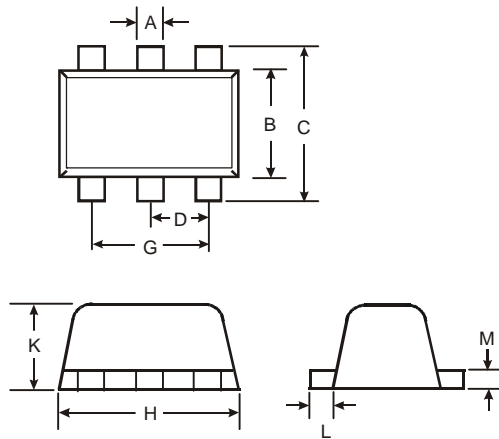
KAR = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: T = 2006)
M = Month (ex: 9 = September)

Date Code Key

Year	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015
Code	S	T	U	V	W	X	Y	Z	A	B	C

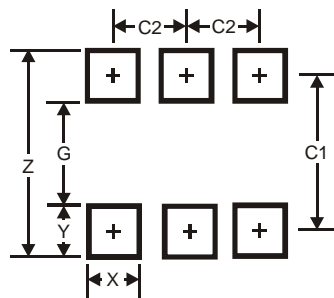
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Package Outline Dimensions



SOT-563			
Dim	Min	Max	Typ
A	0.15	0.30	0.20
B	1.10	1.25	1.20
C	1.55	1.70	1.60
D	-	-	0.50
G	0.90	1.10	1.00
H	1.50	1.70	1.60
K	0.55	0.60	0.60
L	0.10	0.30	0.20
M	0.10	0.18	0.11
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Y	0.5
C1	1.7
C2	0.5

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