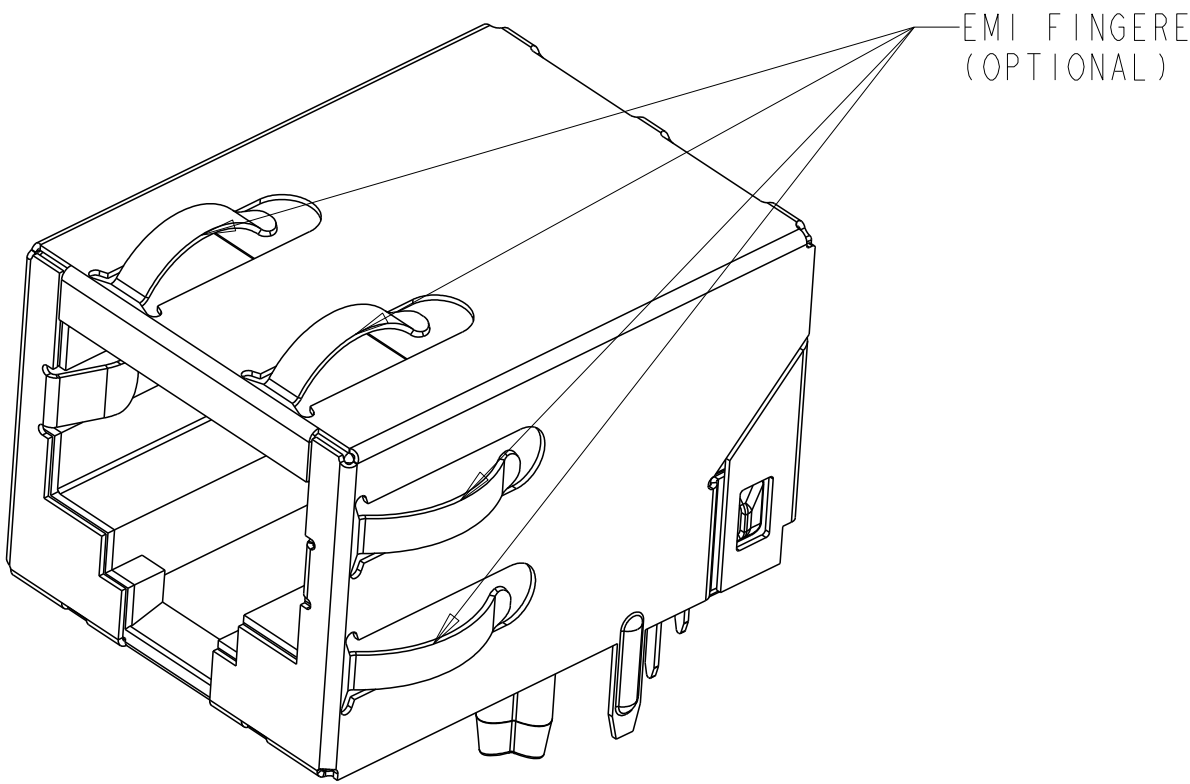
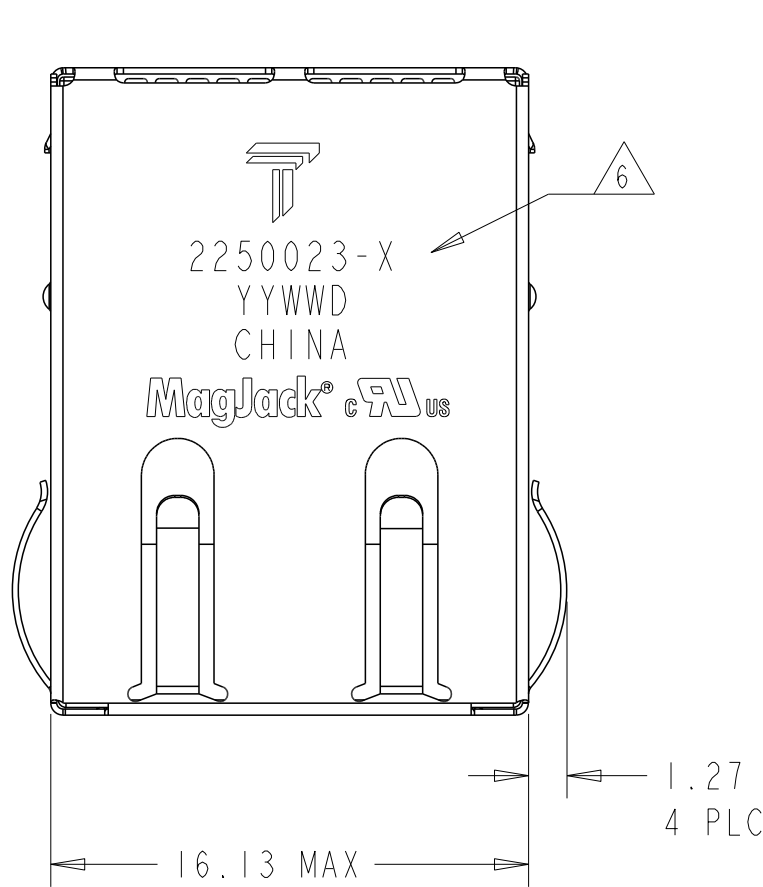


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PRODUCT MAY BE PROTECTED BY ONE OR MORE OF THE FOLLOWING US PATENTS:
5736910 5939955 6425781 6428361 6554638 6840817 7123117
7429195 7717749 7808751 6217391 6149050 7924130

REVISIONS					
P	LYR	DESCRIPTION	DATE	DWN	APVD
	1	INITIAL RELEASE	28MAY2013	RL	TY
	2	SEE LAST SHEET	24NOV2014	TYR	TYN



NOTES:

- 1 MATERIALS
- HOUSING: THERMOPLASTIC, FLAMMABILITY RATING UL 94V-0.
 - SHIELD: BRASS 0.008 INCH THICK, PREPLATED WITH 30uINCH MIN SEMI-BRIGHT NICKEL. SOLDER TABS POST DIPPED WITH 100uINCH MIN SAC SOLDER.
 - CONTACTS: PHOSPHOR BRONZE 0.46mm X 0.30mm, PHOSPHOR BRONZE, 1.27um MIN NICKEL UNDERPLATE, WITH 1.27um MIN GOLD PLATING AT MATING INTERFACE AND 2.54um MIN MATTE TIN ON SOLDER TAILS, OR 1.27um MIN NICKEL UNDERPLATE, WITH 0.05um GOLD PLATING OVER 0.76um MIN PALLADIUM NICKEL PLATING AT MATING INTERFACE AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
 - SOLDER TAILS: 0.25mmTHICK, PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL UNDERPLATE, 3um TIN PLATE

- 2 RJ45 JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUB PART F.

- 3 MAGNETICS
- APPLICATION: 10/100 BASE-T, PoE, EXTENDED TEMPERATURE
 - IMPEDANCE: 100 OHMS
 - TURNS RATIO (CHIP:CABLE): TX = 1:1, RX = 1:1
 - OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM -40°C TO +85°C, TX AND RX
 - POE CURRENT: 350mADC MAX
 - PERFORMANCE @ 25°C:
 - INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
 - RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 30MHz
 - 18-20LOG(f/30)dB MIN FROM 30.1MHz TO 60MHz
 - 12dB MIN FROM 60.1MHz TO 80MHz
 - CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
 - 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
 - COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
 - ISOLATION VOLTAGE: 2250VDC FOR 60 SECONDS WITH A RISE TIME OF 500 V/SEC

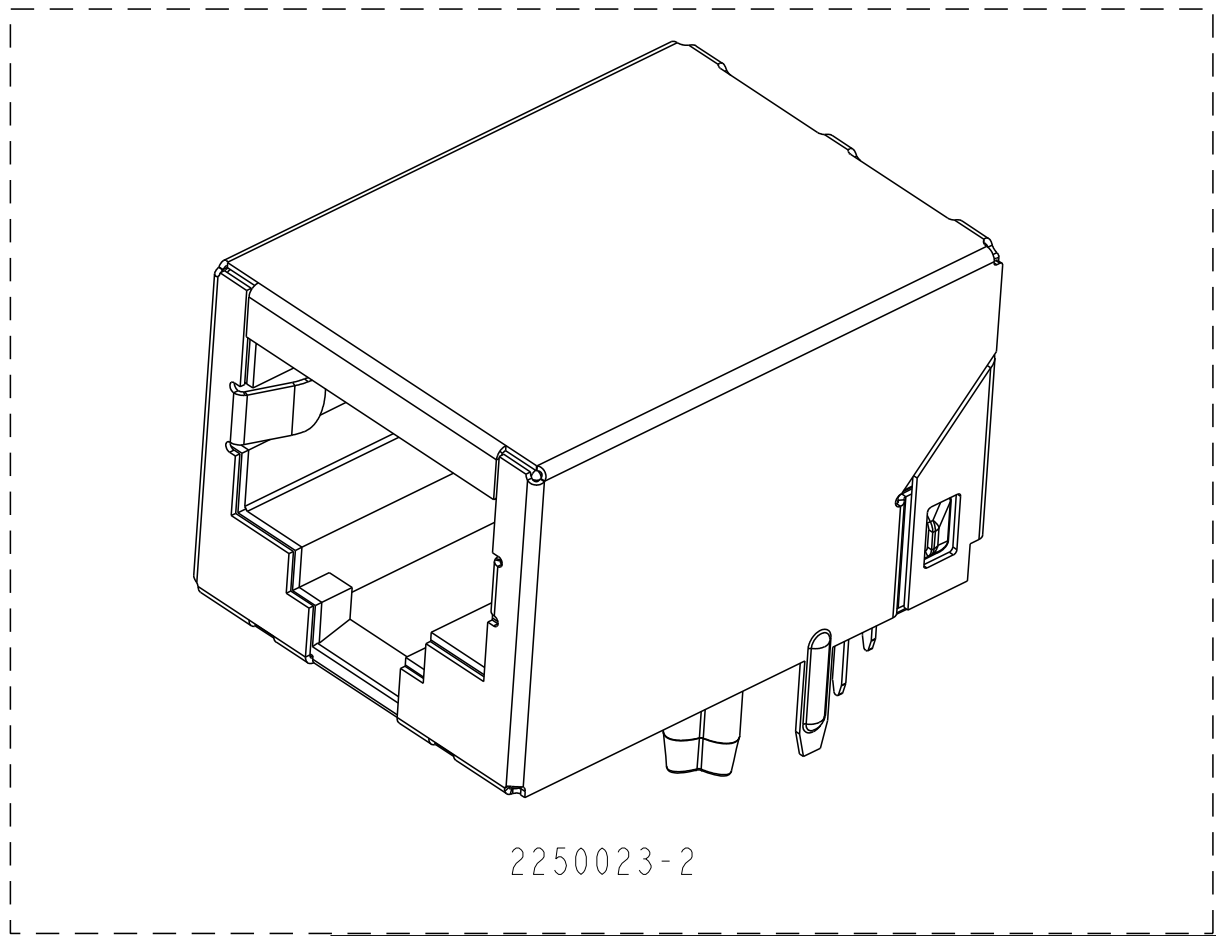
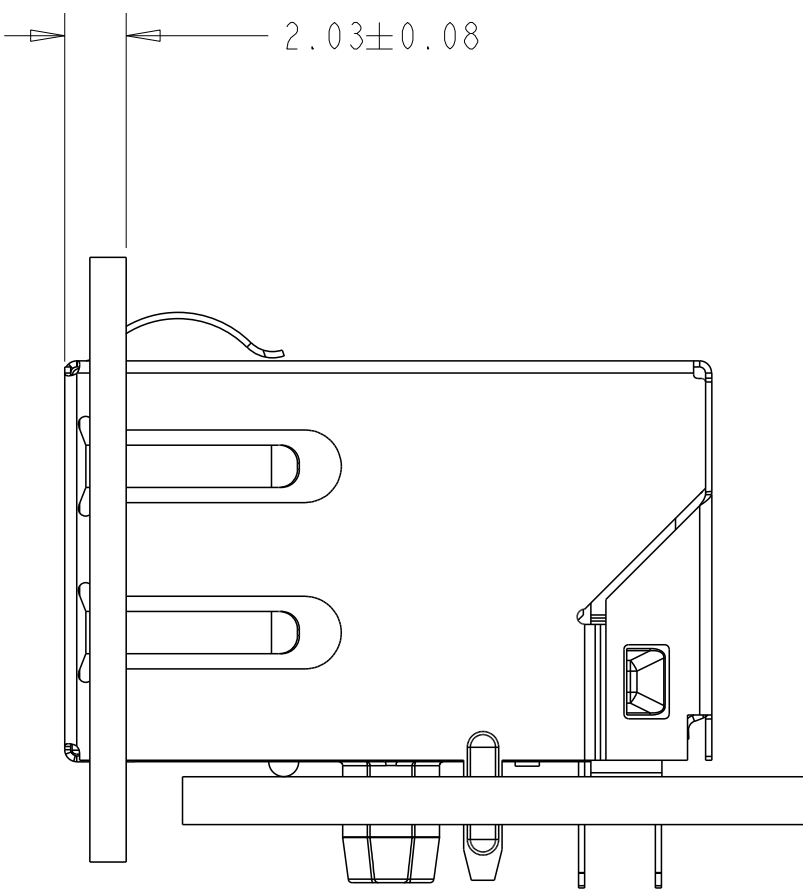
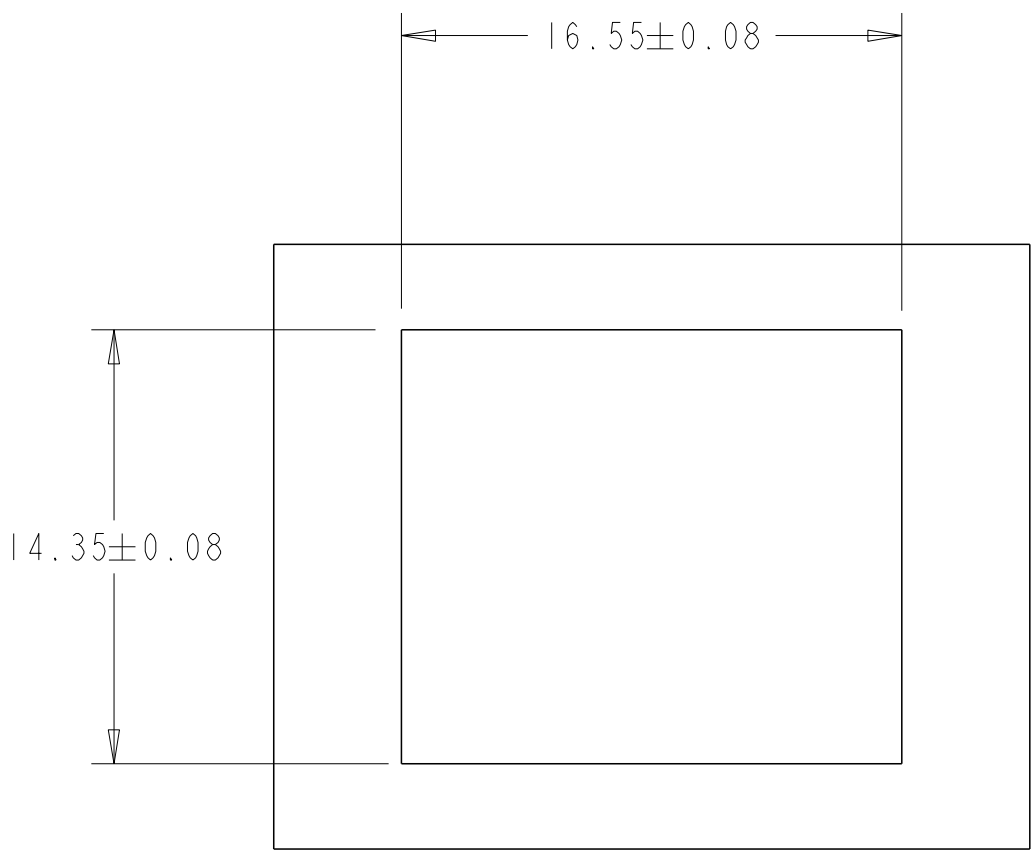
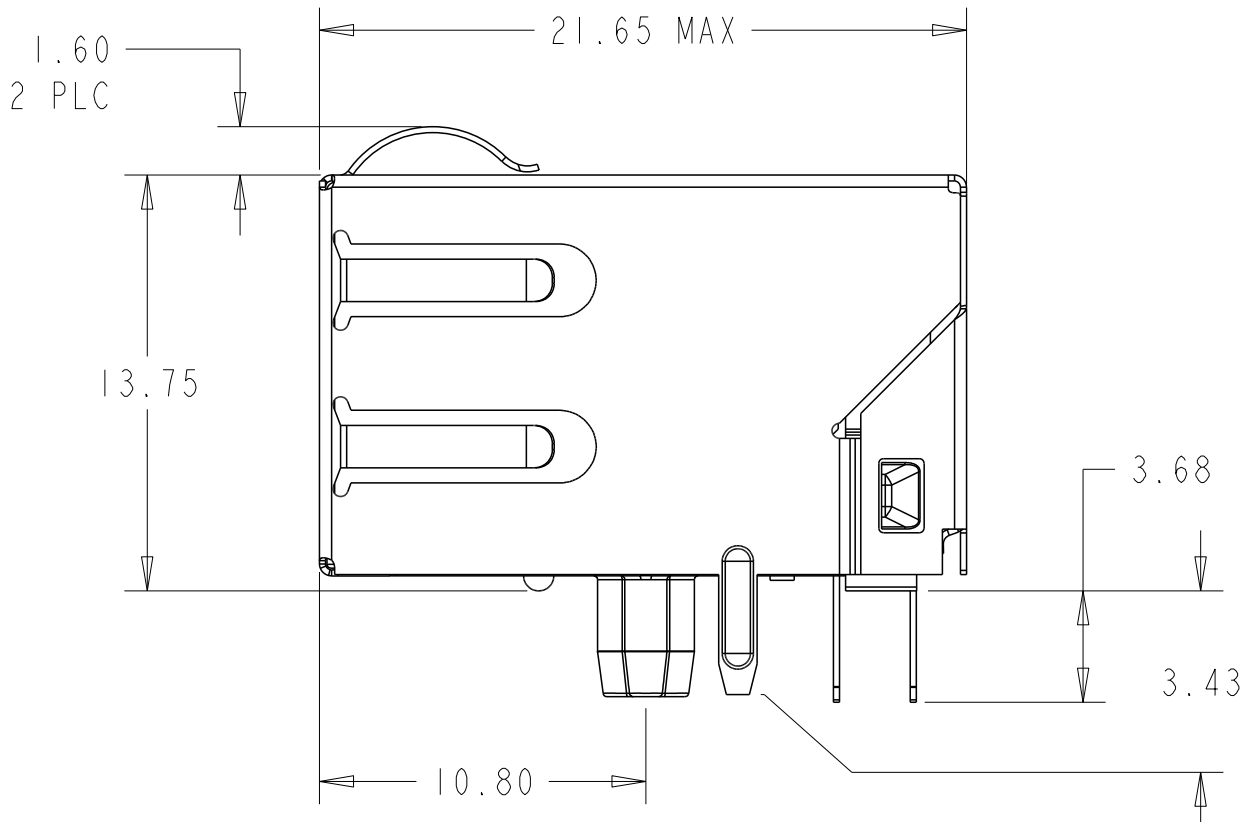
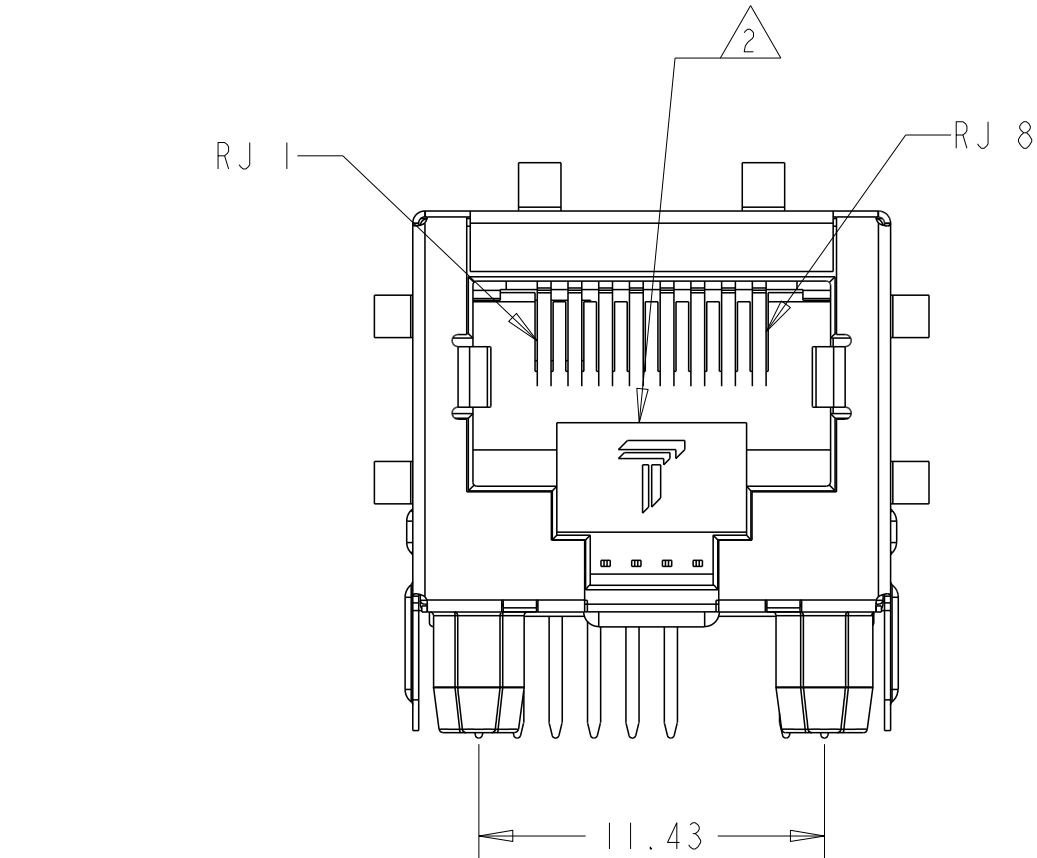
4. OPERATING TEMPERATURE: FROM -40°C TO +85°C.

- 5 INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL TO SUPPORT AUTO-MDI/MDIX.

- 6 TRP CONNECTOR LOGO, PART NUMBER, DATE CODE, COUNTRY OF ORIGIN AND AGENCY APPROVAL MARKING IN APPROXIMATE LOCATION SHOWN.

7. PART SHOULD BE RECOMMENDED FOR WAVE SOLDERING PROCESS,PREHEAT TEMPERATURE IS 120°C TO 160°C, 120 SECONDS TO 180 SECONDS, PEAK WAVE SOLDERING TEMPERATURE IS 260°C MAX,10 SECONDS MAX.

8. 2250023-1 SHOWN UNLESS OTHERWISE NOTED



THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN ROY LIU	27MAY2013		 trp CONNECTOR a bel group		DONGGUAN CHINA				
DIMENSIONS:		CHK TOWER YU/ FRANK LIU	27MAY2013								
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD TONY YUAN	27MAY2013								
mm		MODEL NAME									
		0 PLC ±0.25		DESC		1X1 4DI43ET 10/100 PoE TAB DOWN W/O LED					
1 PLC ±0.25		2 PLC ±0.25		SIZE		CAGE CODE		DRAWING NO		RESTRICTED TO	
2 PLC ±0.25		3 PLC ±		A1		C=2250023					
3 PLC ±		4 PLC ±		SCALE		4:1		SHEET		1 OF 3	
ANGLES		APPLICATION SPEC		CUSTOMER DRAWING						REV 2	

D

D

C

C

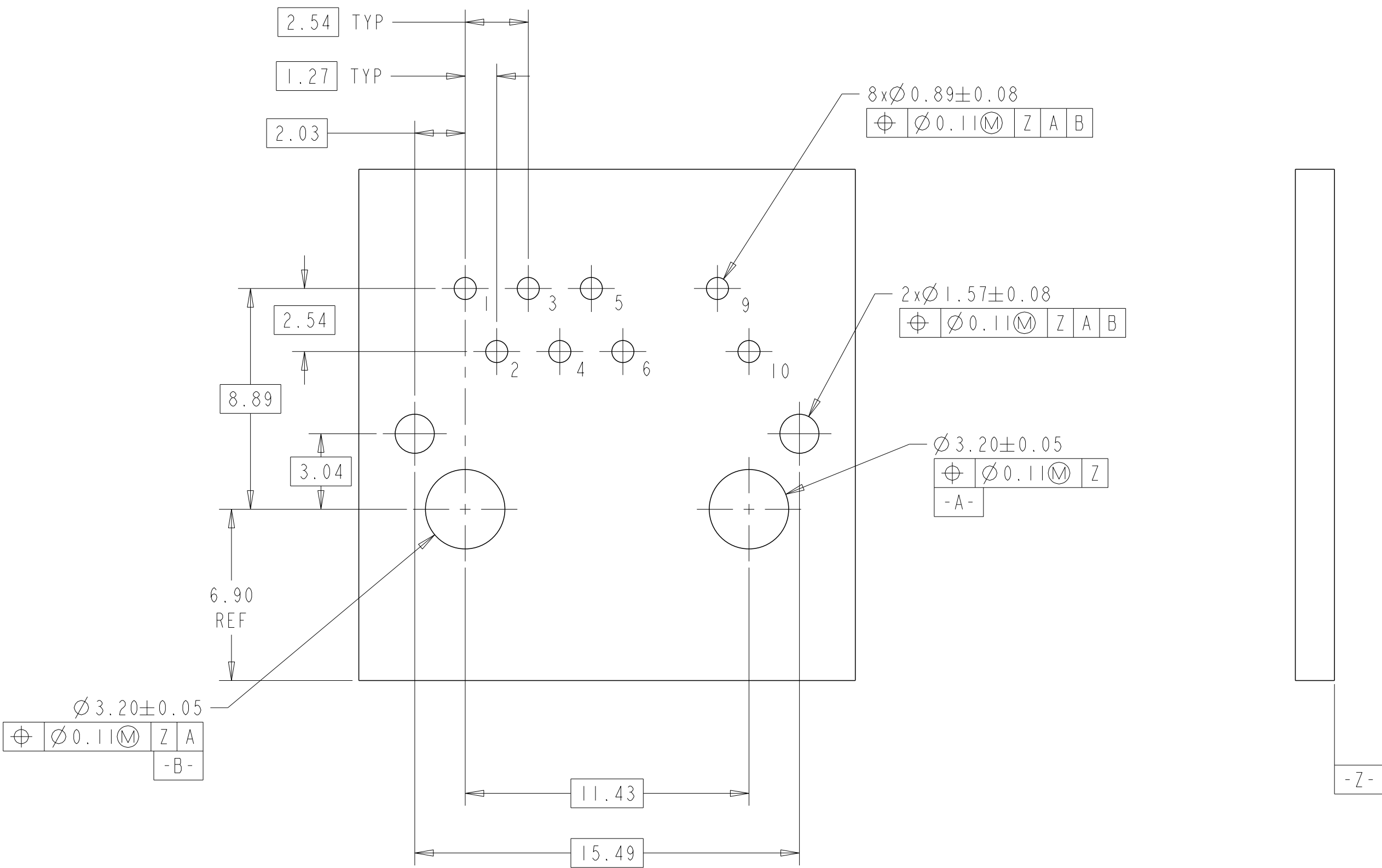
B

B

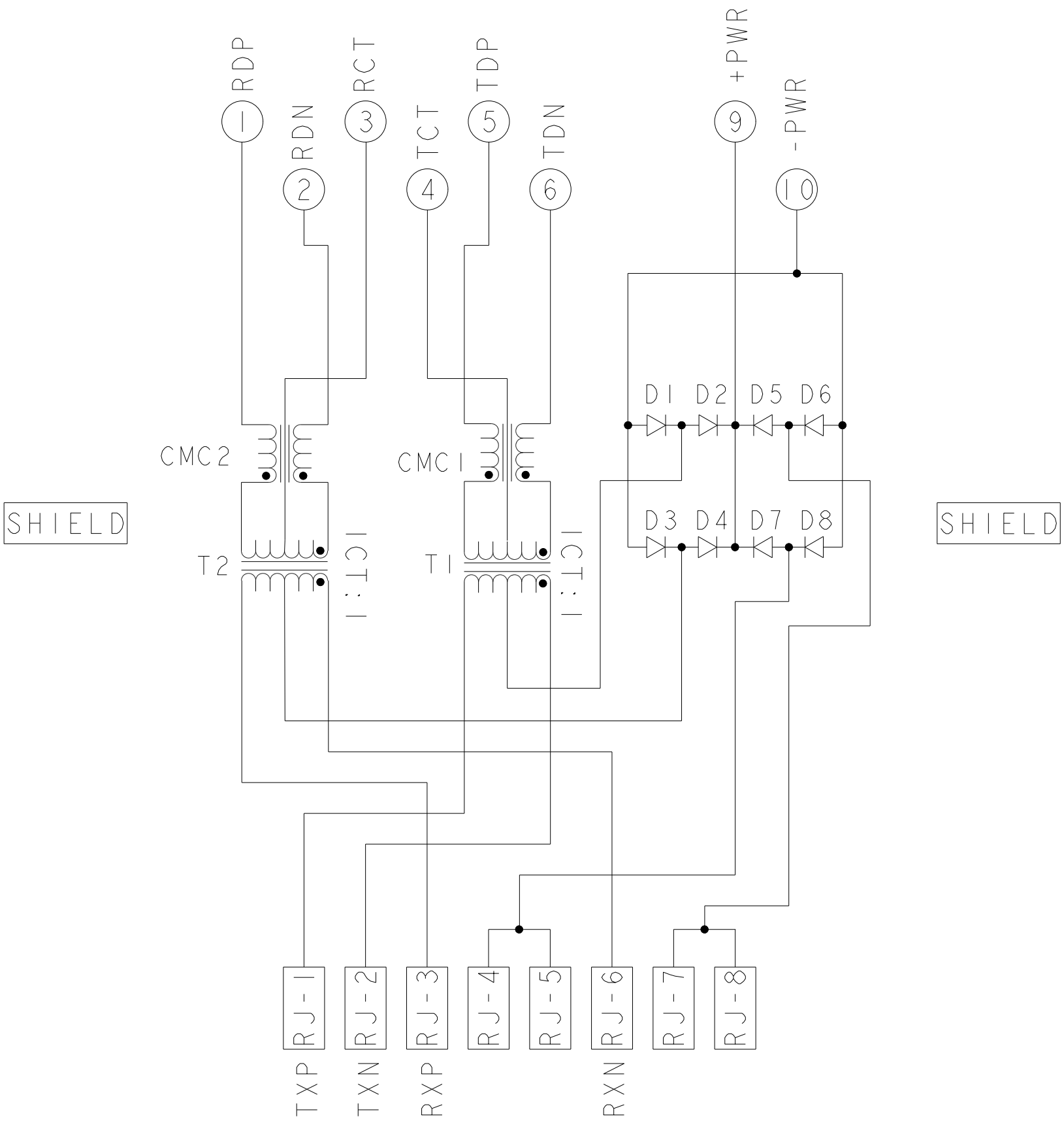
A

A

4DI43ET 10/100 BASE-T PoE CIRCUIT



SUGGESTED CUSTOMER PCB
COMPONENT SIDE



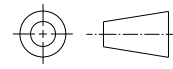
DI-D8 = RECTIFIER DIODE
-FORWARD VOLTAGE, $V_f = 1.2V$ MAX @ $I_f = 0.5A$
-FORWARD CURRENT, $I_f = 0.5A$ MAX
-REVERSE VOLTAGE, $V_r = 80V$ MAX

THIS DRAWING IS A CONTROLLED DOCUMENT.									
		OWN ROY LIU 27MAY2013		 trp CONNECTOR a bel group		DONGGUAN CHINA			
		CHK TOWER YU/ FRANK LIU 27MAY2013							
		APVD TONY YUAN 27MAY2013							
		MODEL NAME							
DIMENSIONS:		TOLERANCES UNLESS OTHERWISE SPECIFIED:		DESC 1X1 4DI43ET 10/100 PoE TAB DOWN W/O LED					
mm		0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ±0.25 4 PLC ± ANGLES ± APPLICATION SPEC ±		SIZE CAGE CODE DRAWING NO		RESTRICTED TO			
				MAGJACK SINGLE PoE		A1 C=2250023		-	
PRODUCT SPEC		CUSTOMER DRAWING		SCALE 4:1		SHEET 2 OF 3		REV 2	

C-2250023					STOCK DISPOSITION			
					A: CONTINUE AS IS B: REWORK C: SCRAP		D: USE OLD FIRST E: PARTS TO CONFORM F: OTHER	
REVISION HISTORY								
DATE	REV	CHANGES	REASON	APPROVAL	INVENTORY (A,B,C,D,E,F)	WORK IN PROCESS (A,B,C,F)	SUPPLIER ORDERS (A,E,F)	OPEN CUST ORDERS (A,E,F)
27MAY2013	1	-	INITIAL RELEASE	TONY YUAN	-	-	-	-
24NOV2014	2	SHEET 1, B3: ADD 2250023-2	MARKET REQUEST	TONY YUAN	-	-	-	-

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS:
mm



TOLERANCES UNLESS OTHERWISE SPECIFIED:

0 PLC ±0.25

1 PLC ±0.25

2 PLC ±0.25

3 PLC ±

4 PLC ±

ANGLES

APPLICATION SPEC

PRODUCT SPEC

-

DWN
ROY LIU
27MAY2013

CHK
TOWER YU/ FRANK LIU
27MAY2013

APVD
TONY YUAN
27MAY2013

MODEL NAME

MAGJACK
SINGLE PoE

DESC

1X1 4DI43ET 10/100 PoE
TAB DOWN W/O LED

SIZE
A1

CAGE CODE

DRAWING NO
C=2250023

RESTRICTED TO
-

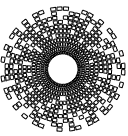
CUSTOMER DRAWING

SCALE
4:1

SHEET
3

OF
3

REV
2

 **trp** CONNECTOR

DONGGUAN
CHINA