To our customers,

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

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BIPOLAR ANALOG INTEGRATED CIRCUIT

μPC29M33A, 29M05A

THREE-TERMINAL LOW DROPOUT VOLTAGE REGULATOR

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DESCRIPTION

The μPC29M33A, 29M05A of low dropout voltage three terminal positive regulators is constructed with PNP output transistor. The μPC29M33A, 29M05A feature the ability to source 0.5 A of output current with a low dropout voltage of typically 0.5 V.

The power dissipation of the μPC29M33A, 29M05A can be drastically reduced compared with the conventional three terminal positive voltage regulators that is constructed with NPN output transistor. Also, this series corresponds to the low voltage output (3.3 V) which is not in the conventional low dropout regulators (μPC24MxxA series).

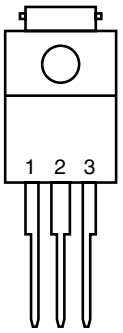
FEATURES

- Output current in excess of 0.5 A
- Low dropout voltage

  V_{DIF} = 0.5 V TYP. (I_O = 0.5 A)
- On-chip over-current and thermal protection circuit
- On-chip output transistor safe operating area protection circuit

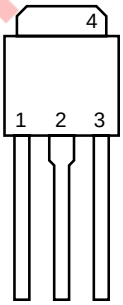
PIN CONFIGURATIONS (Marking Side)

μPC29M33AHF,
μPC29M05AHF: Isolated TO-220 (MP-45G)



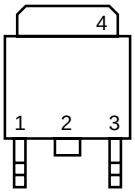
1: INPUT
2: GND
3: OUTPUT

μPC29M33AHB,
μPC29M05AHB: SC-64 (MP-3)



1: INPUT
2: GND[Note1]
3: OUTPUT
4: GND (Fin)

μPC29M33AT,
μPC29M05AT: SC-63 (MP-3Z)



1: INPUT
2: GND[Note2]
3: OUTPUT
4: GND (Fin)

Notes 1. No.2 pin and No.4 fin are common GND.

2. No.2 pin is cut. No.2 pin and No.4 fin are common GND.

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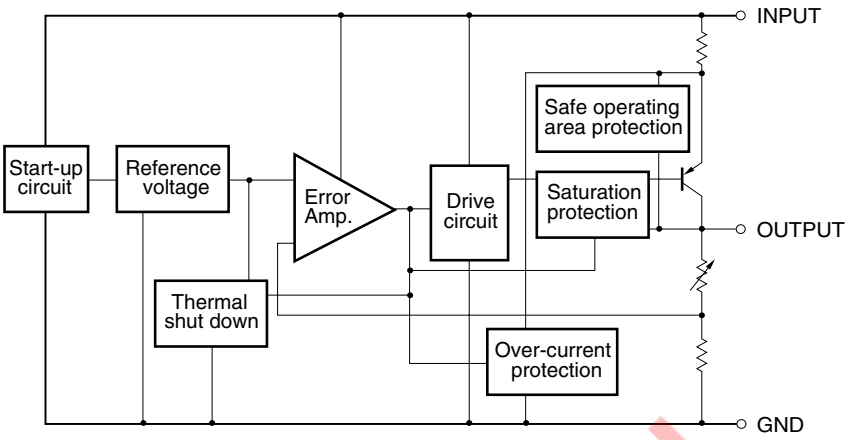
Document No. G15368EJ4V0DS00 (4th edition)
Date Published August 2007 NS
Printed in Japan

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The mark <R> shows major revised points.
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

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BLOCK DIAGRAM



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<R> ORDERING INFORMATION

Part Number	Package	Output Voltage	Marking
μPC29M33AHF	Isolated TO-220 (MP-45G)	3.3 V	29M33A
μPC29M33AHB	SC-64 (MP-3)	3.3 V	29M33A
μPC29M33AT	SC-63 (MP-3Z)	3.3 V	29M33A
μPC29M05AHF	Isolated TO-220 (MP-45G)	5.0 V	29M05A
μPC29M05AHB	SC-64 (MP-3)	5.0 V	29M05A
μPC29M05AT	SC-63 (MP-3Z)	5.0 V	29M05A

Remark Tape-packaged products have the symbol -E1, or -E2 suffixed to the part number. Pb-free products have the symbol -AZ, or -AY suffixed to the part number. Refer to the following table for details.

Part Number [Note1]	Package	Package Type
μPC29MxxAHF	Isolated TO-220 (MP-45G)	• Packed in envelop
μPC29MxxAHF-AZ [Note2]	Isolated TO-220 (MP-45G)	• Packed in envelop
μPC29MxxAHB	SC-64 (MP-3)	• Packed in envelop
μPC29MxxAHB-AZ [Note2]	SC-64 (MP-3)	• Packed in envelop
μPC29MxxAHB-AY [Note3]	SC-64 (MP-3)	• Packed in envelop
μPC29MxxAT-E1	SC-63 (MP-3Z)	• 16 mm wide embossed taping • Pin 1 on draw-out side • 2000 pcs/reel
μPC29MxxAT-E1-AZ [Note2]	SC-63 (MP-3Z)	• 16 mm wide embossed taping • Pin 1 on draw-out side • 2000 pcs/reel
μPC29MxxAT-E1-AY [Note3]	SC-63 (MP-3Z)	• 16 mm wide embossed taping • Pin 1 on draw-out side • 2000 pcs/reel
μPC29MxxAT-E2	SC-63 (MP-3Z)	• 16 mm wide embossed taping • Pin 1 at take-up side • 2000 pcs/reel
μPC29MxxAT-E2-AZ [Note2]	SC-63 (MP-3Z)	• 16 mm wide embossed taping • Pin 1 at take-up side • 2000 pcs/reel
μPC29MxxAT-E2-AY [Note3]	SC-63 (MP-3Z)	• 16 mm wide embossed taping • Pin 1 at take-up side • 2000 pcs/reel

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Notes 1. xx stands for symbols that indicate the output voltage.

2. Pb-free (This product does not contain Pb in the external electrode.)

3. Pb-free (This product does not contain Pb in the external electrode, Sn100% plating.)

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ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

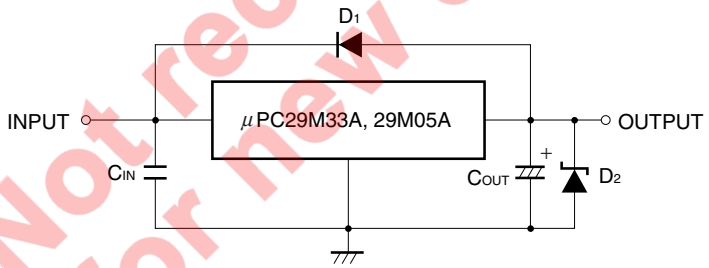
Parameter	Symbol	Rating		Unit
		μPC2933AHF, 2905AHF	μPC2933AHB, 2905AHB μPC2933AT, 2905AT	
Input Voltage	V_{IN}	20		V
Internal Power Dissipation (T_C = 25°C) Note	P_T	15	10	W
Operating Ambient Temperature	T_A	−30 to +85		°C
Operating Junction Temperature	T_J	−30 to +150		°C
Storage Temperature	T_{stg}	−55 to +150		°C
Thermal Resistance (junction to case)	$R_{th(J-C)}$	7	12.5	°C/W
Thermal Resistance (junction to ambient)	$R_{th(J-A)}$	65	125	°C/W

Note Internally limited. When the operating junction temperature rises above 150°C, the internal circuit shuts down the output voltage.

Caution Product quality may suffer if the absolute maximum rating is exceeded even momentarily for any parameter. That is, the absolute maximum ratings are rated values at which the product is on the verge of suffering physical damage, and therefore the product must be used under conditions that ensure that the absolute maximum ratings are not exceeded.

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TYPICAL CONNECTION



C_{IN} : 0.1 μF or higher. Be sure to connect C_{IN} to prevent parasitic oscillation. Set this value according to the length of the line between the regulator and the INPUT pin. Use of a film capacitor or other capacitor with first-rate voltage and temperature characteristics is recommended. If using a laminated ceramic capacitor, it is necessary to ensure that C_{IN} is 0.1 μF or higher for the voltage and temperature range to be used.

C_{OUT}: 47 μF or higher. Be sure to connect C_{OUT} to prevent oscillation and improve excessive load regulation. Place C_{IN} and C_{OUT} as close as possible to the IC pins (within 1 to 2 cm). Also, use an electrolytic capacitor with low impedance characteristics if considering use at sub-zero temperatures.

D_1 : If the OUTPUT pin has a higher voltage than the INPUT pin, connect a diode.

D_2 : If the OUTPUT pin has a lower voltage than the GND pin, connect a Schottky barrier diode.

Caution Make sure that no voltage is applied to the OUTPUT pin from external.

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RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Type Number	MIN.	TYP.	MAX.	Unit
Input Voltage	V_{IN}	μPC29M33A	4.3		16	V
		μPC29M05A	6		16	
Output Current	I_O	All	0		0.5	A
Operating Ambient Temperature	T_A	All	–30		+85	°C
Operating Junction Temperature	T_J	All	–30		+125	°C

ELECTRICAL CHARACTERISTICS

μPC29M33A (T_J = 25°C, V_{IN} = 5 V, I_O = 350 mA, C_{IN} = 0.22 μF, C_{OUT} = 47 μF, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	V_O		3.18	3.3	3.42	V
		0°C ≤ T_J ≤ 125°C, 4.3 V ≤ V_{IN} ≤ 16 V, 0 A ≤ I_O ≤ 350 mA	3.14		3.46	
		0°C ≤ T_J ≤ 125°C, 0 A ≤ I_O ≤ 0.5 A				
Line Regulation	REG_{IN}	4.3 V ≤ V_{IN} ≤ 16 V		8	33	mV
Load Regulation	REG_L	0 A ≤ I_O ≤ 0.5 A		10	33	mV
Quiescent Current	I_{BIAS}	I_O = 0 A		1.8	3.0	mA
		I_O = 0.5 A		15	20	
Startup Quiescent Current	$I_{BIAS\ (s)}$	V_{IN} = 3.1 V, I_O = 0 A		9	20	mA
		V_{IN} = 3.1 V, I_O = 0.5 A			50	
Quiescent Current Change	ΔI_{BIAS}	0°C ≤ T_J ≤ 125°C, 4.3 V ≤ V_{IN} ≤ 16 V		2.9	15	mA
Output Noise Voltage	V_n	10 Hz ≤ f ≤ 100 kHz		56		$\mu V_{r.m.s.}$
Ripple Rejection	R•R	4.3 V ≤ V_{IN} ≤ 16 V, f = 120 Hz	48	64		dB
Dropout Voltage	V_{DIF}	0°C ≤ T_J ≤ 125°C, I_O = 0.5 A		0.5	1.0	V
Short Circuit Current	I_{Oshort}	V_{IN} = 4.5 V	0.7	1.1	1.5	A
		V_{IN} = 16 V		0.6		
Peak Output Current	I_{Opeak}	V_{IN} = 4.5 V	0.7	1.2	1.5	A
		V_{IN} = 16 V	0.6	1.0	1.5	
Temperature Coefficient of Output Voltage	$\Delta V_O/\Delta T$	0°C ≤ T_J ≤ 125°C, I_O = 5 mA		–0.4		mV/°C

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μPC29M05A (T_J = 25°C, V_{IN} = 8 V, I_O = 350 mA, C_{IN} = 0.22 μF, C_{OUT} = 47 μF, unless otherwise specified)

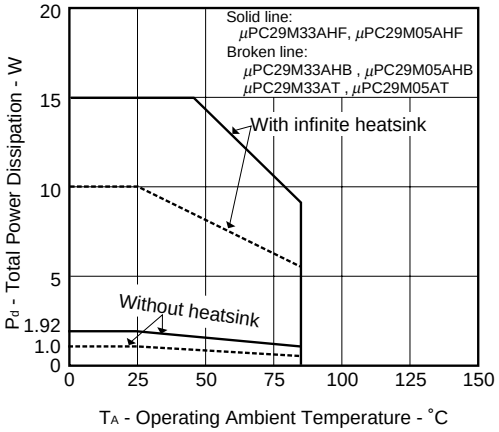
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	V_O		4.83	5.0	5.18	V
		0°C ≤ T_J ≤ 125°C, 6 V ≤ V_{IN} ≤ 16 V, 0 A ≤ I_O ≤ 350 mA	4.75		5.25	
		0°C ≤ T_J ≤ 125°C, 0 A ≤ I_O ≤ 0.5 A				
Line Regulation	REG_{IN}	6 V ≤ V_{IN} ≤ 16 V		26	50	mV
Load Regulation	REG_L	0 A ≤ I_O ≤ 0.5 A		17	50	mV
Quiescent Current	I_{BIAS}	I_O = 0 A		1.9	4.0	mA
		I_O = 0.5 A		15	20	
Startup Quiescent Current	$I_{BIAS\ (s)}$	V_{IN} = 4.5 V, I_O = 0 A		10	20	mA
		V_{IN} = 4.5 V, I_O = 0.5 A			50	
Quiescent Current Change	ΔI_{BIAS}	0°C ≤ T_J ≤ 125°C, 6 V ≤ V_{IN} ≤ 16 V		2.4	15	mA
Output Noise Voltage	V_n	10 Hz ≤ f ≤ 100 kHz		87		$\mu V_{r.m.s.}$
Ripple Rejection	R•R	6 V ≤ V_{IN} ≤ 16 V, f = 120 Hz	46	60		dB
Dropout Voltage	V_{DIF}	0°C ≤ T_J ≤ 125°C, I_O = 0.5 A		0.5	1.0	V
Short Circuit Current	I_{Oshort}	V_{IN} = 6.5 V	0.65	1.1	1.5	A
		V_{IN} = 16 V		0.6		
Peak Output Current	I_{Opeak}	V_{IN} = 6.5 V	0.7	1.2	1.5	A
		V_{IN} = 16 V	0.6	1.1	1.5	
Temperature Coefficient of Output Voltage	$\Delta V_O/\Delta T$	0°C ≤ T_J ≤ 125°C, I_O = 5 mA		0.5		mV/°C

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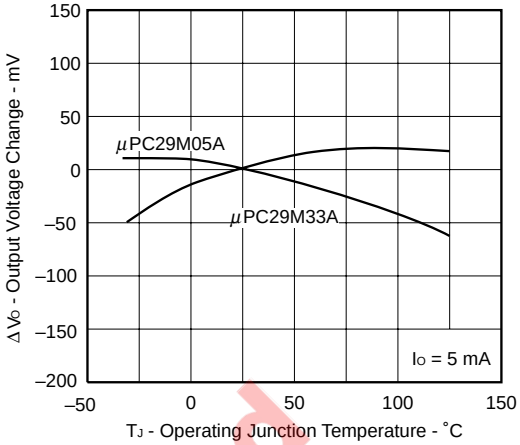
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TYPICAL CHARACTERISTICS

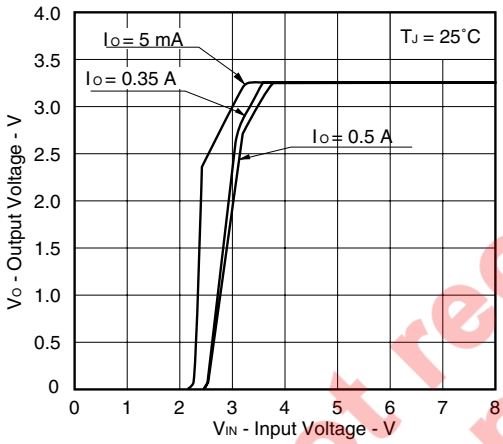
P_d vs. T_A



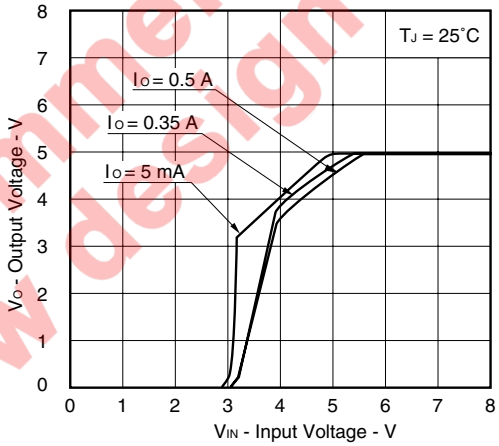
ΔVO vs TJ



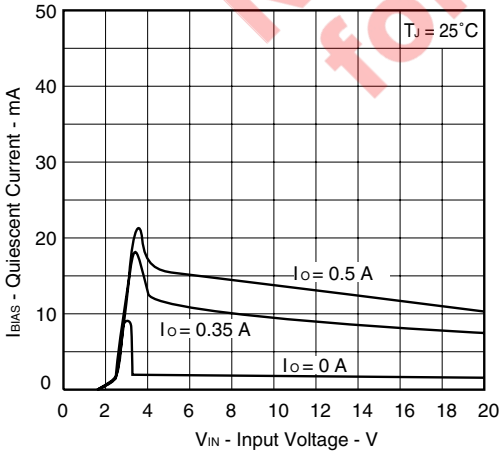
VO vs. VIN (μPC29M33A)



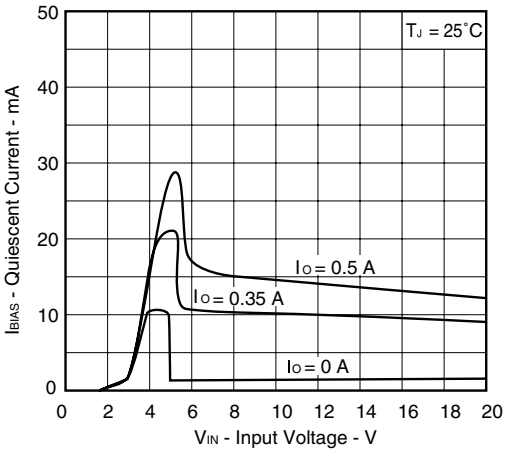
VO vs. VIN (μPC29M05A)



IBIAS (IBIAS(s)) vs. VIN (μPC29M33A)



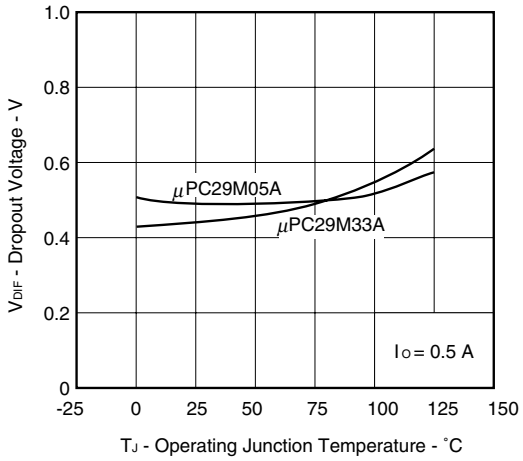
IBIAS (IBIAS(s)) vs. VIN (μPC29M05A)



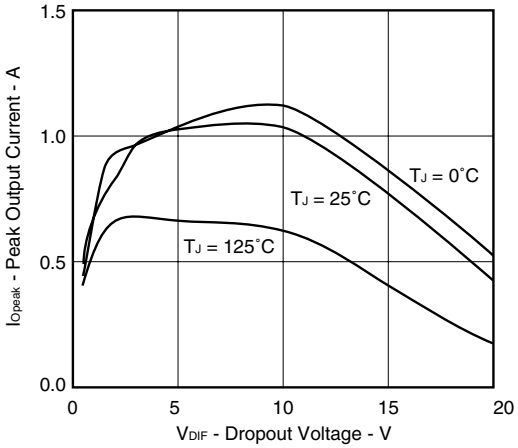
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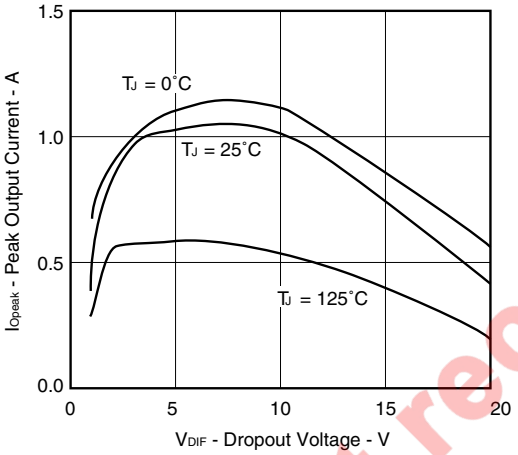
VDIF vs. TJ



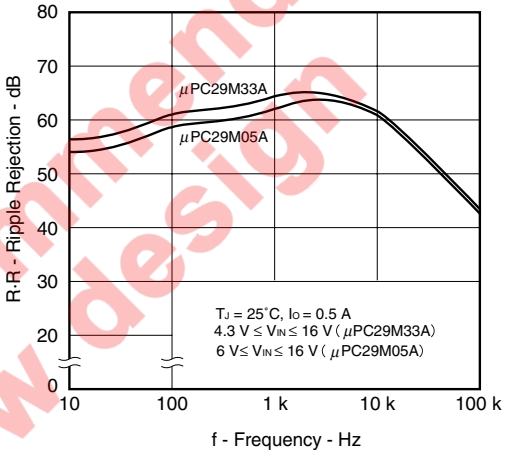
IOpeak vs. VDIF (μPC29M33A)



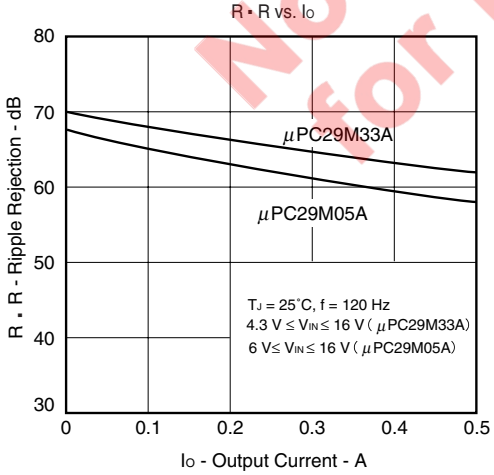
IOpeak vs. VDIF (μPC2905A)



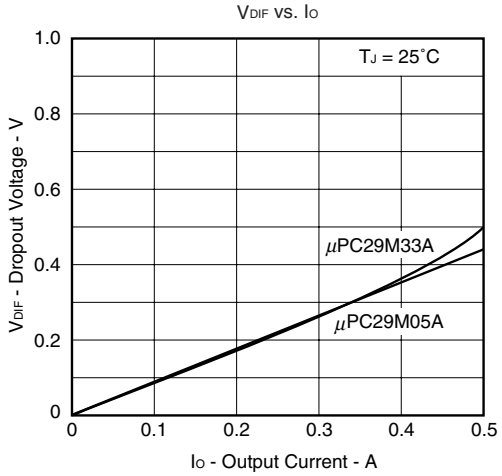
R·R vs f



R - R vs. IO

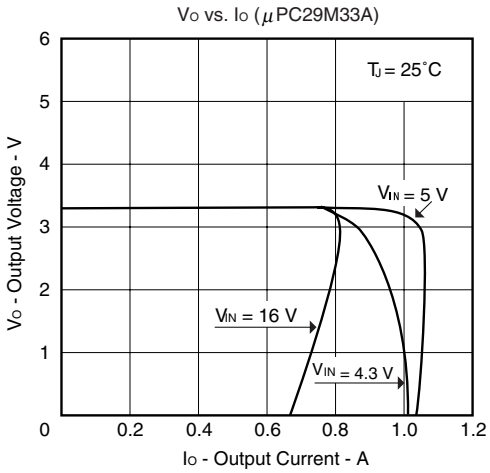


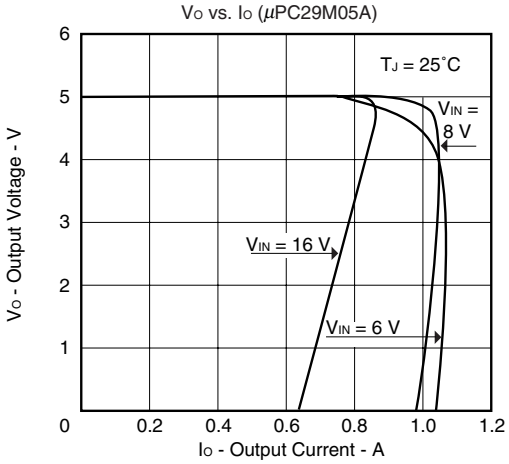
VDIF vs. IO



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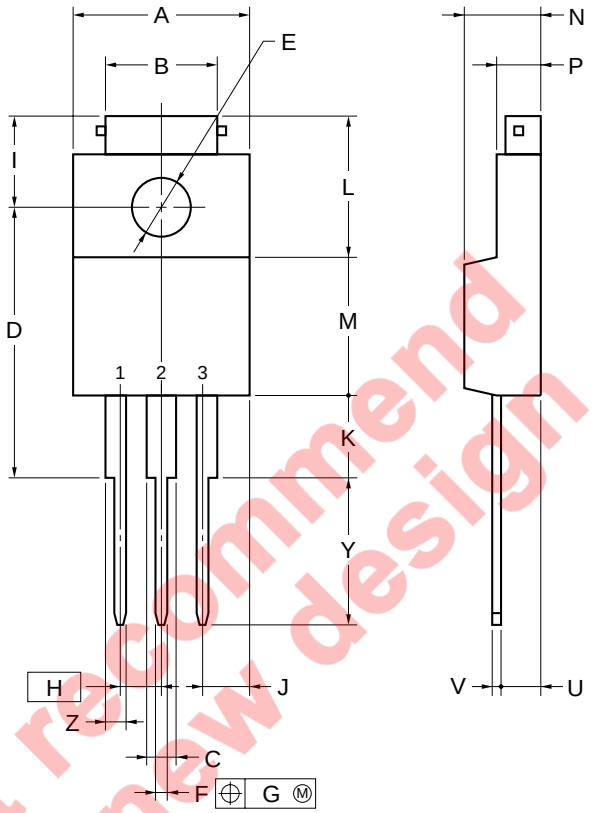
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PACKAGE DRAWINGS

μPC29M33AHF, μPC29M05AHF

3PIN PLASTIC SIP (MP-45G)



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NOTE

Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.

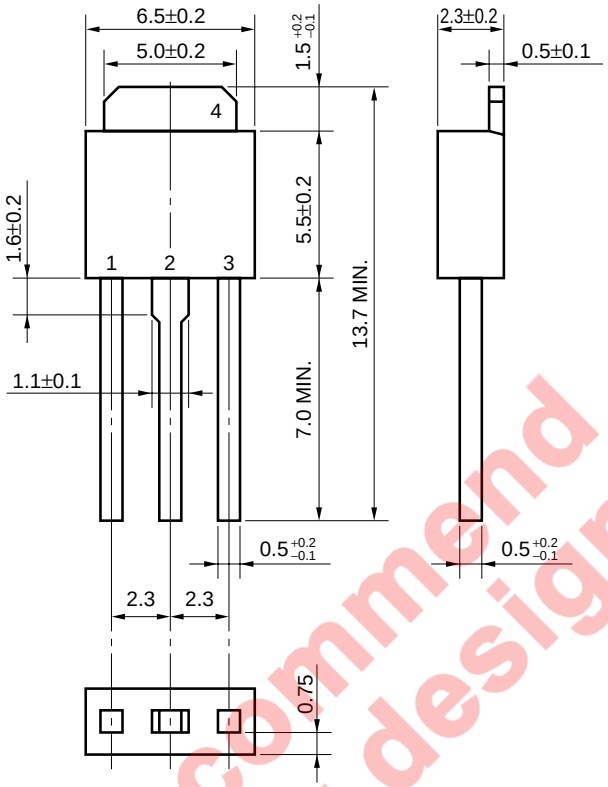
ITEM	MILLIMETERS
A	10.0±0.2
B	7.0±0.2
C	1.50±0.2
D	17.0±0.3
E	φ3.3±0.2
F	0.75±0.10
G	0.25
H	2.54 (T.P.)
I	5.0±0.3
J	2.46±0.2
K	5.0±0.2
L	8.5±0.2
M	8.5±0.2
N	4.5±0.2
P	2.8±0.2
U	2.4±0.5
V	0.65±0.10
Y	8.9±0.7
Z	1.30±0.2

P3HF-254B-4

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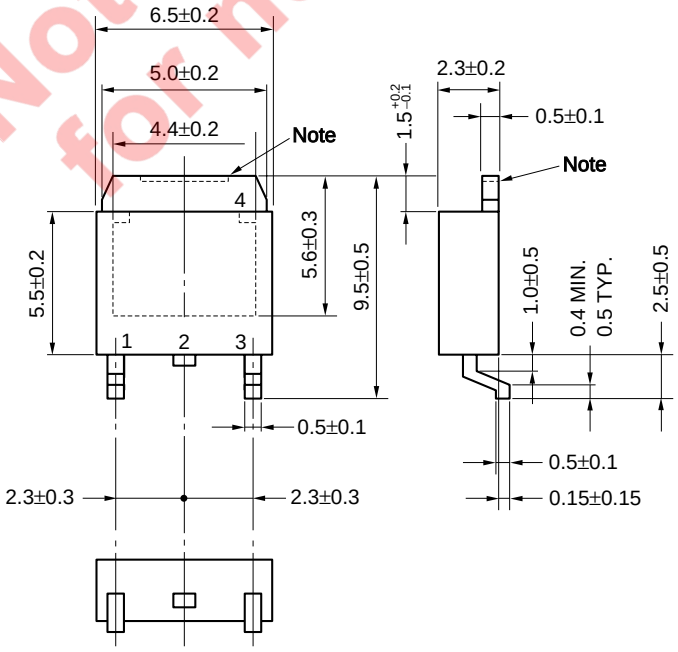
μPC29M33AHB, μPC29M05AHB

SC-64 (MP-3) (Unit: mm)



μPC29M33AT, μPC29M05AT

<R> **SC-63 (MP-3Z) (Unit: mm)**



Note The depth of notch at the top of the fin is from 0 to 0.2 mm.

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<R> RECOMMENDED SOLDERING CONDITIONS

The μPC29M33A, 29M05A should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

Surface Mount Device

μPC29MxxAT Series: SC-63 (MP-3Z)

Process	Conditions	Symbol
Infrared Ray Reflow	Peak temperature: 235°C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210°C or higher), Maximum number of reflow processes: 3 times or less.	IR35-00-3
Vapor Phase Soldering	Peak temperature: 215°C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200°C or higher), Maximum number of reflow processes: 3 times or less.	VP15-00-3
Partial Heating Method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (Per each side of the device).	P350

μPC29MxxAT-AZ Series[Note1], μPC29MxxAT-AY Series[Note2]: SC-63 (MP-3Z)

Process	Conditions	Symbol
Infrared Ray Reflow	Peak temperature: 260°C or below (Package surface temperature), Reflow time: 60 seconds or less (at 220°C or higher), Maximum number of reflow processes: 3 times or less.	IR60-00-3
Partial Heating Method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (Per each side of the device).	P350

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Notes 1. Pb-free (This product does not contain Pb in the external electrode.)

2. Pb-free (This product does not contain Pb in the external electrode, Sn100% plating.)

Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

Remark Flux: Rosin-based flux with low chlorine content (chlorine 0.2 Wt% or below) is recommended.

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Through-hole devices

μPC29MxxAHF Series, μPC29MxxAHF-AZ Series [Note1]: Isolated TO-220 (MP-45G)
μPC29MxxAHB Series, μPC29MxxAHB-AZ Series [Note1], μPC29MxxAHB-AY Series [Note2]: SC-64 (MP-3)

Process	Conditions	Symbol
Wave soldering (only to leads)	Solder temperature: 260°C or below, Flow time: 10 seconds or less.	WS60-00-1
Partial heating method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (Per each pin).	P350

Notes 1. Pb-free (This product does not contain Pb in the external electrode.)

2. Pb-free (This product does not contain Pb in the external electrode, Sn100% plating.)

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

NOTES ON USE

When the μPC29M33A, 29M05A are used with an input voltage that is lower than the value indicated in the recommended operating conditions, a high quiescent current flows through the device due to saturation of the transistor of the output stage. (Refer to the "**I_{BIAS} ($I_{BIAS(S)}$) vs. V_{IN} curves in TYPICAL CHARACTERISTICS**").

These products have saturation protector, but a current of up to 80 mA MAX. may flow through the device. Thus the power supply on the input side must have sufficient capacity to allow this quiescent current to pass when the device starts up.

REFERENCE DOCUMENTS

USER'S MANUAL USAGE OF THREE TERMINAL REGULATORS	Document No.G12702E
REVIEW OF QUALITY AND RELIABILITY HANDBOOK	Document No.C12769E
INFORMATION VOLTAGE REGULATOR OF SMD	Document No.G11872E
SEMICONDUCTOR DEVICE MOUNT MANUAL	http://www.necel.com/pkg/en/mount/index.html

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