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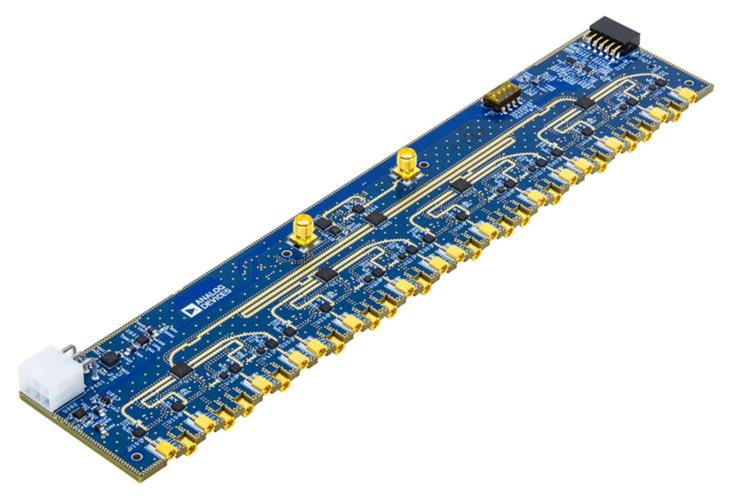
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This version (14 Sep 2021 16:44) was *approved* by Chas Frick [https://ez.analog.com/members/cfrick].

The Previously approved version (/resources/eval/user-guides/quadmxfe/calboard?rev=1631121481) (08 Sep 2021 19:18) is available.

# **Calibration Board**

# Splitter/Combiner, Calibration, Power Analyzer

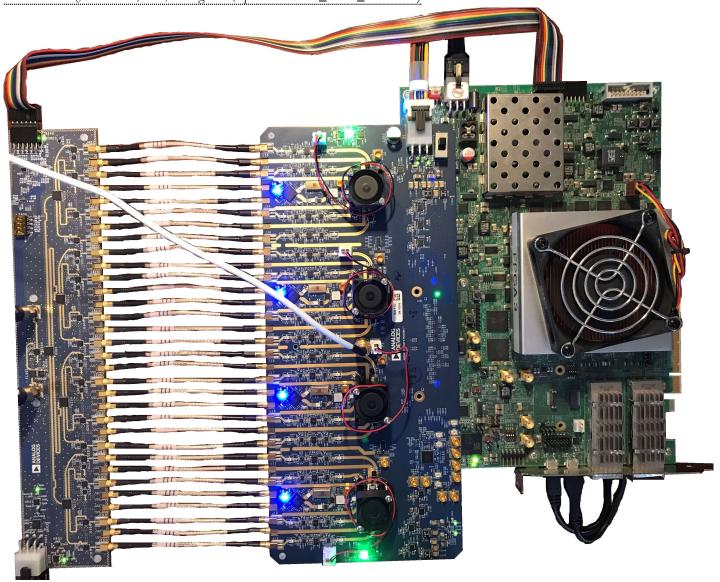


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### **Features**

- Mates to Quad-MxFE Digitizing Card & VCU118 PMOD Interface (Cable Included)
- MATLAB Control Enables System-Level Calibration Algorithm Development
- Provides Both Individual Adjacent Channel Loopback and Combined Channel Loopback Options
- Combined Tx Channels Out Via SMA Option
- Combined Rx Channels In Via SMA Option
- On-Board Log Power Detectors With AD5592R Output To VCU118 Over PMOD
- On-Board Power Regulation from Single 12V Power Adapter (Included)

Below is the full integrated system including the Xilinx VCU118 [https://www.xilinx.com/VCU118], ADQUADMXFE1EBZ, and ADQUADMXFE-CAL (/resources/eval/user-guides/quadmxfe/calboard) in full operation. For LED identification please see Calibration Broad LED Information (/resources/eval/user-guides/quadmxfe/calboard#led\_identification), or Quad MxFE LED Information (/resources/eval/user-guides/quadmxfe#led\_status\_indicators)

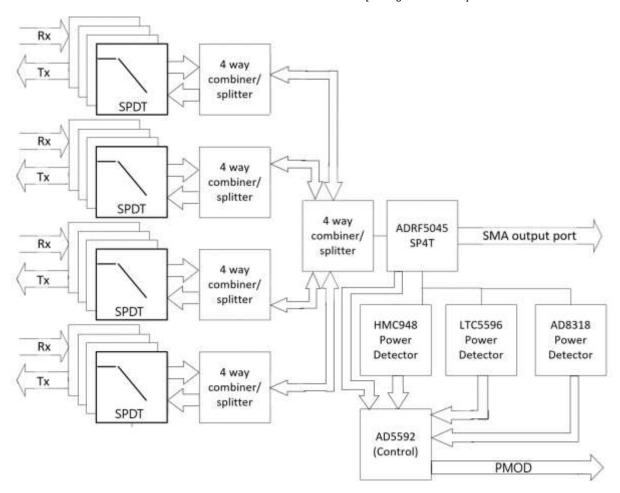


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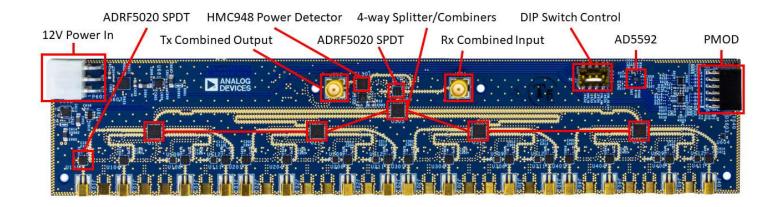
# **General Description**

This page serves to inform system engineers and software developers about the calibration board addition kit that pairs with the Quad-MxFE [https://www.analog.com/en/design-center/evaluation-hardware-and-software/evaluation-boards-kits/Quad-MxFE.html]. This board directly aligns with the 16 Rx and 16 Tx ports on the output of the Quad MxFE and includes power detectors and loopback configurations for the system-level calibration of the Quad MxFE. The board contains a PMOD 2A interface that connects the VCU118 [https://www.xilinx.com/VCU118] Evaluation Board from Xilinx®. A DIP switch and multi-channel ADC/DAC,AD5592R [https://www.analog.com/AD5592R], are also included for additional control and power readings. The goal of this kit is to enable users to demonstrate combined-channel dynamic range, spurious, and phase noise improvements, develop system-level calibration algorithms, and demonstrate immediate phase determinism.

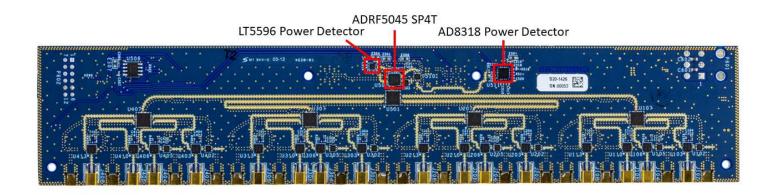
### High Level Block Diagram



## **Key Component Location**



(/\_detail/resources/eval/user-guides/quadmxfe/cal\_brd\_labels.png?id=resources%3Aeval%3Auser-guides%3Aquadmxfe%3Acalboard)



(/\_detail/resources/eval/user-guides/quadmxfe/cal\_brd\_labels2.png?id=resources%3Aeval%3Auser-guides%3Aquadmxfe%3Acalboard)

#### **LED** Identification

LED Status Indicators	
LED Ref Des	Function
DS601	1.9V @ 1.2mA Good From ADM7150 (LDO)

LED Status Indicators	
LED Ref Des	Function
DS602	1.9V @ 1.2mA Good From ADM7150 (LDO)
DS603	3.3V_FPGA Good From VCU118 PMOD 2A

# **Equipment Needed**

- PMOD Cable
  - Cable Assembly [https://www.samtec.com/products/idsd-06-d-15.00]
- 12V (150W) Wall Supply
  - Power Brick Assembly [https://www.digikey.com/scripts/DkSearch/dksus.dll?KeywordSearch?Site=US&Keywords=TE150A1251F01]
- 2x SMA Cables (50 Ω)
- MMCX to SMA cables
- MMCX to MMCX cables
  - Cable Assembly [https://www.samtec.com/products/rf316-03sp1-03sp1-0100]
- Board Standoff
  - Standoff Assembly [https://www.mcmaster.com/catalog/127/3467/]
- Static Shielding Bag
- · Packaging (Box, Foam)
- Startup Documentation
- VCU118 FPGA Board (optional)
  - VCU118 [https://www.xilinx.com/VCU118]
- Quad MxFE Board (optional)
  - Quad MxFE [https://www.analog.com/en/design-center/evaluation-hardware-and-software/evaluation-boards-kits/Quad-MxFE.html]

### **Control Interfaces**

The calibration board can be controlled in multiple ways including a DIP Switch and 2A PMOD Interface. The board can be controlled with 4 main signals: 5045 V1 and 5045 V2 which control an ADRF5045 which switches the transmit path between combined loopback, low frequency path, high frequency path, and power detection path, CTLR IND which turns off and on adjacent individual loopback for each Rx and Tx channel, and CTRL RX COMB which turns off and on the combined receive loopback path. The DIP Switch controls 4 signals: 5045 V1, 5045 V2, CTRL IND, and CTRL RX COMB. The PMOD Interface has 8 signals: 5045 V1, 5045 V2, CTRL IND, CTRL RX COMB, and 4 SPI (Serial Peripheral Interface) Communications signals. The AD5592R is an 8 channel GPIO (General Purpose Input/Output)/ADC/DAC and it is connected the same control signals as the DIP switch as well as the three power detectors (AD8318 [https://www.analog.com//en/products/ad8318.html], HMC948 [https://www.analog.com//en/products/hmc948.html], LTC5596 [https://www.analog.com//en/products/ltc5596.html]), plus a temperature sensor on the AD8318. Below is a tabular representation of these control signals.

Calibration Board Status	CLTR IND	5045 V1	5045 V2	CLTR RX COMB
Combined Tx into Combined Rx Loopback	1	1	1	0
Adjacent Individual Loopback		Х	Х	Х
Combined Tx to J502		0	0	х
Combined Rx from J501		Х	1	1
Combined Tx path to LTC5596 Detector		0	1	Х
Combined Tx path to AD8318 Detector	1	1	0	Х

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guides/quadmxfe/capture.png?id=resources%3Aeval%3Auser-guides%3Aquadmxfe%3Acalboard)

### Software Needed

The following link contains the MATLAB driver for controlling the 16 Tx / 16 Rx Calibration board via the VCU118 PMOD interface. The driver may be used to configure the Quad MxFE platform in a Combined Loopback, Individual Loopback, Combined Tx to SMA, Combined Rx from SMA, Combined Tx Power reading, and Power readings from the AD5592R [https://www.analog.com//en/products/ad5592r.html] and subsequent (AD8318 [https://www.analog.com//en/products/ad8318.html], HMC948 [https://www.analog.com//en/products/hmc948.html], LTC5596 [https://www.analog.com//en/products/ltc5596.html]).

Cal Board VCU118 PMOD (/resources/eval/user-guides/quadmxfe/quickbringup#calboardvcu118m)

### **Related Documents**

### Related Parts Pages

- AD5592R [https://www.analog.com/ad5592r] 8 Channel, 12-Bit, Configurable ADC/DAC with on-chip Reference, SPI (Serial Peripheral Interface) interface
- HMC948 [https://www.analog.com/hmc948] 54 dB (decibel) Logarithmic Detector SMT, 1 23 GHz (Gigahertz)
- LTM5596 [https://www.analog.com/ltc5596] 100MHz to 40GHz Linear-in-dB (decibel) RMS Power Detector with 35dB Dynamic Range
- AD8318 [https://www.analog.com/ad8318] 1 MHz (megahertz) TO 8 GHz (Gigahertz), 70 dB (decibel) Logarithmic Detector/Controller
- ADP5020 [https://www.analog.com/adp5020] Power Management Unit for Imaging Modules
- AD5045 [https://www.analog.com/ad5045] Fully Accurate 14-Bit VOUT nanoDAC® SPI (Serial Peripheral Interface) Interface 4.5 V (volt) to 5.5 V (volt) in a TSSOP

#### **Schematic**

 $\bullet \quad ADQUADMXFE-CAL\ (/\_media/resources/eval/user-guides/quadmxfe/02-064888-01-b\_2\_pdf)$ 

resources/eval/user-guides/quadmxfe/calboard.txt · Last modified: 14 Sep 2021 16:44 by Chas Frick [https://ez.analog.com/members/cfrick]

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