TOSHIBA Photocoupler IRED & Photo-IC

TLP250

Industrial Inverter
Inverter For Air Conditioner
IGBT Gate Drive
Power MOS FET Gate Drive

The TOSHIBA TLP250 consists of an infrared emitting diode and a integrated photodetector.

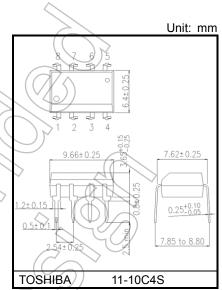
This unit is 8-lead DIP package.

TLP250 is suitable for gate driving circuit of IGBT or power MOS FET.

- Input threshold current: 5mA(max)
- Supply current : 11mA(max)
- Supply voltage: 10-35V
- Output current : ±1.5A (max)
- Switching time tpLH/tpHL): 0.5µs(max)
- Isolation voltage: 2500V_{rms}(min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A
 File No.E67349

VDE-Approved: EN 60747-5- 5 (Note 1)

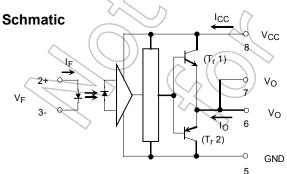
Note 1: When a VDE approved type is needed, please designate the **Option(D4)**.



Weight: 0.54 g (typ.)

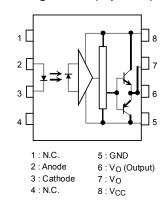
Truth Table

		TM	Tr2
Input LED	On//	On	Off
	Off	Øff	On



A 0.1µF bypass capcitor must be connected between pin 8 and 5

Pin Configuration (top view)



Start of commercial production 1990-11

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Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit
	Forward current	lF	20	mA	
	Forward current derating (Ta ≥ 70°C)	Δl _F / ΔTa	-0.36	mA / °C	
	Peak transient forward curent	IFPT	1	Α	
LED	Reverse voltage	VR	5	V	
_	Diode power dissipation		PD	40	mW
	Diode power dissipation derating (Ta≥70°C)		ΔP _D /°C	-0.72	mW / °C
	Junction temperature	Tj	125	°C	
	"H"peak output current (P _W ≤ 2.5µs,f ≤ 15kHz)	JOPH (// 1.5	Α	
	"L"peak output current (Pw ≤ 2.5µs,f ≤ 15kHz)	(Note 2)	IOPL	41/.5	А
Output voltag	Custout voltage	(Ta ≤ 70°C)	Va	35	V
	Output voltage	(Ta ≤ 85°C)	Vo	24	V
	Cumply voltage	(Ta ≤ 70°C)		35	
Detector	Supply voltage	(Ta ≤ 85°C)	Vcc	24	
Ď	Output voltage derating (Ta ≥ 70°C)		Δ√ο / ΔΤα	-0.73	V1.℃
	Supply voltage derating (Ta ≥ 70°C)		ΔV _{CC} / ΔTa	-0.73	V/°C
	Power dissipation		Pc	800	/mW
	Power dissipation derating (Ta ≥ 70°C)	200	ΔP _C / °C	-14.5	mW / °C
	Junction temperature	4()	Tj	125	°C
Opera	ating frequency	f	25	kHz	
Opera	ating temperature range	T _{opr} ///	-20 to 85	°C	
Stora	ge temperature range	Tstg	-55 to 125	°C	
Lead	soldering temperature (10 s)	Tsol	260	°C	
Isolat	ion voltage (AC, 60 s., R.H.≤ 60 %)	BVs	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width Pw ≤ 1 µs, 300 pps

Note 2: Exporenential waveform

Note 3: Exporenential wavefom, IOPH \leq -1.0 A(\leq 2.5 μ s), IOPL \leq +1.0 A(\leq 2.5 μ s)

Note 4: Device considerd a two terminal device: Pins 1, 2, 3 and 4 shorted together, and pins 5, 6, 7 and 8 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Input current, on	IF(ON)	7	8	10	mA
Input voltage, off	VF(OFF)	0	_	0.8	V
Supply voltage	Vcc	15	_	30	V
Peak output current	IOPH/IOPL	_	_	±0.5	Α
Operating temperature	Topr	-20	25	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Note: A ceramic capacitor $(0.1 \ \mu F)$ should be connected from pin 8 to pin 5 to stabilize the operation of the high gain linear amplifier. Failure to provide the bypassing may impair the switching proparty. The total lead length between capacitor and coupler should not exceed 1 cm.

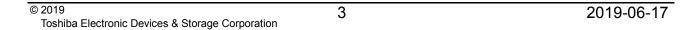
Note: Input signal rise time(fall time)<0.5 µs.

Electrical Characteristics (Ta = -20 to 70°C, unless otherwise specified)

Characte	eristic	Symbol	Test Cir- cuit	Test Condition	Min	Typ.*	Max	Unit	
Input forward voltage		VF	l	I _F = 10 mA, Ta = 25 °C	_	1.6	1.8	V	
Temperature coefficient of forward voltage		ΔV _F / ΔTa	_	IF = 10 mA		-2.0	_	mV / °C	
Input reverse current	1	IR	_	V _R = 5 V, Ta = 25 °C	7		10	μΑ	
Input capacitance		Ст	_	V = 0 V, f = 1 MHz , Ta = 25 °C		45	250	pF	
Output ourront	"H" level	ІОРН	1	V _{CC} = 30 V	-0.5	-1.5	_	А	
Output current	"L" level	IOPL	2	(Note 1) $I_F = 0 \text{ mA}$ $V_{6-5} = 2.5 \text{ V}$	0.5	2	_	A	
Output voltage	"H" level	Voh	3	V _{CC1} = +15 V, V _{EE1} = -15 V R _L = 200 Ω, I _F = 5 mA	11	12.8	1//	V	
	"L" level	V _{OL}	4	V _{CC1} = +15 V, V _{EE1} = -15 V R _L = 200 Ω, V _F = 0.8 V		-14.2	-12.5		
	"H" level	Іссн	-	V _{CC} = 30 V, I _F = 10mA Ta = 25 °C	\\	2	_		
				$V_{CC} = 30 \text{ V, I}_{F} = 10 \text{ mA}$		\ <u></u>	11		
Supply current	"L" level	ICCL		V _{CC} = 30 V, I _F = 0 mA Ta = 25 °C		7.5	ı	mA	
				V _{CC} = 30 V, I _F = 0 mA	(X)	_	11		
Threshold input current	"Output L→H"	lFLH	4	V _{CC1} = +15 V, V _{EE1} = -15 V R _L = 200 Ω, V _O > 0 V	_	1.2	5	mA	
Threshold input voltage	"Output H→L"	VFHL		V _{CC1} = +15 V, V _{EE1} = -15 V R _L = 200 Ω, V _O < 0 V	0.8	_	-	V	
Supply voltage		Vcc) [<u> </u>	10	_	35	V	
Capacitance (input-output)		Cs) _	V _S = 0 V, f = 1 MHz Ta = 25 °C	_	1.0	2.0	pF	
Resistance(input-output)			V _S = 500 V , Ta = 25 °C R.H.≤ 60 %	1×10 ¹²	10 ¹⁴	_	Ω		

^{*} All typical values are at Ta = 25°C

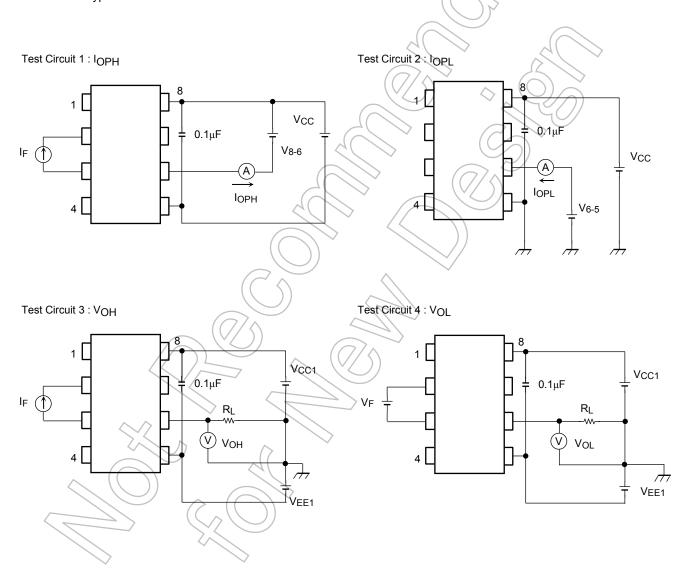
Note 1: Duration of IO time ≤ 50µs



Switching Characteristics (Ta = -20 to 70°C, unless otherwise specified)

Characteristic		Symbol	Test Cir- cuit	Test Condition	Min	Тур.	Max	Unit
Propagation	L→H	tpLH	5	$\begin{array}{l} \text{IF} = 8 \text{ mA} \\ \text{VCC1} = +15 \text{ V}, \text{VEE1} = -15 \text{ V} \\ \text{RL} = 200 \ \Omega \end{array}$	_	0.15	0.5	μs
delay time	H→L	tpHL			_	0.15	0.5	
Common mode transient immunity at high level output		СМн	- 6	V _{CM} = 600 V, I _F = 8 mA V _{CC} = 30 V, Ta = 25 °C	-5000	12	ı	V / µs
Common mode transient immunity at low level output		CML	0	V _{CM} = 600 V, I _F = 0 mA V _{CC} = 30 V, Ta = 25 °C	5000			V / µs

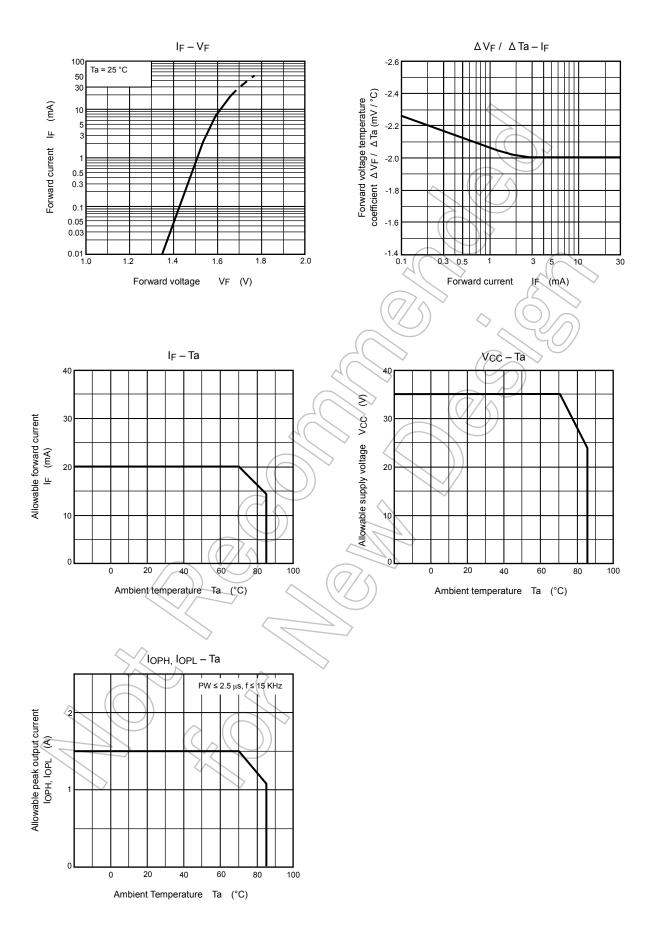
Note: All typical values are at Ta = 25°C



Test Circuit 5: tpLH, tpHL, tr tf tf 0.1μF VCC1 80% Vон ှ Vo R_L Vo GND H80% = V_{OL} t_pLH tpHL V_{EE1} Test Circuit 6: CMH, CML 0.1μF SW V_{CC} -o Vo Vсм 600V 90% Vсм 10% 480 (V) CM_L = t_{r (μs)} SW: A(IF=8mA) 480 (V) СМн CM_H = 26V Vo CH_L/ SW: B(I_F=0)

CML(CMH) is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the low (high) state.

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NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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