



# PMEG3050CEP

30 V, 5 A low VF Schottky barrier rectifier

10 March 2025

Product data sheet

## 1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- Average forward current:  $I_{F(AV)} \leq 5 \text{ A}$
- Reverse voltage:  $V_R \leq 30 \text{ V}$
- Low forward voltage
- High power capability due to clip-bond technology
- Small and flat lead SMD plastic package
- Suitable for both reflow and wave soldering

## 3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$I_{F(AV)}$	average forward current	$\delta = 0.5$ ; $f = 20 \text{ kHz}$ ; square wave; $T_{sp} \leq 151 \text{ }^\circ\text{C}$		-	-	5	A
$V_R$	reverse voltage	$T_j = 25 \text{ }^\circ\text{C}$		-	-	30	V
$V_F$	forward voltage	$I_F = 5 \text{ A}$ ; pulsed; $T_j = 25 \text{ }^\circ\text{C}$	[1]	-	480	560	mV
$I_R$	reverse current	$V_R = 30 \text{ V}$ ; pulsed; $T_j = 25 \text{ }^\circ\text{C}$	[1]	-	35	100	$\mu\text{A}$

[1] Very short pulse, in order to maintain a stable junction temperature.

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		
2	A	anode	 CFP5 (SOD128)	 sym001

[1] The marking bar indicates the cathode.

**nexperia**

## 6. Ordering information

**Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
PMEG3050CEP	CFP5	plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	<a href="#">SOD128</a>

## 7. Marking

**Table 4. Marking codes**

Type number	Marking code
PMEG3050CEP	GD

## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_R$	reverse voltage	$T_j = 25 \text{ }^\circ\text{C}$		-	30	V
$I_{F(AV)}$	average forward current	$\delta = 0.5$ ; $f = 20 \text{ kHz}$ ; square wave; $T_{sp} \leq 151 \text{ }^\circ\text{C}$		-	5	A
$I_{FSM}$	non-repetitive peak forward current	$t_p = 8.3 \text{ ms}$ ; half-sine wave; $T_{j(init)} = 25 \text{ }^\circ\text{C}$		-	50	A
$P_{tot}$	total power dissipation	$T_{amb} \leq 25 \text{ }^\circ\text{C}$	[1]	-	750	mW
			[2]	-	1.25	W
$T_j$	junction temperature			-	175	$^\circ\text{C}$
$T_{amb}$	ambient temperature			-55	175	$^\circ\text{C}$
$T_{stg}$	storage temperature			-65	175	$^\circ\text{C}$

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

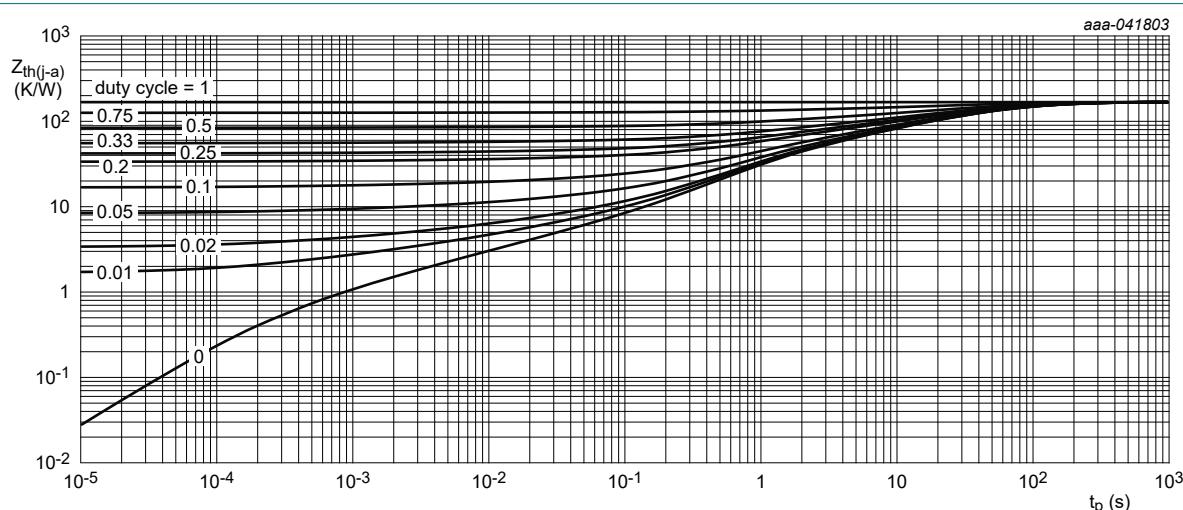
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode  $1 \text{ cm}^2$ .

## 9. Thermal characteristics

**Table 6. Thermal characteristics**

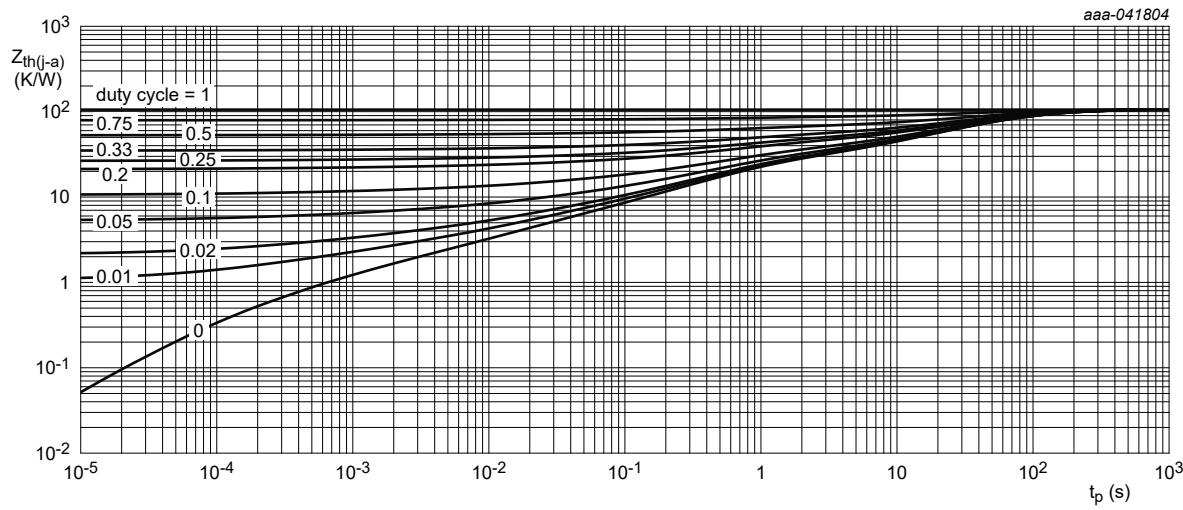
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	200	K/W
			[3] [2]	-	-	120	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[4]	-	-	12	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.  
 [2] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses  $P_R$  are a significant part of the total power losses.  
 [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.  
 [4] Soldering point of cathode tab.



FR4 PCB, standard footprint

**Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

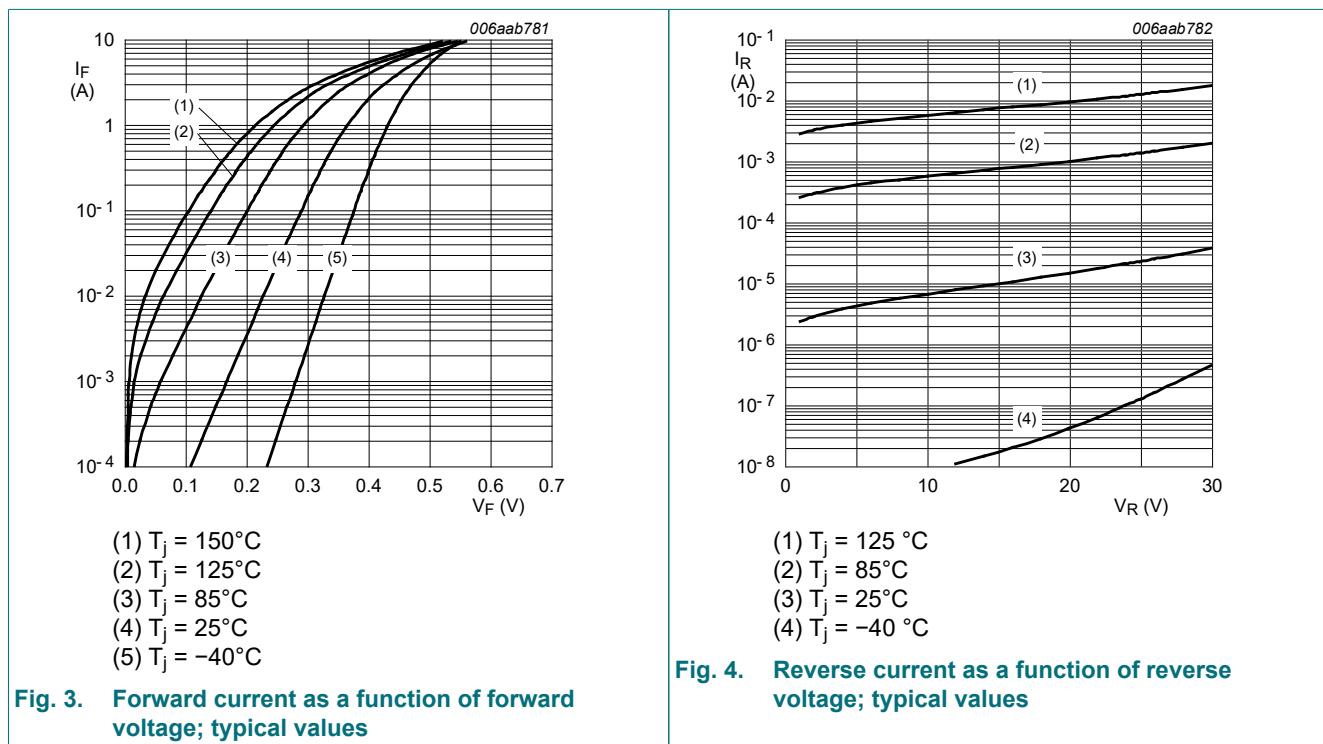
**Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**

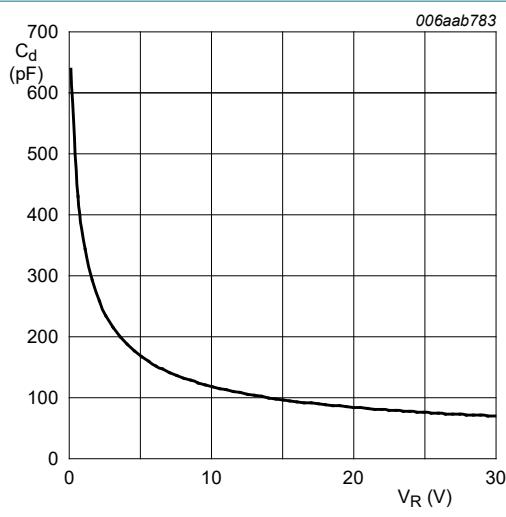
## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$V_F$	forward voltage	$I_F = 1 \text{ A}; \text{ pulsed}; T_j = 25^\circ\text{C}$	[1]	-	365	420	mV
		$I_F = 5 \text{ A}; \text{ pulsed}; T_j = 25^\circ\text{C}$	[1]	-	480	560	mV
		$I_F = 5 \text{ A}; \text{ pulsed}; T_j = -40^\circ\text{C}$	[1]	-	530	-	mV
		$I_F = 5 \text{ A}; \text{ pulsed}; T_j = 125^\circ\text{C}$	[1]	-	420	-	mV
$I_R$	reverse current	$V_R = 10 \text{ V}; \text{ pulsed}; T_j = 25^\circ\text{C}$	[1]	-	6	-	$\mu\text{A}$
		$V_R = 30 \text{ V}; \text{ pulsed}; T_j = 25^\circ\text{C}$	[1]	-	35	100	$\mu\text{A}$
$C_d$	diode capacitance	$V_R = 1 \text{ V}; f = 1 \text{ MHz}; T_j = 25^\circ\text{C}$		-	340	-	pF
		$V_R = 10 \text{ V}; f = 1 \text{ MHz}; T_j = 25^\circ\text{C}$		-	120	-	pF
$t_{rr}$	reverse recovery time ramp recovery	$dI_F/dt = 200 \text{ A}/\mu\text{s}; I_F = 6 \text{ A}; V_R = 26 \text{ V}; T_j = 25^\circ\text{C}$		-	9.7	-	ns
$I_{RM}$	peak reverse recovery current			-	0.9	-	A
$Q_{rr}$	reverse recovery charge			-	5	-	nC

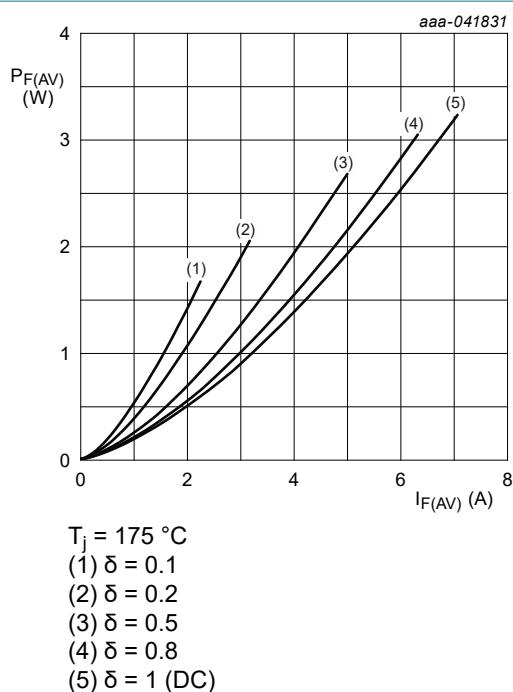
[1] Very short pulse, in order to maintain a stable junction temperature.





$f = 1 \text{ MHz}$ ;  $T_{\text{amb}} = 25 \text{ }^{\circ}\text{C}$

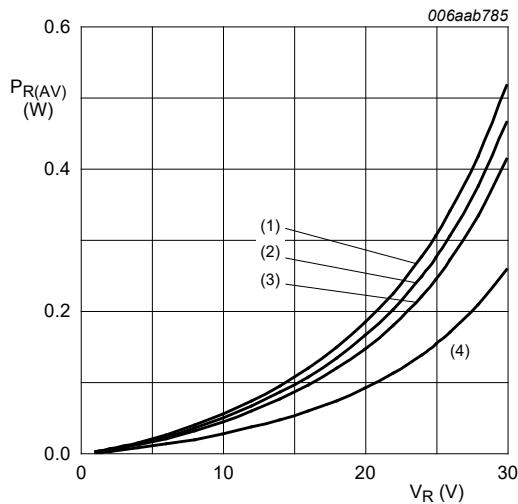
**Fig. 5. Diode capacitance as a function of reverse voltage; typical values**



$T_j = 175 \text{ }^{\circ}\text{C}$

- (1)  $\delta = 0.1$
- (2)  $\delta = 0.2$
- (3)  $\delta = 0.5$
- (4)  $\delta = 0.8$
- (5)  $\delta = 1 \text{ (DC)}$

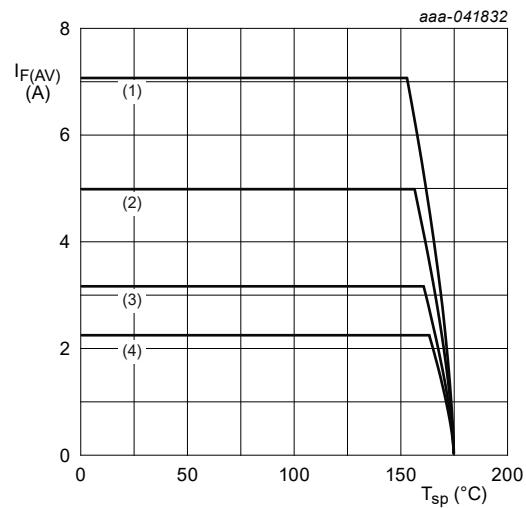
**Fig. 6. Average forward power dissipation as a function of average forward current; typical values**



$T_j = 125 \text{ }^{\circ}\text{C}$

- (1)  $\delta = 1$
- (2)  $\delta = 0.9$
- (3)  $\delta = 0.8$
- (4)  $\delta = 0.5$

**Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values**



$T_j = 175 \text{ }^{\circ}\text{C}$

- (1)  $\delta = 1 \text{ (DC)}$
- (2)  $\delta = 0.5; f = 20 \text{ kHz}$
- (3)  $\delta = 0.2; f = 20 \text{ kHz}$
- (4)  $\delta = 0.1; f = 20 \text{ kHz}$

**Fig. 8. Average forward current as a function of solder point temperature; typical values**

## 11. Test information

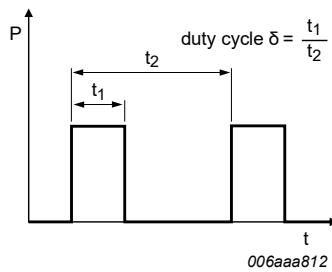


Fig. 9. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:

$$I_{F(AV)} = I_M \times \delta \text{ with } I_M \text{ defined as peak current}$$

$$I_{RMS} = I_{F(AV)} \text{ at DC}$$

$$I_{RMS} = I_M \times \sqrt{\delta} \text{ with } I_{RMS} \text{ defined as RMS current}$$

## 12. Package outline

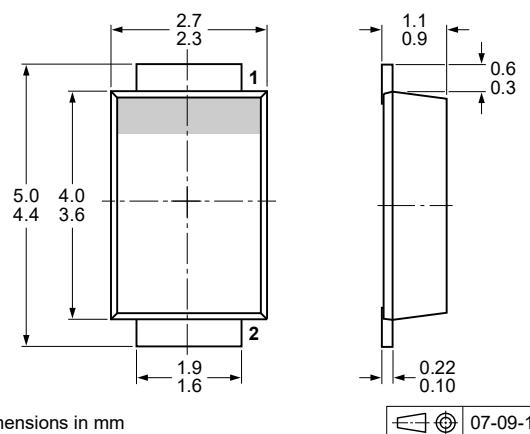


Fig. 10. Package outline CFP5 (SOD128)

## 13. Soldering

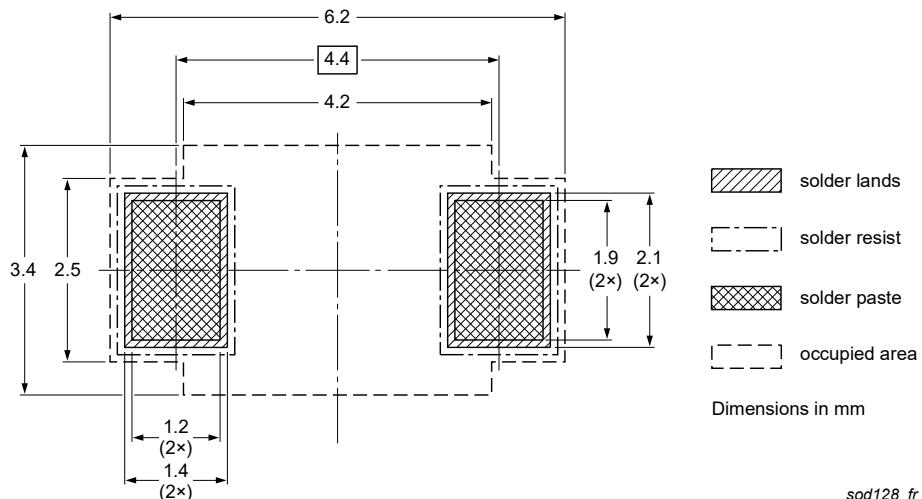
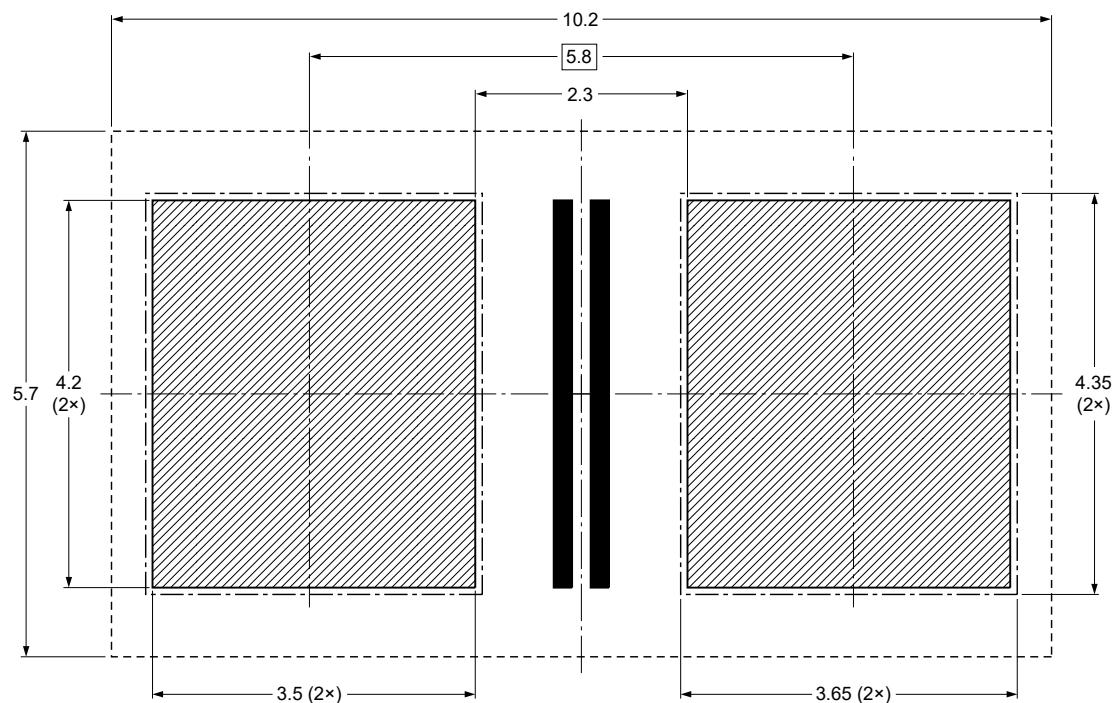


Fig. 11. Reflow soldering footprint for CFP5 (SOD128)

## Wave soldering footprint information

SOD128



----- | occupied area

solder resist

A square with diagonal hatching, representing a solder pad, followed by the text "solder lands".

dummy track (solder resist and Cu free)

Dimensions in mm

Issue date 17-06-06  
17-06-07

*sod128 fw*

**Fig. 12. Wave soldering footprint for CFP5 (SOD128)**

## 14. Revision history

**Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG3050CEP v.1	20250310	Product data sheet	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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