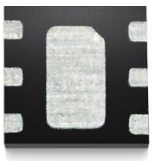
DATA SHEET

RELEASE A DATA SHEET



GRF2133

ULTRA-HIGH GAIN Low Noise Amplifier 0.1 to 4.2 GHz

FEATURES

- Internally Matched to 50 Ω
- Unconditionally Stable
- Flexible Biasing
- Process: GaAs pHEMT
- Compact 1.5 x 1.5 mm DFN-6 Package

Reference: 5 V / 70 mA / 700 MHz

- Gain: 40 dB
- OP1dB: 20 dBm
- OIP3: 31 dBm
- Evaluation Board Noise Figure: 0.7 dB

Reference: 5 V / 70 mA / 1950 MHz

- Gain: 28 dB
- OP1dB: 20 dBm
- OIP3: 31 dBm
- Evaluation Board Noise Figure: 0.6 dB

Reference: 5 V / 70 mA / 2500 MHz

- Gain: 23.5 dB
- OP1dB: 20 dBm
- OIP3: 30 dBm
- Evaluation Board Noise Figure: 0.75 dB

APPLICATIONS

- High Gain LNA
- Linear Driver Amplifiers
- Cellular Boosters/Repeaters

DESCRIPTION

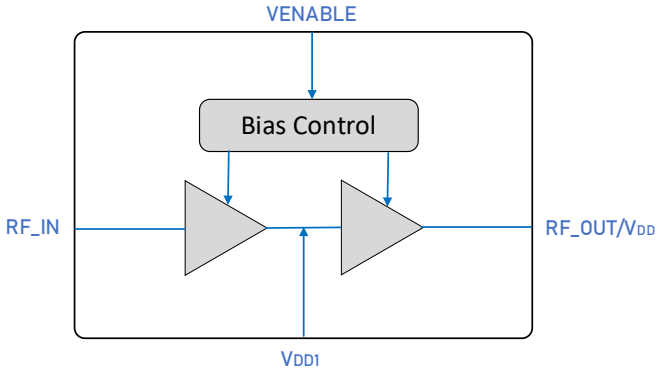
The GRF2133 is a broadband linear gain block featuring ultra-high gain and sub 0.85 dB noise figure (NF) for small cell, cellular boosters, wireless infrastructure and other high-performance applications.

Configured as a linear driver, LNA or cascaded gain block, it offers significant reuse potential both within a single design and across multiple platforms. The device is operated from a supply voltage of 1.8 to 5 volts with a selectable I_{DDQ} range of 35 to 120 mA for optimal efficiency and linearity.

Please consult with the GRF applications engineering team for custom tuning/evaluation board data, device S-parameters and for applications with V_{DD}< 2.7 volts.

Additional tunes can be found on the GRF2133 "Custom Tunes" product page: GRF2133 Custom Tunes

BLOCK DIAGRAM



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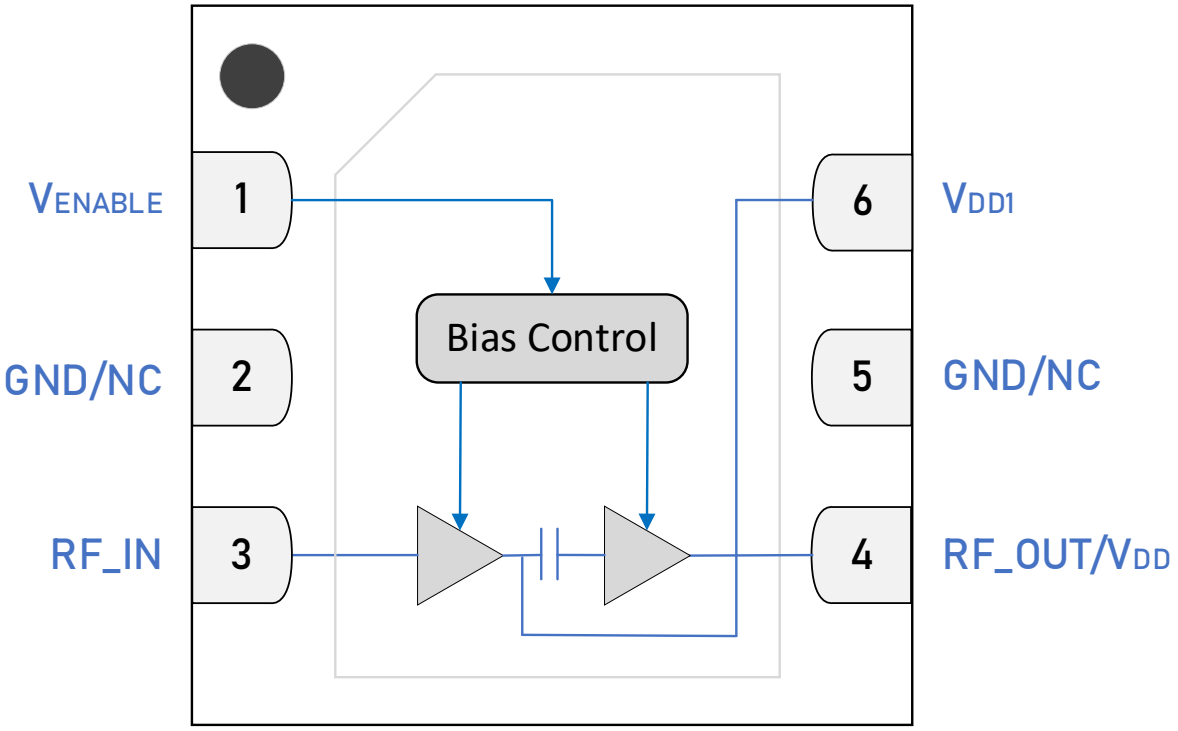

DFN-6 1.5 x 1.5 mm Pin Out (Top View)

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Pin Assignments

Pin	Name	Description	Note
1	V_{ENABLE}	Enable Voltage Input	V_{ENABLE} and series resistor sets I_{DDQ}. V_{ENABLE} < 0.2 volts disables the device. On-die pull-down resistor will turn the device off if this node is allowed to float.
2, 5	GND/NC	Ground or No Connect	No internal connection to die. We recommend connecting these pins to ground. Use a via as close to the pin as possible if grounded.
3	RF_IN	RF Input	An external DC blocking capacitor must be used.
4	RF_OUT/V_{DD}	RF Output	V_{DD} applied to this pin. An external DC blocking capacitor must be used.
6	V_{DD1}	Bias Supply	Typically tied to V_{DD} via an external resistor or an inductor (for V_{DD} < 4 volts). Tying to V_{DD} allows for the reuse of M8 for the required decoupling.
PKG BASE	GND	Ground	Provides DC and RF ground as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.

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Absolute Ratings

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V_{DD}	0	6	V
RF Input Power: Load VSWR < 2:1, V_{DD} = 5 V.	$P_{IN\ MAX}$		23	dBm
Operating Temperature (package base).	$T_{PKG\ BASE}$	-40	115	°C
Maximum Channel Temperature (MTTF > 10^6 hours).	T_{MAX}		170	°C
Maximum Dissipated Power	$P_{DISS\ MAX}$		700	mW

Electrostatic Discharge

Human Body Model	HBM	250		V

Storage

Storage Temperature	T_{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	--

Caution! ESD Sensitive Device

Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.
Note: For additional information, please refer to *Manufacturing Note MN-001 — Package and Manufacturing Information.*

All Guerrilla RF products are provided in RoHS compliant lead (Pb)-free packaging requiring no exemptions. Additional information for this topic can be found at this link - *Environmental and Restricted Substance Statement Library.*

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Recommended Operating Conditions

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Voltage	V_{DD}	0	5	6	V	
RF Frequency Range	F_{RF}	0.1	1.95	4.2	GHz	Typical application schematic with external matching components **(notes 1 & 2).**
Operating Temperature (package base)	$T_{PKG\ BASE}$	-40		115	°C	
RF_IN Port Impedance	Z_{RF_IN}		50		Ω	Single-ended.
RF_OUT Port Impedance	Z_{RF_OUT}		50		Ω	Single-ended.

Note 1: Operation outside of this range is supported by using different custom tunes. Examples of other optimized tunes can be found here: GRF2133 Custom Tunes

Note 2: Contact the Guerrilla RF applications team for guidance on optimizing the tuning of the device for alternative bands.

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Nominal Operating Parameters – General

The following conditions apply unless noted otherwise: typical application schematic using the 0.1 to 2.7 GHz tuning set. V_{DD} = 5 V, V_{ENABLE} = 5 V, I_{DDQ} = 70 mA, M5 = 4 kΩ, F_{TEST} = 1.95 GHz, 50 Ω system impedance, $T_{PKG\ BASE}$ = 25 °C. Evaluation board losses are included within the specifications.

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Current	I_{DD}		70		mA	V_{DD} = 5 V, V_{ENABLE} = 5 V.
Enable Current	I_{ENABLE}		2		mA	V_{DD} = 5 V, V_{ENABLE} = 5 V.
Switching Rise Time	T_{RISE}		10		µs	Disabled mode to Gain mode **(note 3)**.
Switching Fall Time	T_{FALL}		200		ns	Gain mode to Disabled mode **(note 4)**.

Disabled Mode

Leakage Current	$I_{LEAKAGE}$		1		µA	V_{DD} = 5 V, V_{ENABLE} = 0 V.

Thermal Data

Thermal Resistance (Infrared Scan)	Θ_{JC}		65		°C/W	On standard evaluation board.
Channel Temperature at +85 °C (reference package base).	$T_{CHANNEL}$		105		°C	V_{DD} = 5 V. No RF applied. P_{DISS} = 300 mW **(note 5)**.

Note 3: Switching Time: 50% of V_{ENABLE} to 90% of Pout.

Note 4: Switching Time: 50% of V_{ENABLE} to 10% of Pout.

Note 5: MTTF > 10^6 hours for $T_{CHANNEL}$ < 170 °C.

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Nominal Operating Parameters – RF

The following conditions apply unless noted otherwise: typical application schematic using the 0.1 to 2.7 GHz tuning set. V_{DD} = 5 V, V_{ENABLE} = 5 V, I_{DDQ} = 70 mA, M5 = 4 kΩ, F_{TEST} = 1.95 GHz, 50 Ω system impedance, $T_{PKG\ BASE}$ = 25 °C. Evaluation board losses are included within the specifications.

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Gain	S21	26.5	28		dB	V_{DD} = 5 V, V_{ENABLE} = 5 V.
Noise Figure	NF		0.6	0.8	dB	On standard evaluation board.
Output 3rd Order Intercept Point	OIP3		31		dBm	+2 dBm P_{OUT} per tone at 2 MHz spacing (1949 and 1951 MHz).
Output 1 dB Compression Power	OP1dB	18	20		dBm	

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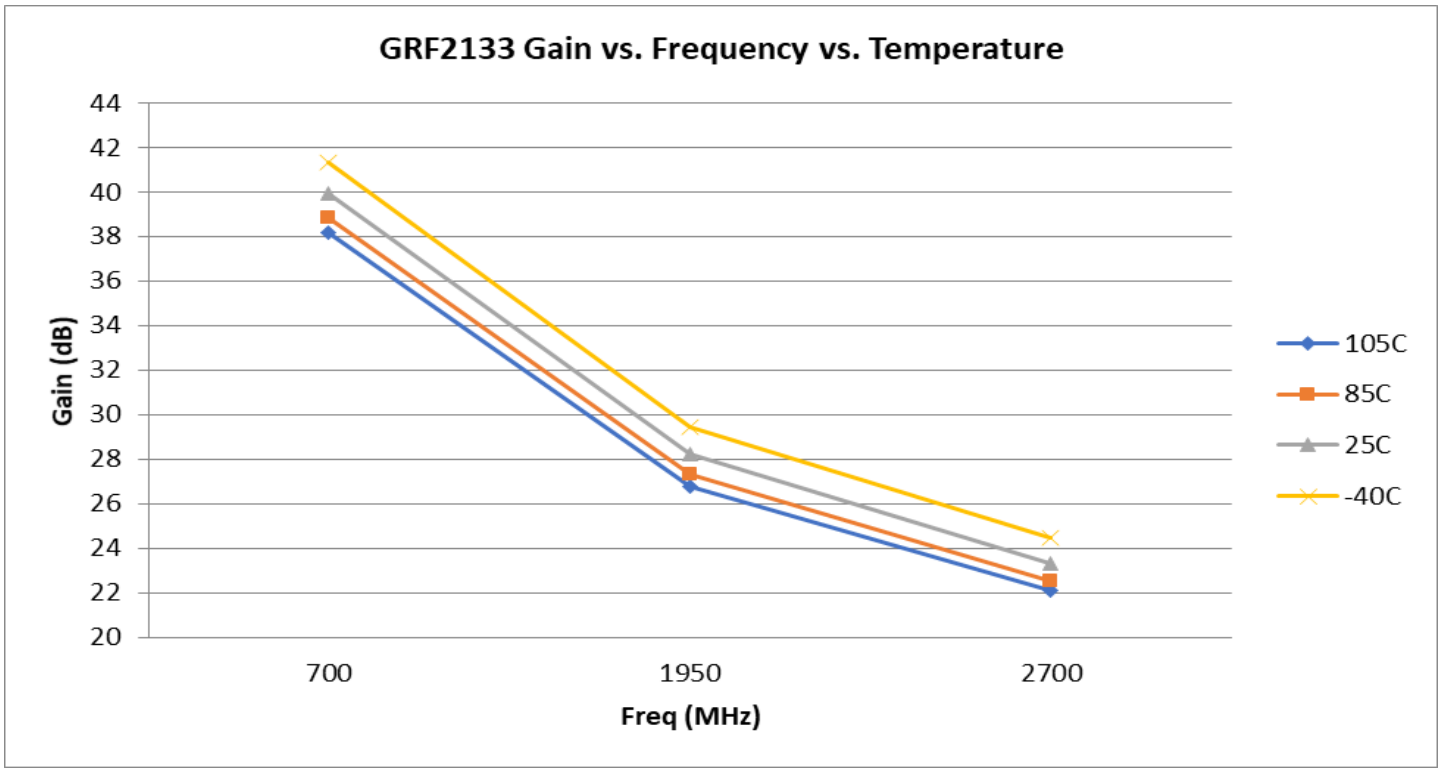
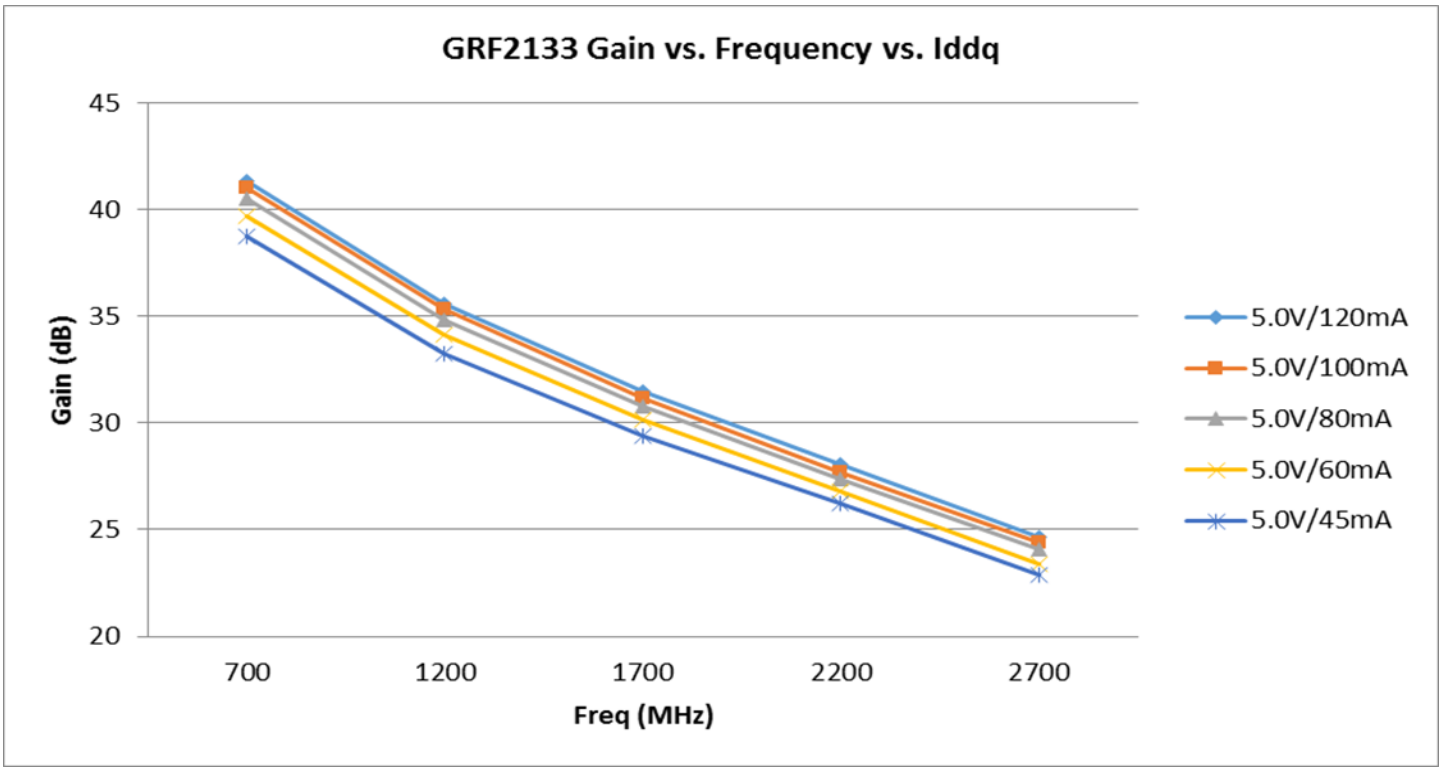
GRF2133 Typical Operating Curves: 0.1 to 2.7 GHz Tune





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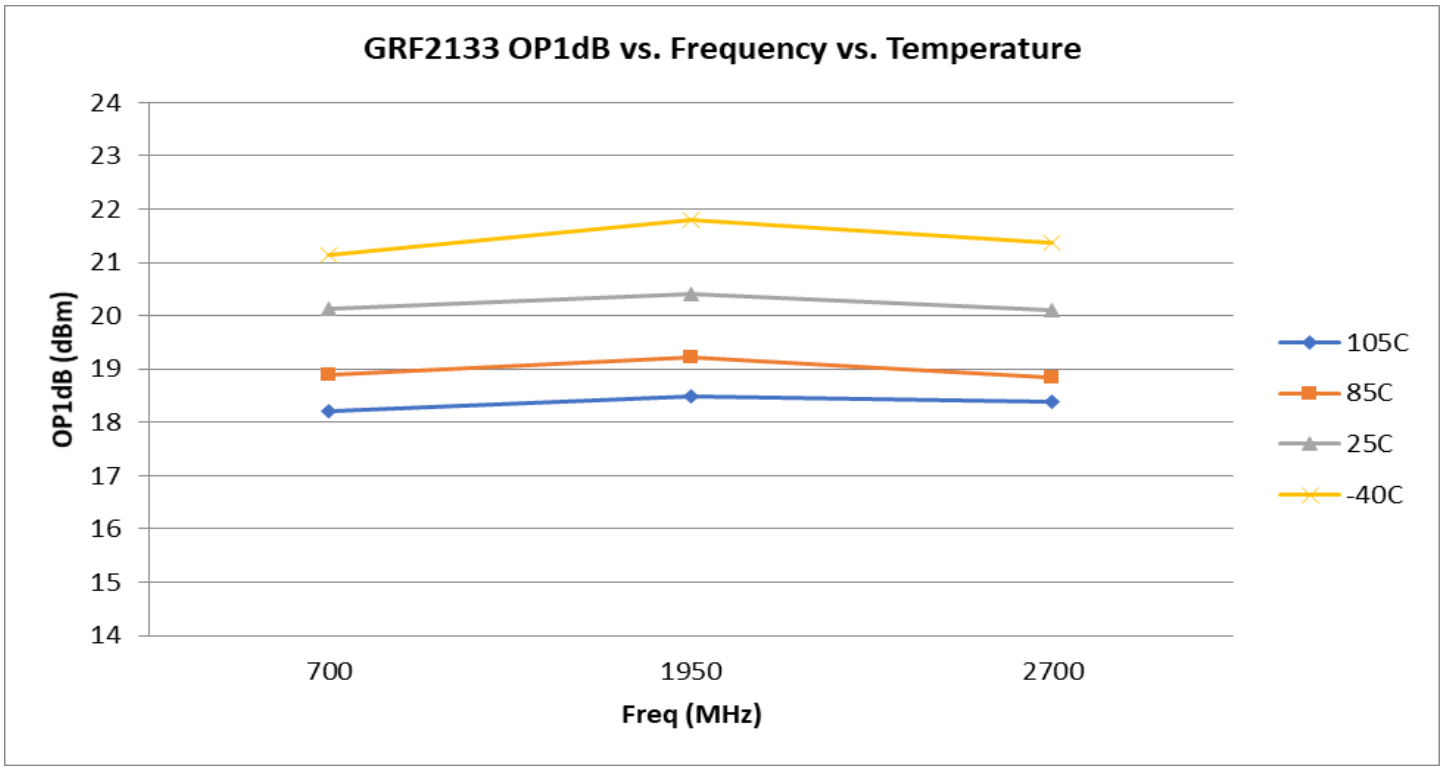
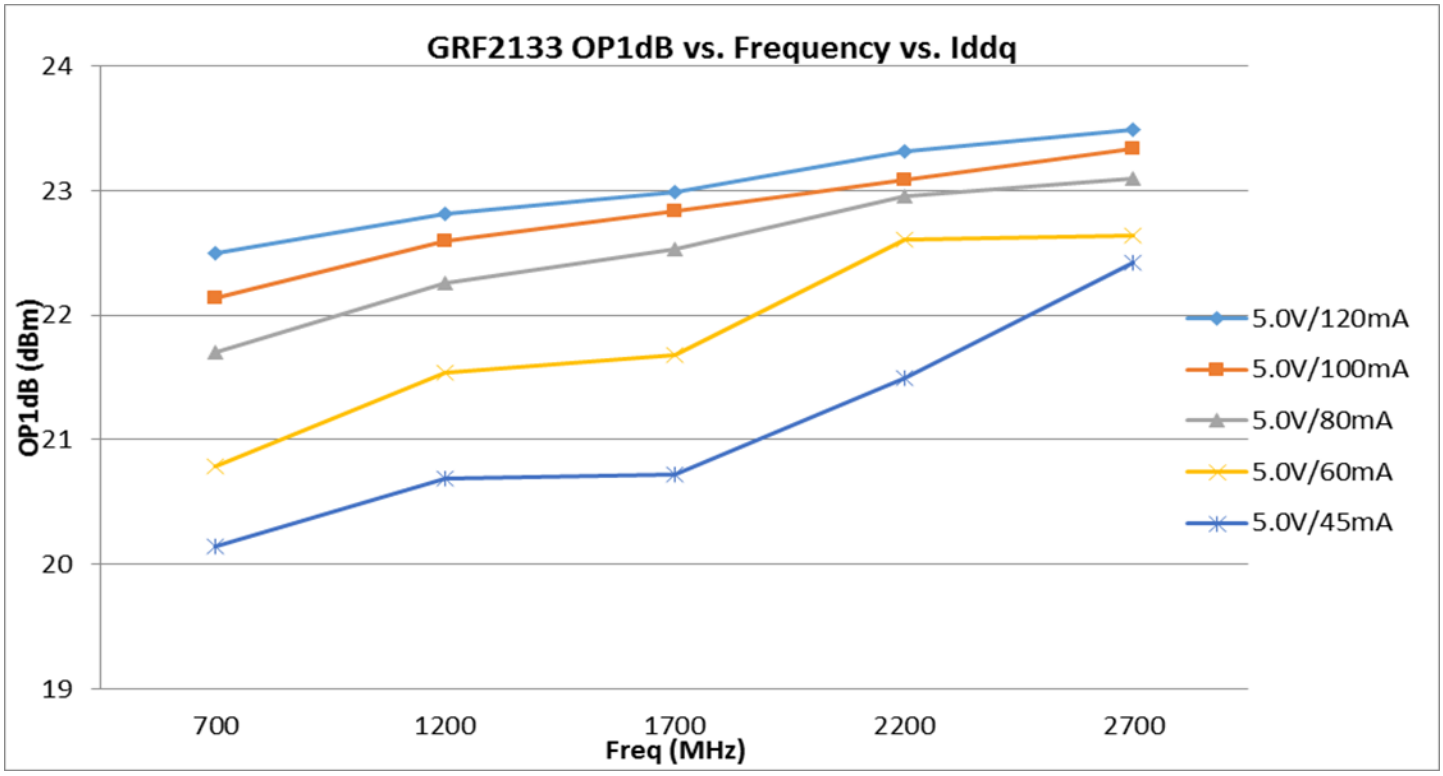
GRF2133 Typical Operating Curves: 0.1 to 2.7 GHz Tune





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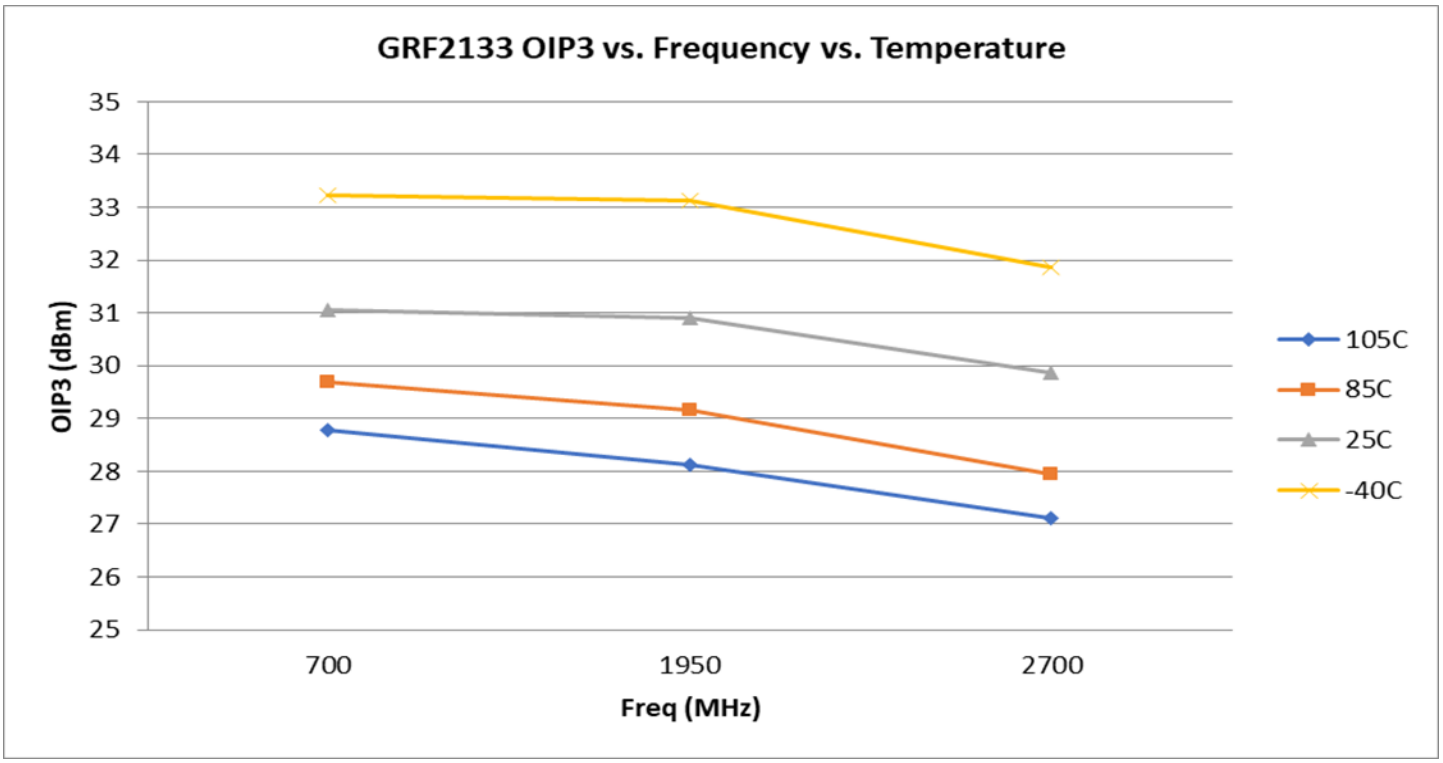
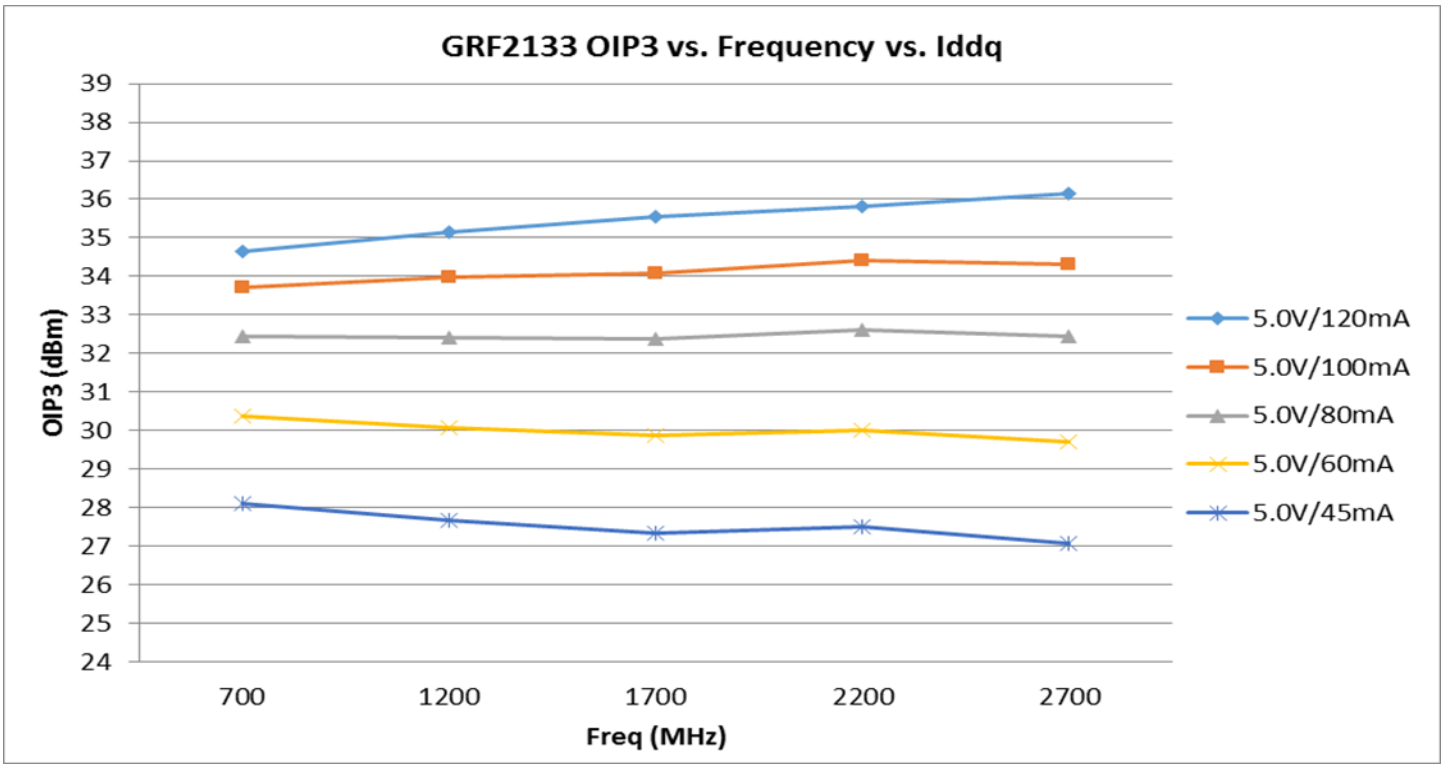
GRF2133 Typical Operating Curves: 0.1 to 2.7 GHz Tune





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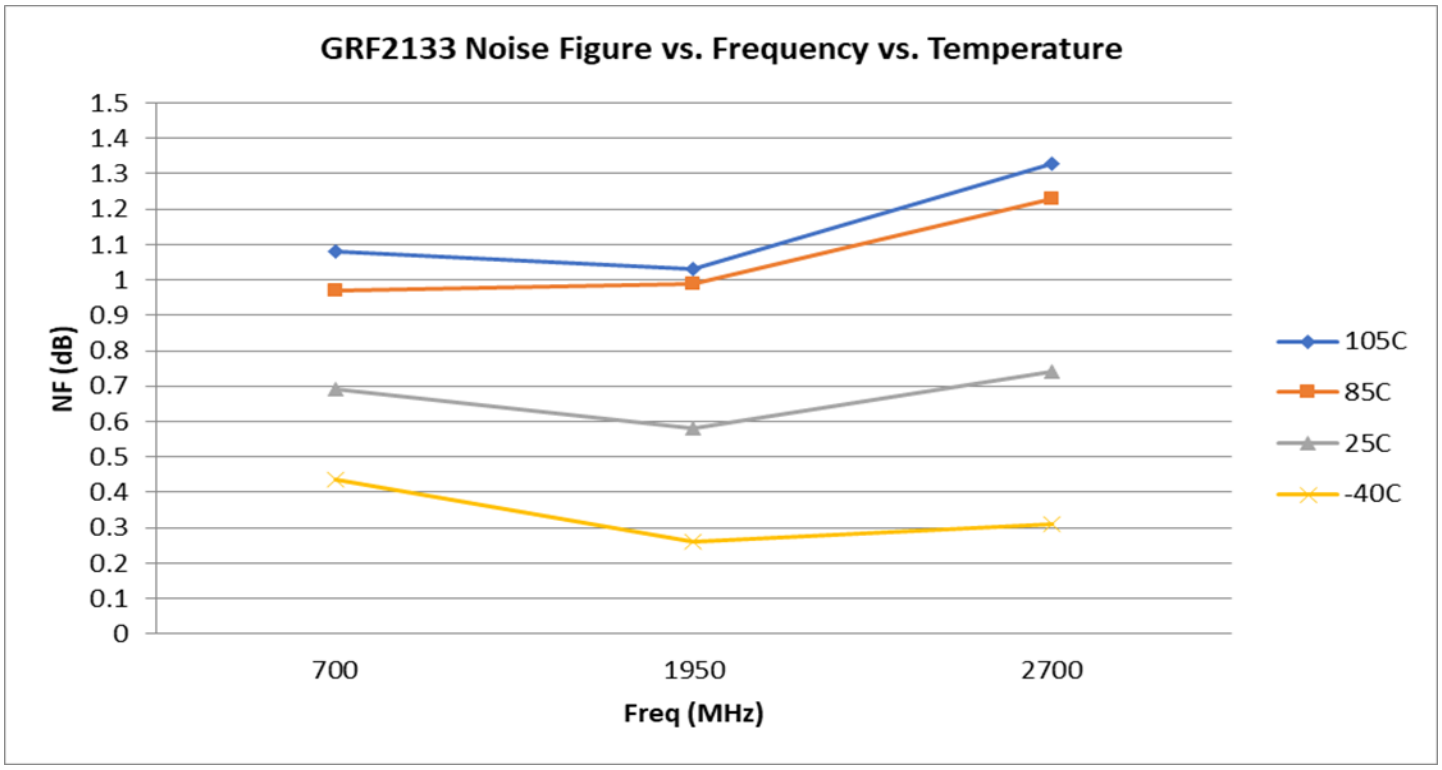
GRF2133 Typical Operating Curves: 0.1 to 2.7 GHz Tune



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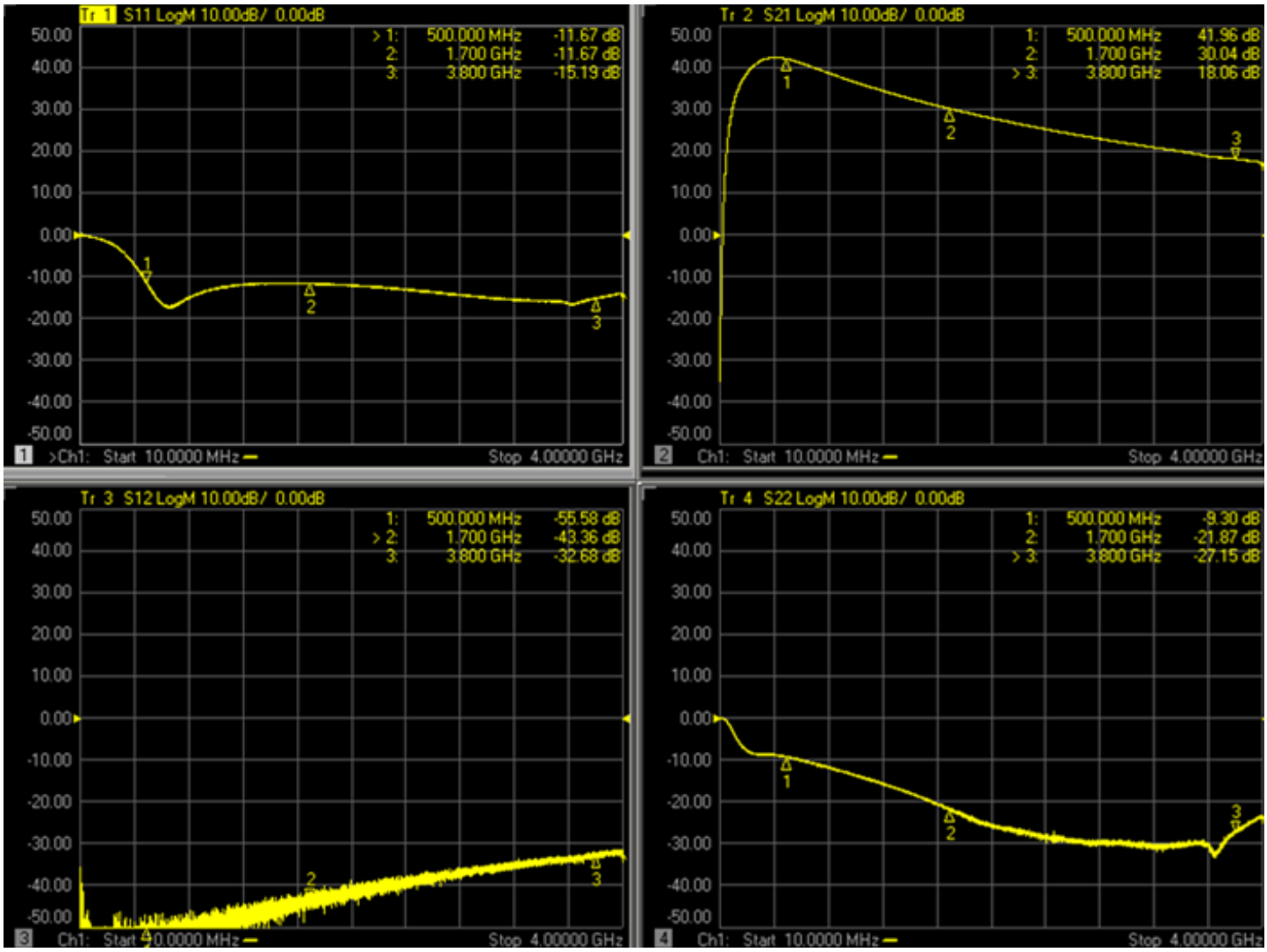
GRF2133 Typical Operating Curves: S-Parameters (0.1 to 2.7 GHz Tune)



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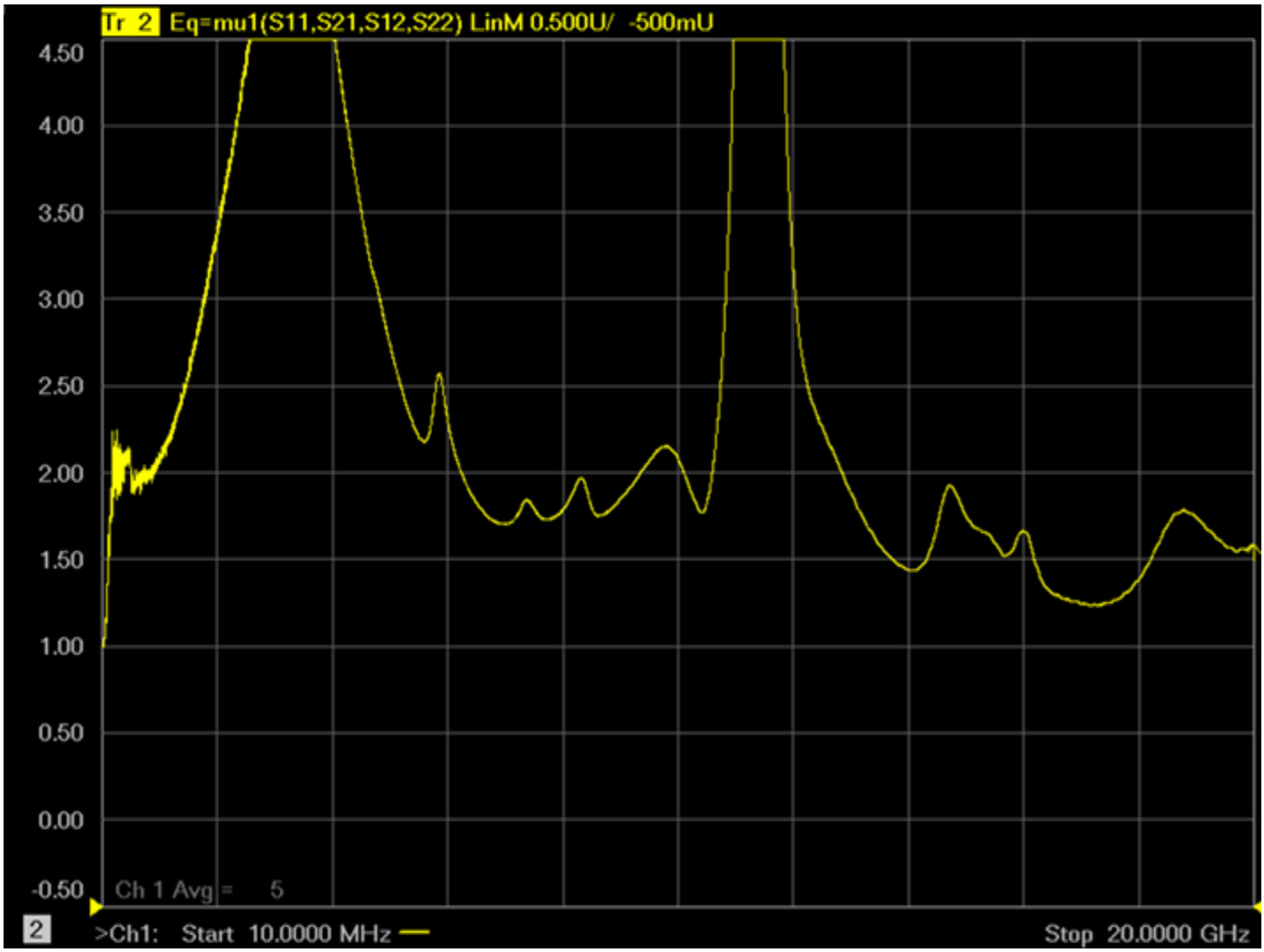
GRF2133 Typical Operating Curves: Stability Mu Factor (10 MHz to 20 GHz)



Note: Mu factor ≥ 1 implies unconditional stability.

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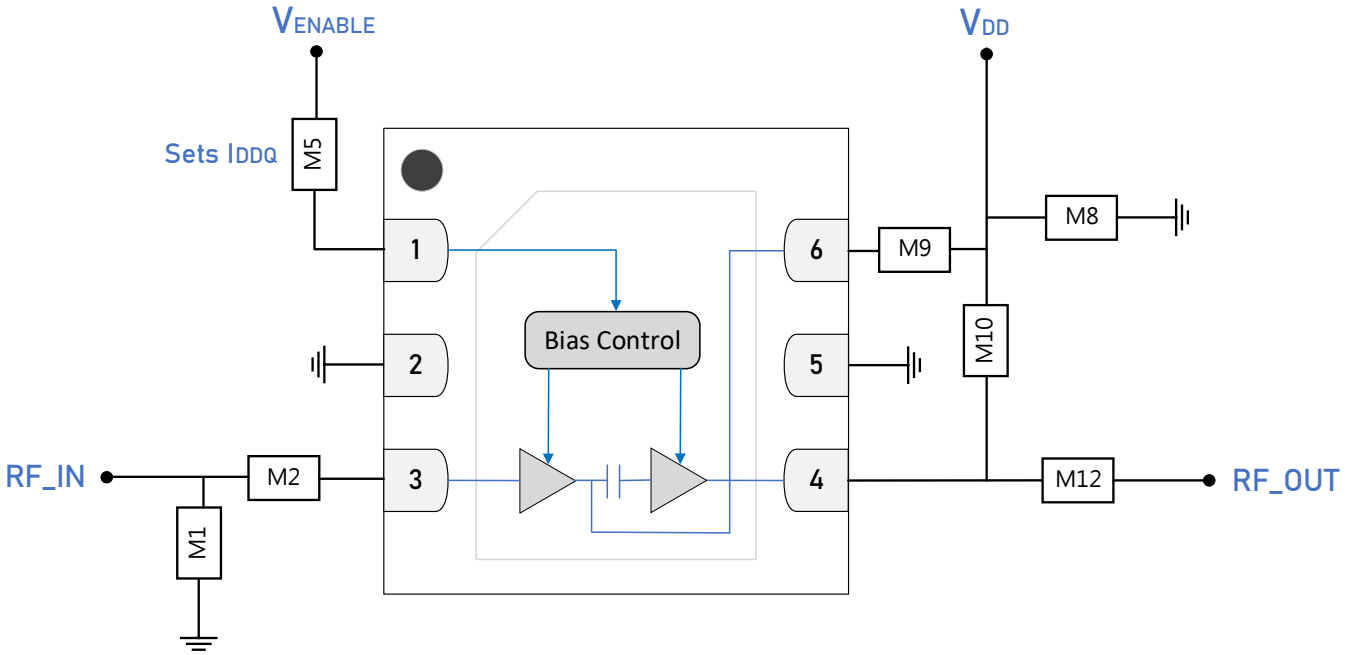
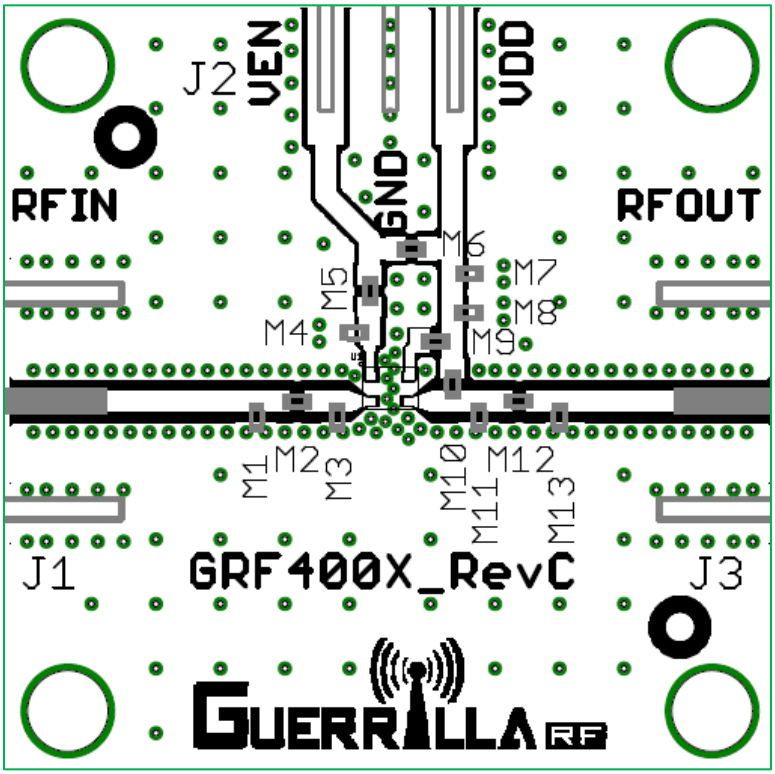


GRF2133 Standard Evaluation Board Schematic



GRF2133 Evaluation Board Assembly Diagram

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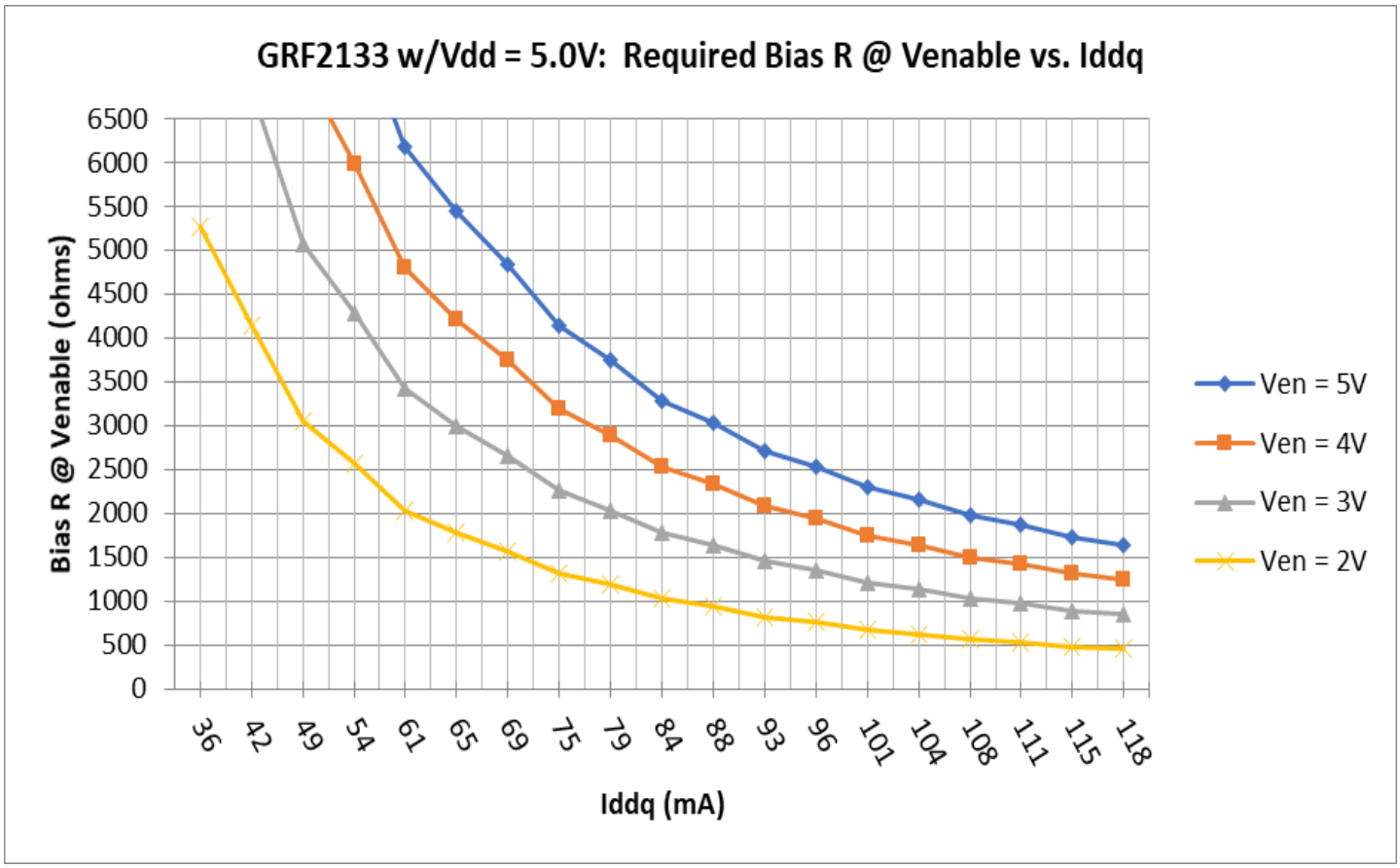
GRF2133 Evaluation Board Assembly Diagram Reference: 0.1 to 2.7 GHz Tune

Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M1	Inductor	Murata	LQG	18 nH	0402	ok
M2	Capacitor	Murata	GJM	33 pF	0402	ok
M5 (sets I_{DDQ})	Resistor	Various	5%	See Curves	0402	ok
M8	Capacitor	Murata	GRM	0.1 µF	0402	ok
M9	Resistor	Various	5%	75 Ω	0402	ok
M10	Inductor	Murata	LQG	33 nH	0402	ok
M12	Capacitor	Murata	GJM	33 pF	0402	ok
Evaluation Board	GRF400X_RevC					

GRF2133 Bias Resistor Selection Curves:



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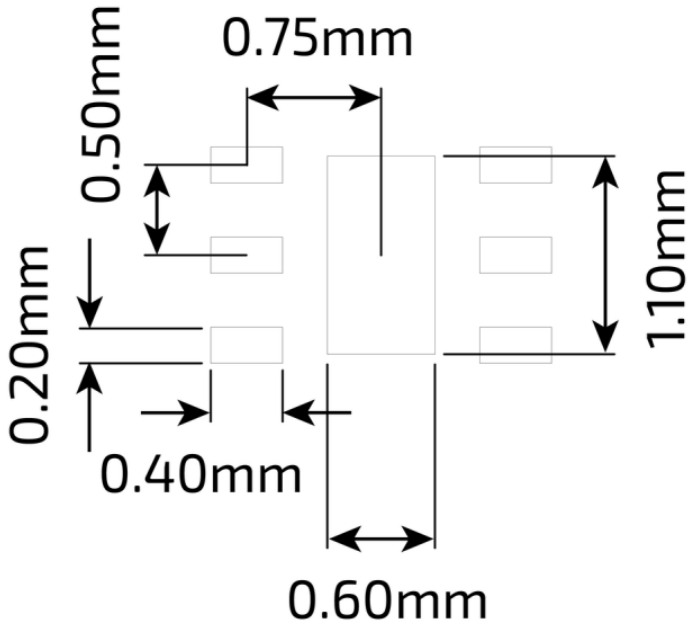
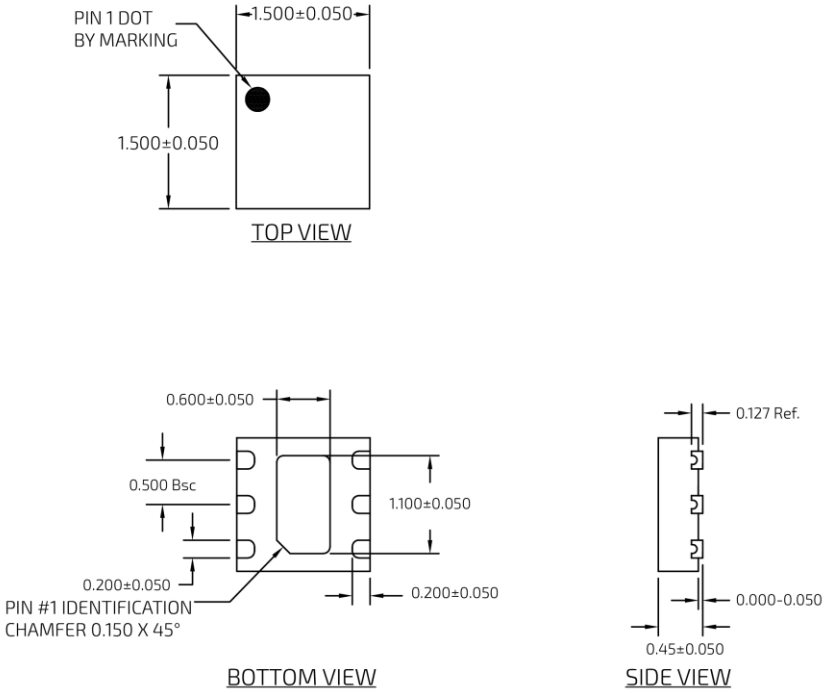


DFN-6 1.5 x 1.5 mm Suggested PCB Footprint (Top View)



DFN-6 1.5 x 1.5 mm Package Dimensions

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Package Marking Diagram

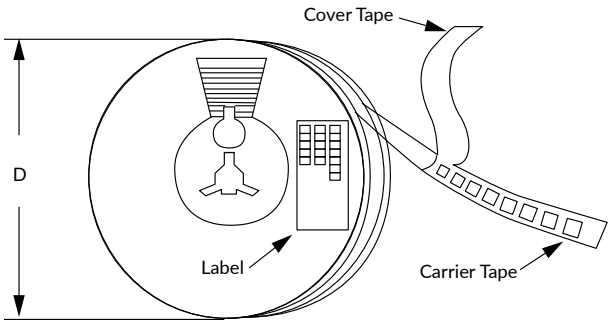
- Line 1: "Y" = YEAR (single digit). "WW" = WORK WEEK the device was assembled.
- Line 2: "XXXX" = Device PART NUMBER.

Tape and Reel Information

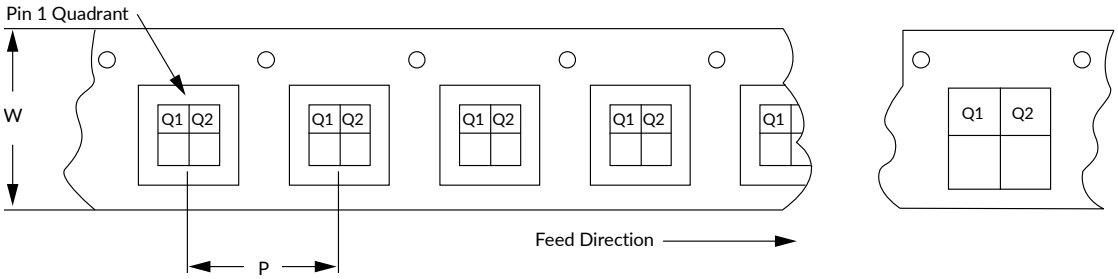
Guerrilla RF's tape and reel specification complies with Electronic Industries Alliance (EIA) standards for "Embossed Carrier Tape of Surface Mount Components for Automatic Handling" (reference EIA-481). See the following page for the Tape and Reel Specification and Device Package Information table, which includes units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape and reeled onto a plastic reel. Each reel is packaged in a cardboard box. There are product labels on the reel, the protective ESD bag and the outside surface of the box.

For the Tape and Reel Reference Table, please refer to: *https://www.guerrilla-rf.com/prodFiles/Manufacturing/MN001.pdf*



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information

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Revision History

Revision Date	Description of Change
July 9, 2018	Release Ø Data Sheet.
April 4, 2023	Upgraded Data Sheet to new format only. No change to device or device specifications.
March 21, 2024	Upgraded Data Sheet to newest format only. No change to device or device specifications.
June 10, 2024	Release A Data Sheet. Changed I_{DD} to 70 mA. Added M5 = 4 kΩ to match production test plan.
April 30, 2025	Raised maximum operating temperature from 105 to 115 °C. Extended upper frequency range from 2.7 to 4.2 GHz.
October 9, 2025	Upgraded Data Sheet to new format only. No change to device or device specifications.
December 18, 2025	Changed evaluation board to GRF400X_RevC.

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Data Sheet Classifications

Data Sheet Status	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry-supplied transistor S-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on limited evaluation board measurements taken within the Guerrilla RF Applications Lab. All parametric values are subject to change pending the collection of additional data.
Release Ø	All data based on measurements taken with *production-released* material. TYP values are based on a combination of ATE and bench-level measurements, with MIN/MAX limits defined using *modelled estimates* that account for part-to-part variations and expected process spreads. Although unlikely, future refinements to the TYP/MIN/MAX values may be in order as multiple lots are processed through the factory.
Release A-Z	All data based on measurements taken with production-released material *derived from multiple lots which have been fabricated over an extended period of time.* MIN/MAX limits may be refined over previous releases as more statistically significant data is collected to account for process spreads.

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