DATASHEET 5487, REV F

1200 VOLT, 40 AMP MOSFET FULL-BRIDGE MODULE

Features

- Isolated base plate
- Light weight low profile standard package
- Aluminum Nitride substrate
- High temperature engineering plastic shell construction
- Enhanced die coating
- Die back metal change from silver to gold



ELECTRICAL CHARACTERISTICS PER MOSFET LEG

/T 0500	LINII ECC	OTHERWISE	CDECIEIED)
CLIEZO G	UNLESS	OTHERWISE	SPECIFIED

ELECTRICAL CHARACTERIOTICS I ER MICCI ET ELC			(1)=23 C CIALLOS OTTILINAVISE SI ECILIE			
SYMBOL	PARAMETER	MIN	TYP	MAX	UNIT	
MOSFET S	SPECIFICATIONS					
BV _{DSS}	Drain to Source Breakdown Voltage I _D = 100 μA, V _{GS} = 0V	1200	-	-	٧	
I _D	Continuous Drain Current $T_C = 25^{\circ}C$ $T_C = 100^{\circ}C$	-	-	60 40	Α	
I _{D(pulse)}	Pulsed Drain Current, 1ms	-	-	160	Α	
V _G s	Gate to Source Voltage	-	-	-10/+25	V	
I _{GSS}	Gate-Source Leakage Current , V _{GS} = +20V / -5V	-	-	250	nA	
V _{GS(th)}	Gate Threshold Voltage, $T_J = 25^{\circ}\text{C}$ $I_D = 10\text{mA}, V_{DS} = V_{GS}$ $T_J = 150^{\circ}\text{C}$	2.0 1.4	3.2 2.3	4.0 3.2	V	
I _{DSS}	Zero Gate Voltage Drain Current VDS = 1200 V, VGS=0V	-	1	100	μA	
R _{DS(on)}	Drain-Source On-State Resistance $T_J = 25^{\circ}C$ $I_D = 40A, V_{GS} = 20V$ $T_J = 150^{\circ}C$		47 98	56 118	mΩ	
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Cap. VDS = 1000 V, VGS = 0 V, f = 1 MHz, VAC = 25 mV	- - -	2287 157 8	- - -	pF	
$\begin{array}{c} t_{\text{D(on)}} \\ t_{\text{R}} \\ t_{\text{D(off)}} \\ t_{\text{F}} \end{array}$	Turn On Delay Time Rise Time Turn Off Delay Time Fall Time $V_{DS} = 800 \text{ V}$, $V_{DS} = 40 \text{ A}$, $V_{GS} = -5/+20 \text{ V}$, $V_{CS} = 2.5 \Omega$, $V_{CS} = 2.5 \Omega$	- - -	15 52 26 34	- - - -	ns	
Eas	Avalanche Energy, Single Pulse I _D = 40A, V _{DS} = 50V	-	2	-	J	
E _{OFF}	Turn on Energy Loss Turn off Energy Loss V_{DS} = 800 V, I_{D} = 40A, V_{GS} = -5/+20V, R_{G} = 2.5 Ω , L = 80 μ H	-	1000 400		μJ	
R _{G(int)}	Internal Gate Resistance f = 1MHz, V _{AC} = 25mV	-	1.8	-	Ω	
Qgs Qgd Qg	Gate to Source Charge Gate to Drain Charge Total Gate Charge V _{DS} = 800 V, I _D = 40A, V _{GS} = -5/+20V	-	29 45 131	-	nC	

SENSITRON SEMICONDUCTOR

DATASHEET 5487, REV F

REVERSE DIODE CHARACTERISTICS

(T_J=25°C UNLESS OTHERWISE SPECIFIED)

SYMBOL	PARAMETER		TYP	MAX	UNIT
DIODE SPECIFICATIONS					
V _{SD}	$\begin{array}{ccc} \mbox{Diode Forward Voltage} & T_{J} = 25^{\circ}\mbox{C} \\ \mbox{V}_{GS} = -5\mbox{V}, \mbox{I}_{SD} = 20\mbox{A} & T_{J} = 150^{\circ} \end{array}$	_	4.2 3.7	4.5 4.0	V
ls	Continuous Forward Current, T _J = 25°C	-	-	60	Α
t _{rr}	Reverse Recovery Time $V_{GS} = -5V$, $I_{SD} = 40A$, $V_{R} = 800V$, $di/dt = 1406A/ \mu s$	-	63	-	ns
Qrr	Reverse Recovery Charge $V_{GS} = -5V$, $I_{SD} = 40A$, $V_{R} = 800V$, $di/dt = 1406A/ \mu s$	-	964	-	nC
Irrm	Peak Reverse Recovery Current $V_{GS} = -5V$, $I_{SD} = 40A$, $V_{R} = 800V$, $di/dt = 1406A/ \mu s$	-	18	-	Α

ZVS SIC DIODE CHARACTERISTICS

(T_J=25°C UNLESS OTHERWISE SPECIFIED)

240 010 DIODE ONANAOTENIO 1100		(TJ=25 C ONLESS OTTLEN VISE STECHTED)					
SYMBOL	PARAMETER		MIN	TYP	MAX	UNIT	
DIODE SPECIFICATIONS							
V _{RRM}	Repetitive Peak Reverse Voltage		1200	-	-	V	
V _{RSM}	Surge Peak Reverse Voltage		1300	-	-	V	
V _R	DC Peak Blocking Voltage		1200	-	-	V	
I _F	Continuous Forward Current,	T _J = 150°C	-	-	5	Α	
I _{FRM}	Repetitive Peak Forward Surge Current tp= 10ms, Half Sine Pulse	T _C = 25°C T _C = 110°C	-	-	26 18	А	
I _{FSM}	Non-Repetitive Forward Surge Current tp= 10ms, Half Sine Pulse	$T_C = 25$ °C $T_C = 110$ °C	-	-	46 36	Α	
VF	Forward Voltage I _F = 5A	T _J = 25°C T _J = 150°C	-	1.4 1.9	1.8 3.0	V	
I _R	Reverse Current V _R = 1200V	T _J = 25°C T _J = 150°C	-	20 40	150 300	μΑ	
Qc	Total Capacitive Charge $V_{R=}$ 800V, I_F = 5A, di/dt = 200A/ μ s, T_J = 25 $^{\circ}$ C		-	27	-	nC	
С	Total Capacitance $V_R = 0V$, $T_J = 25$ °C, $f = 1MHz$ $V_R = 400V$, $T_J = 25$ °C, $f = 1MHz$ $V_R = 800V$, $T_J = 25$ °C, $f = 1MHz$		-	390 27 20	-	pF	

Note: Production units are only tested at room temperature. Low/High temperature operation is guaranteed by design.

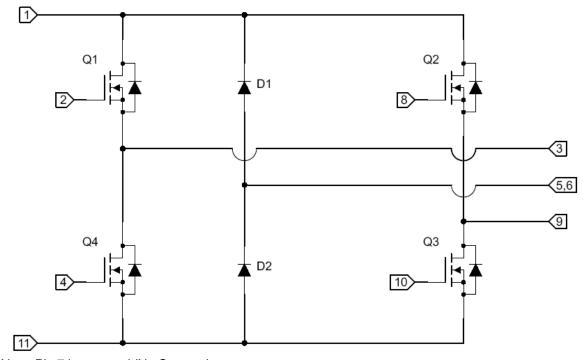
DATASHEET 5487, REV F

THERMAL AND MECHANICAL CHARACTERISTICS

SYMBOL	PARAMETER	MIN	TYP	MAX	UNIT
R _{θЈВ_М}	MOSFET Junction-to-Base Plate Thermal Resistance Per Leg	-	0.30	0.36	°C/W
$R_{\theta JB_D}$	Diode Junction-to-Base Plate Thermal Resistance Per Leg	-	1.60	1.76	°C/W
V _{iso}	Isolation to Base Plate	-	-	2500	VDC
TJ	Operating Junction Temperature	-55	-	150	°C
T _{STG}	Storage Temperature	-55	-	150	°C
	Mounting Torque for Module Mounting	3	-	4	in-lbs.
	Weight	-	10	-	g

Recommended TIM = Laird Tgon 805

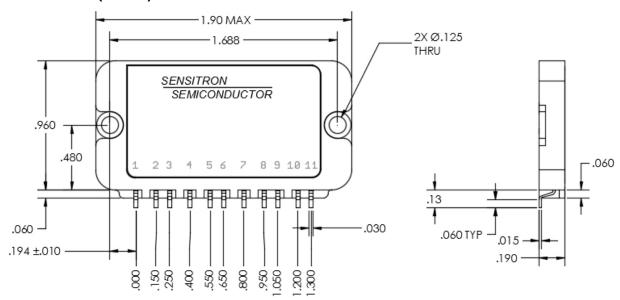
Schematic Diagram:



Note: Pin 7 is not used (No Connect)

DATASHEET 5487, REV F

Mechanical Outline (inches):



TOLERANCE UNLESS OTHERWISE NOTED:

 $.XX = \pm .010$

 $.XXX = \pm .005$

DISCLAIMER:

- 1- The information given herein, including the specifications and dimensions, is subject to change without prior notice to improve product characteristics. Before ordering, purchasers are advised to contact the Sensitron Semiconductor sales department for the latest version of the datasheet(s).
- 2- In cases where extremely high reliability is required (such as use in nuclear power control, aerospace and aviation, traffic equipment, medical equipment, and safety equipment), safety should be ensured by using semiconductor devices that feature assured safety or by means of users' fail-safe precautions or other arrangement.
- 3- In no event shall Sensitron Semiconductor be liable for any damages that may result from an acc**ident or any other cause during o**peration of the user's units according to the datasheet(s). Sensitron Semiconductor assumes no responsibility for any intellectual property claims or any other problems that may result from applications of information, products or circuits described in the datasheets.
- 4- In no event shall Sensitron Semiconductor be liable for any failure in a semiconductor device or any secondary damage resulting from use at a value exceeding the absolute maximum rating.
- 5- No license is granted by the datasheet(s) under any patents or other rights of any third party or Sensitron Semiconductor.
- 6- The datasheet(s) may not be reproduced or duplicated, in any form, in whole or part, without the expressed written permission of Sensitron Semiconductor.
- 7- The products (technologies) described in the datasheet(s) are not to be provided to any party whose purpose in their application will hinder maintenance of international peace and safety nor are they to be applied to that purpose by their direct purchasers or any third party. When exporting these products (technologies), the necessary procedures are to be taken in accordance with related laws and regulations.