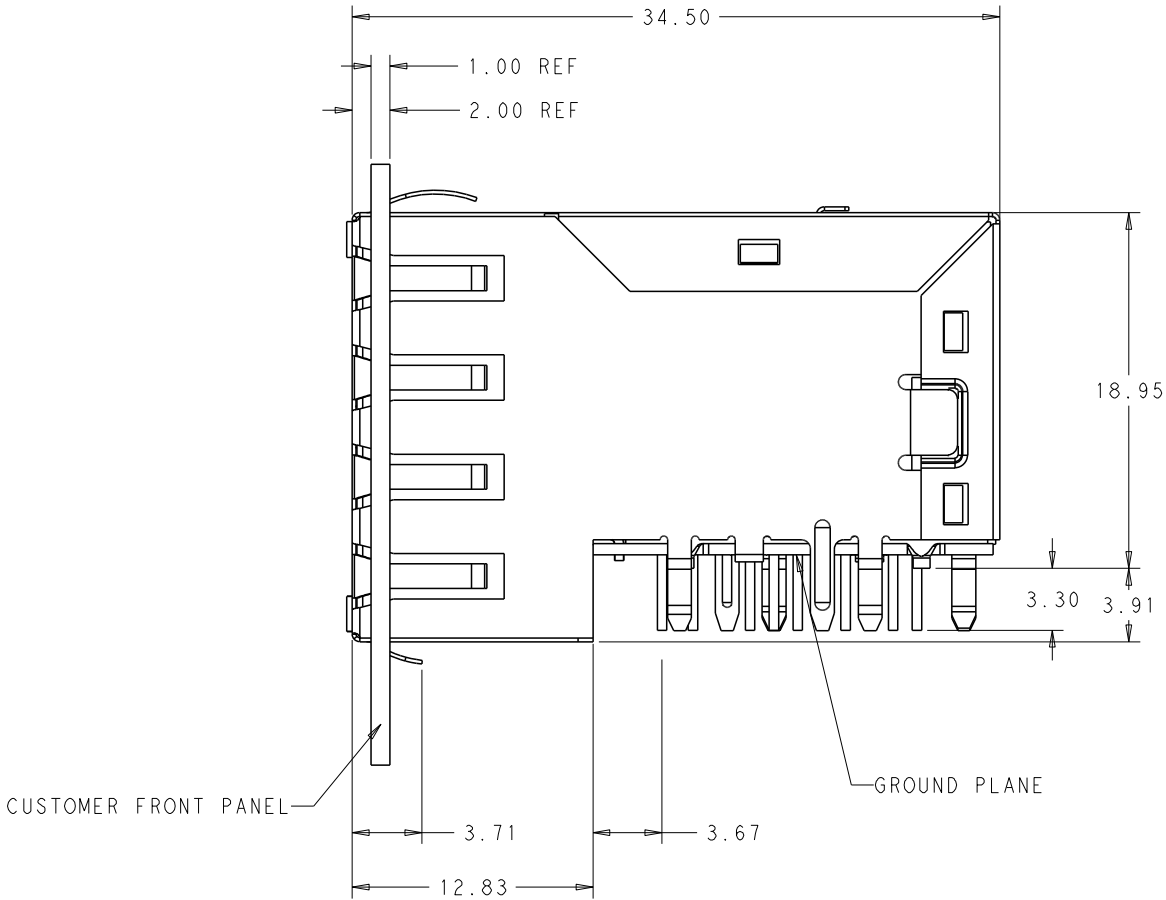
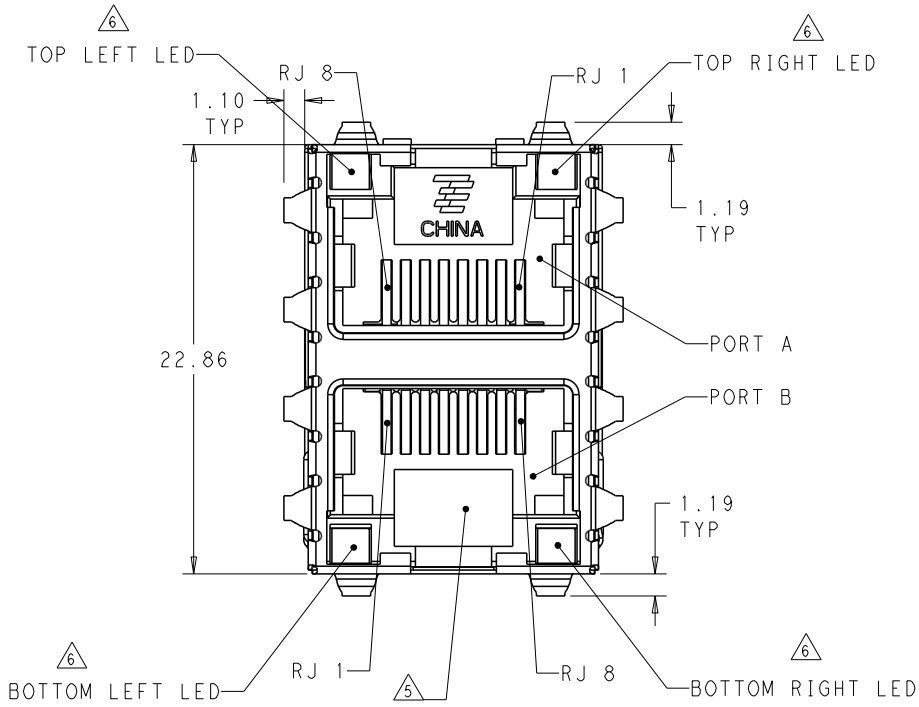
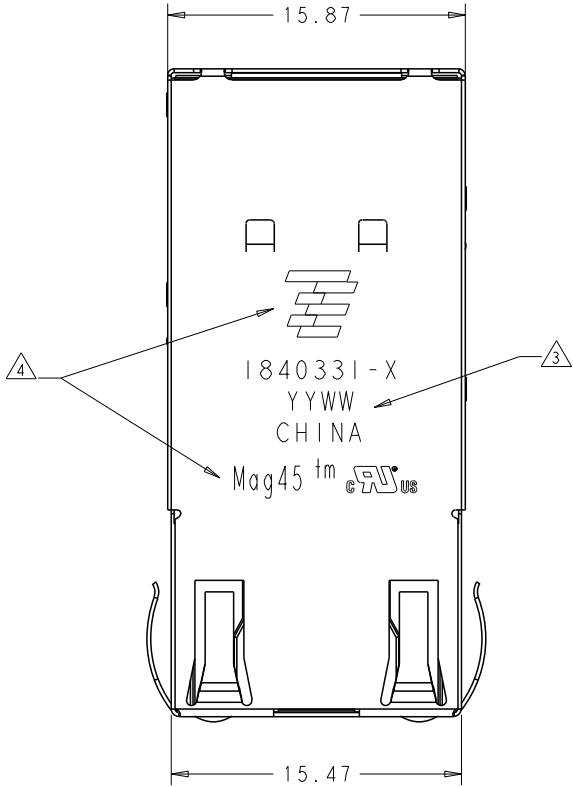


LOC		DIST		REVISIONS			
P	LYR	DESCRIPTION		DATE	DWN	APVD	
-	-	SEE SHEET 1		-	-	-	-



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DIMENSIONS: mm				TOLERANCES UNLESS OTHERWISE SPECIFIED:				NAME MAG45(TM) MODULE JACK, 2X1 OFFSET S8G31 GIGABIT CIRCUIT, WITH LED 10 PIN VERTICAL			
0 PLC ±				1 PLC ±				PRODUCT SPEC			
1 PLC ±				2 PLC ±				108-104004			
2 PLC ±				3 PLC ±0.25				APPLICATION SPEC			
3 PLC ±				4 PLC ±				SIZE CAGE CODE DRAWING NO			
4 PLC ±				ANGLES ±				A1 100779 C=1840350			
MATERIAL				FINISH				RESTRICTED TO			
				WEIGHT				SCALE 5:1 SHEET 2 OF 4 REV B			
				CUSTOMER DRAWING							

LOC		DIST		REVISIONS			
P	LYR	DESCRIPTION		DATE	DWN	APVD	
-	-	SEE SHEET 1		-	-	-	-

D

C

B

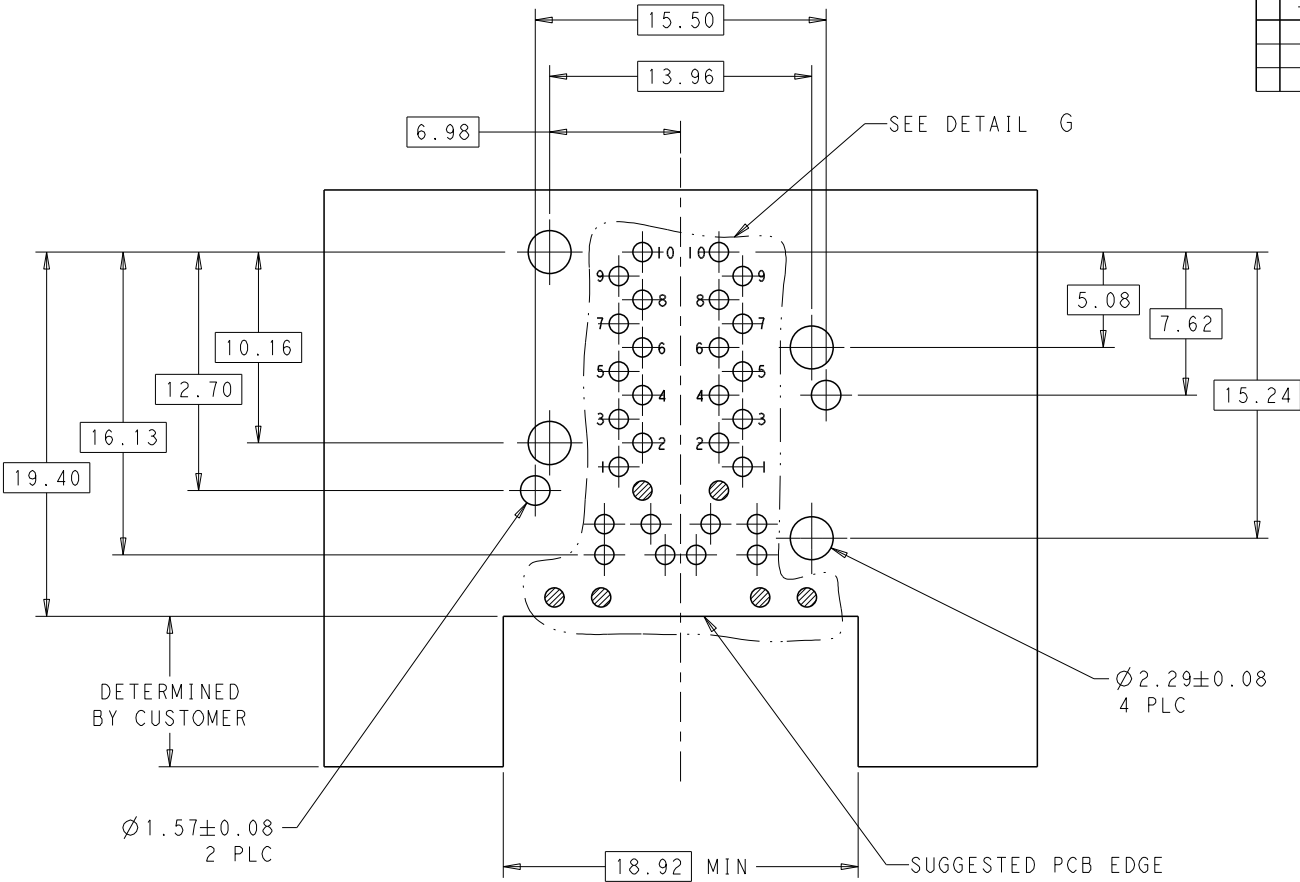
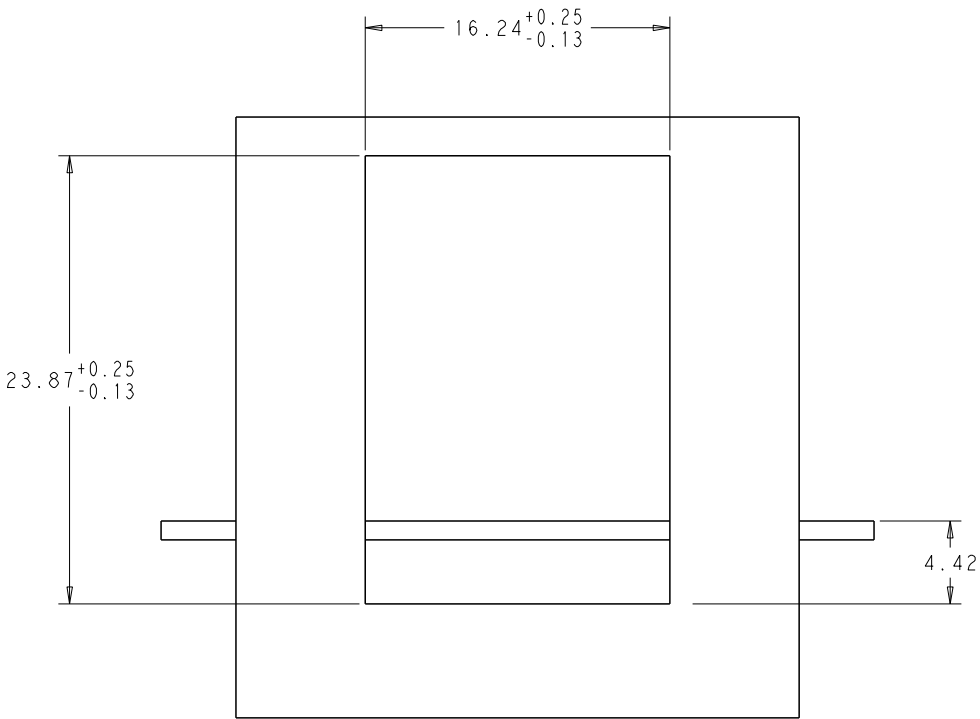
A

D

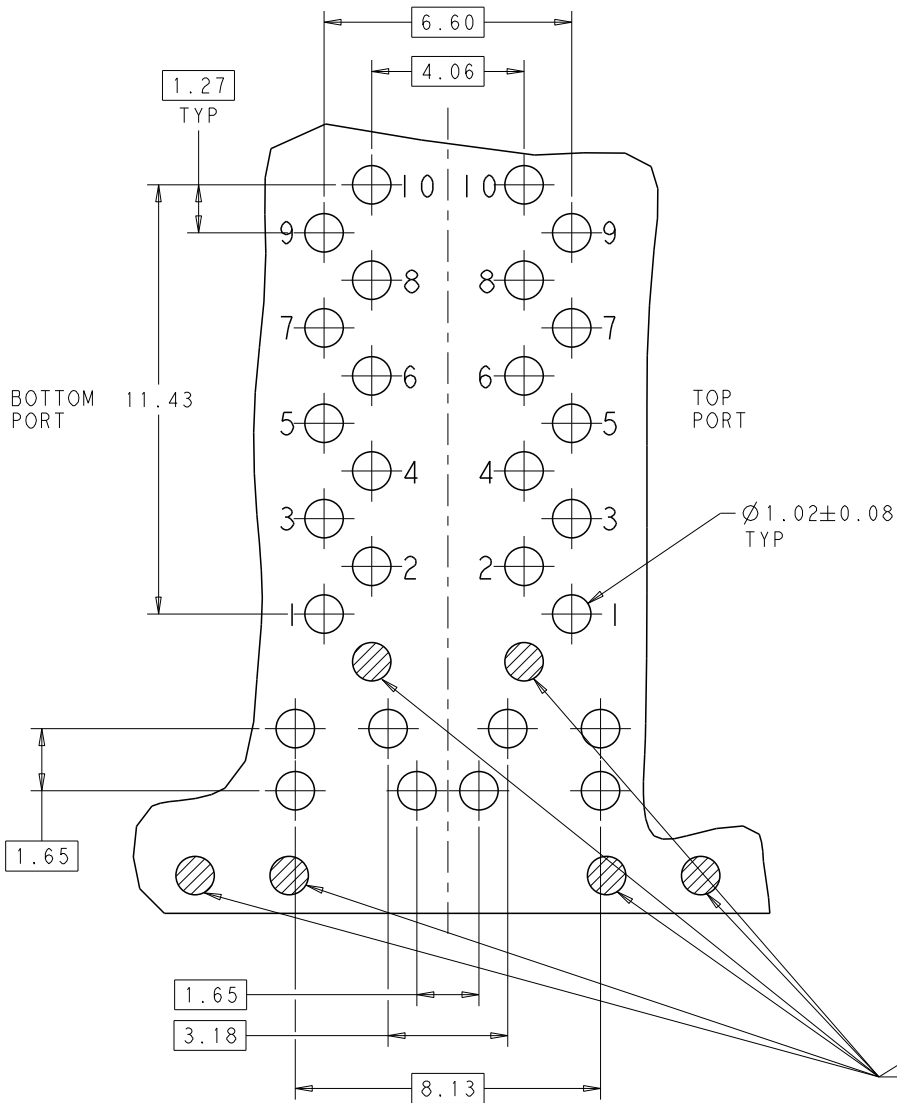
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B

A

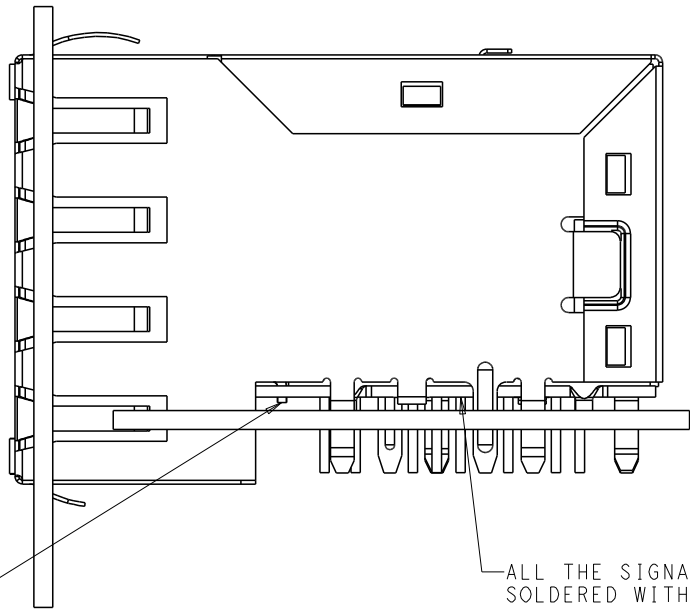


RECOMMENDED PC BOARD LAYOUT
VIEWED FROM COMPONENT SIDE



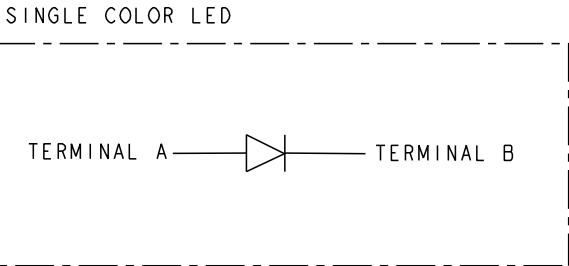
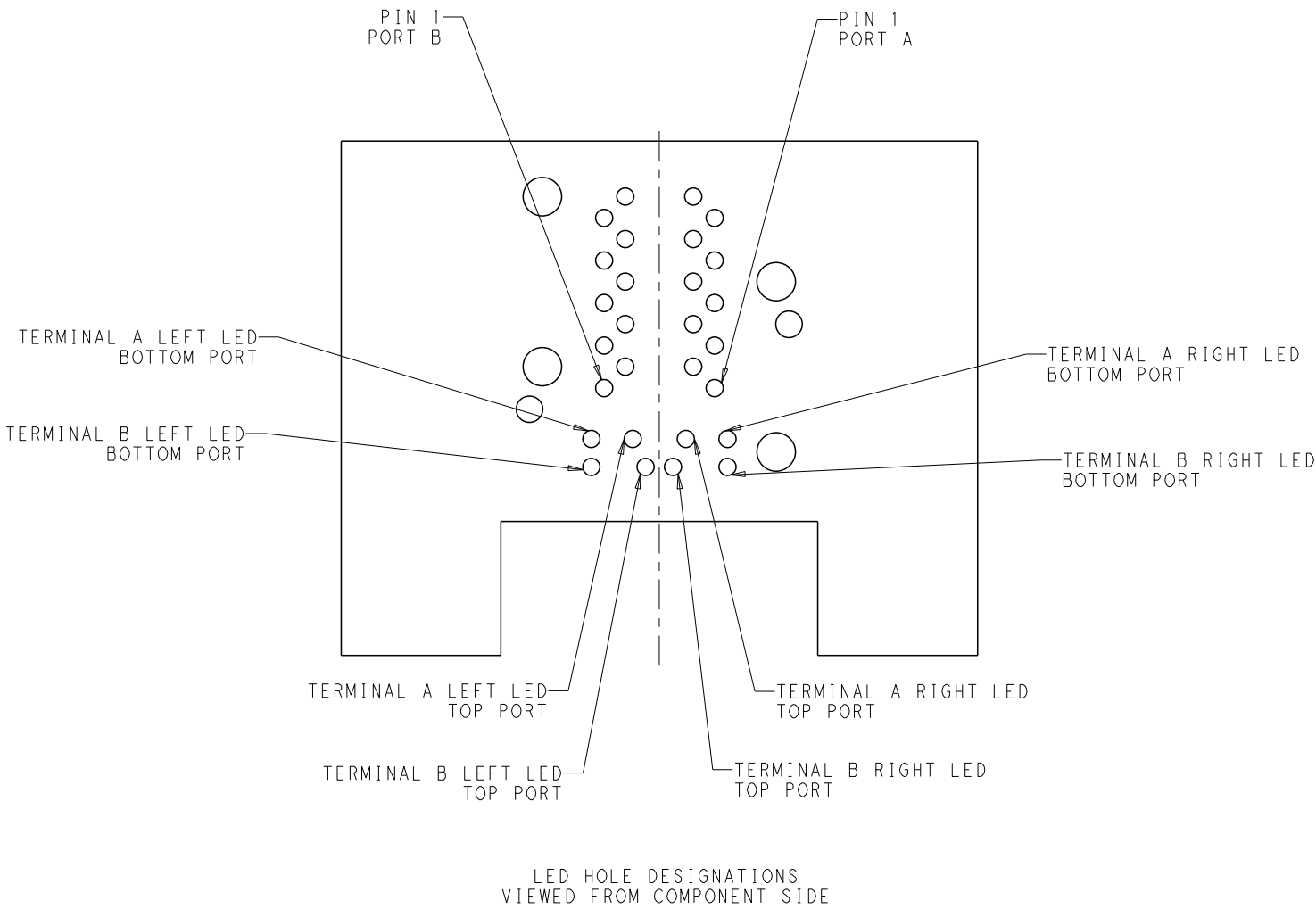
DETAIL G
SCALE 10:1

PIN 0#: BOTTOM LED'S PIN ON MODULAR JACK AND BOTTOM PCB ARE STUB PINS WITH LENGTH SIMILAR TO PCB STANDOFF HEIGHT. PRECAUTIONS SHOULD BE TAKEN IN PCB DESIGN TO GUARD AGAINST SHORTING TRACES TO STUB PINS.



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DIMENSIONS: mm		CHK SEAN HE 27MAY2009	NAME MAG45(TM) MODULE JACK, 2X1 OFFSET S8G31 GIGABIT CIRCUIT, WITH LED 10 PIN VERTICAL	
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± 1 PLC ± 2 PLC ± 3 PLC ±0.25 4 PLC ± ANGLES ±		APVD LIJUN RAO 27MAY2009	PRODUCT SPEC 108-104004 APPLICATION SPEC	
MATERIAL		FINISH	WEIGHT	RESTRICTED TO
CUSTOMER DRAWING		SCALE 5:1	SHEET 3 OF 4	REV B

LOC	DIST	REVISIONS					
		P	LTR	DESCRIPTION	DATE	DWN	APVD
-	-	-	-	SEE SHEET 1	-	-	-



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DIMENSIONS: mm		NAME LI JIN RAO	NAME MAG45(TM) MODULE JACK, 2X1 OFFSET S8G31 GIGABIT CIRCUIT, WITH LED 10 PIN VERTICAL	
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± 1 PLC ± 2 PLC ± 3 PLC ±0.25 4 PLC ± ANGLES ±		PRODUCT SPEC 108-104004	SIZE A1	
MATERIAL		APPLICATION SPEC	CAGE CODE 100779	
FINISH		WEIGHT	DRAWING NO C=1840350	
CUSTOMER DRAWING		SCALE 5:1	SHEET 4 OF 4	
			REV B	